

Hw4 for CS2305

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Q: You are given a non-pipelined processor design which has a cycle time of 10ns and average CPI of 1.4. Which one is NOT correct in the following statements?

- A. If the pipeline stalls 20% of the time for 1 cycle and 5% of the time for 2 cycles, the new CPI is 1.8
- B. The best speed up we could get by pipelining it into 5 stages is 5X
- C. If the 5 stages are 1ns, 1.5ns, 4ns, 3ns, and 0.5ns, the best speedup is 2.5X

reference

- CS152/CS253A,22spring, EECS Berkeley
- CS252, 1999fall, EECS Berkeley

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