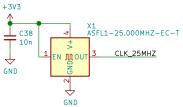
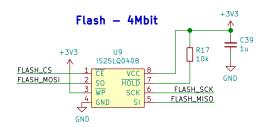
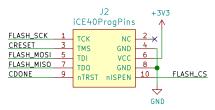


# 25 MHz clock

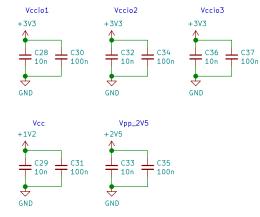




### Programming header







## **FPGA**

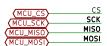
Sheet: /FPGA/ File: FPGA.sch

 Title: Arbitrary function generator

 Size: A4
 Date: 11.07.2019
 Rev: 1.0

 KiCad E.D.A. kicad (5.1.2)-1
 Id: 3/5

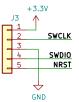
#### Interface with FPGA



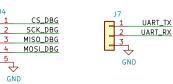
#### Interface with PC



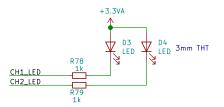
### **Programming** header



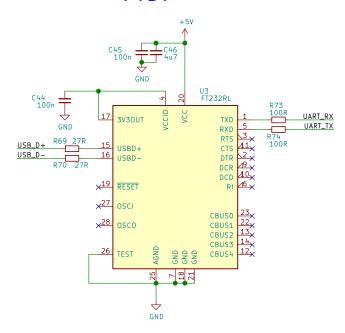
### Debug headers (SPI & UART)



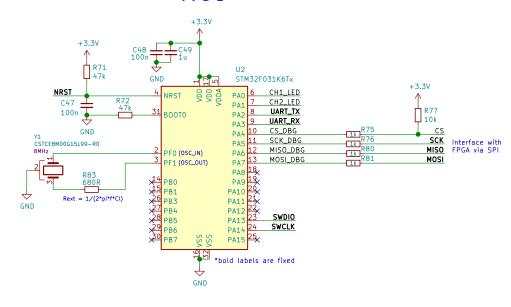
#### **Channel Status LEDs**



## **FTDI**



# MCU



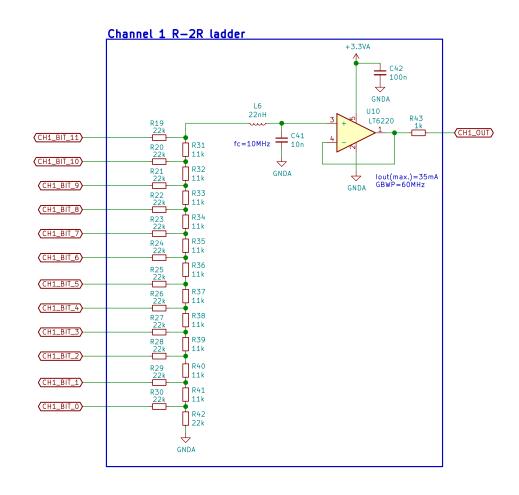
# MCU & PC interface

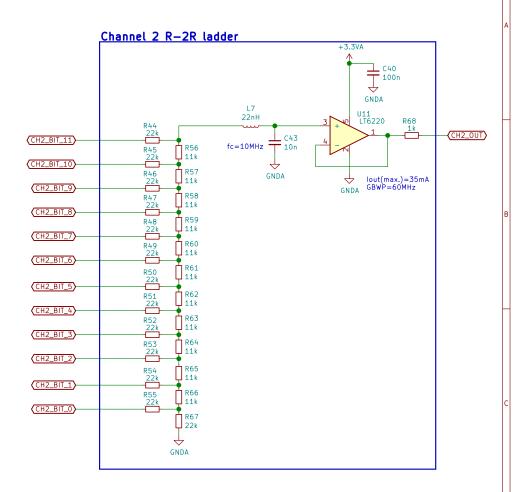
KiCad E.D.A. kicad (5.1.2)-1

Sheet: /MCU/ File: MCU.sch Title: Arbitrary function generator Date: **11.07.2019** Size: A4 Rev: 1.0

Id: 4/5

## 12-bit DAC







Sheet: /DAC/ File: DAC.sch

Title: Arbitrary function generator

 Size: A4
 Date: 11.07.2019
 Rev: 1.0

 KiCad E.D.A. kicad (5.1.2)-1
 Id: 5/5