

## **Zisis M. Tsioumaras**

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GitHub: <https://github.com/ZiTsi>

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### **Education**

#### **École Polytechnique Fédérale de Lausanne**

**Online Course (Coursera.org)** : Dec 2012

Functional Programming Principles in Scala

#### **University of Athens, Department of Informatics and Telecommunications**

**MSc in Advanced Information Systems:** *October 2010 – April 2014*

Thesis: Integration of WindNinja, an open source software for modeling and simulating spatially varying wind fields based on the area topography, with FLogA, a tool for realistic simulation to forecast and visualize wildfire spread, developed at the University of Athens, department of Informatics and Telecommunications. The programming languages used for the thesis was PHP for the web Interface and C++ for the integration of the two systems.

*Overall GPA: 8 / 10*

#### **University of Athens, Department of Informatics and Telecommunications**

**Bachelor:** 2004 – 2008, GPA: 8.12 / 10

Dissertation: Design and implementation of an Embedded System on Chip for supporting signal and image processing operations on an FPGA. The hardware description language used was VHDL and the implementation was based on a Xilinx Spartan-3 FPGA, residing on the Spartan-3 development Board by Digilent Inc. For data transmission between PC and board's SRAM, a library with MATLAB functions was developed. Finally UML sequence diagrams and flowcharts were used to specify and describe the system.

*Overall GPA: 8.12 / 10 - Graduation rank: 5<sup>th</sup> / 48.*

**20<sup>th</sup> High School of Athens**, Grade: 18.7 / 20

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## Work Experience

**Helintec S.A. (helintec.com), Jun 2010 – now**

**Software Engineer**

I am currently working as a .Net developer creating rich client application based on Windows for the naval industry. I am involved in the whole process of designing, implementing and supporting enterprise maritime software.

**Army General Staff**, military service (Nov 2009 – Feb 2010)

**Computerization Assistant** in Career Military Personnel Division

**OTE A.E.**, Feb 2008 – Feb 2009

**International Operator** in Manually-operated Calls department

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## Projects

- Design and implementation of an Embedded System on Chip for supporting signal and image processing operations on an FPGA, using **VHDL** and **MATLAB**
  - Design, synthesis and implementation of a MIPS processor with a particular instructions subset with the use of Xilinx ISE WebPACK, **VHDL** και **Assembly**
  - Development of a multithreaded IRC server with programming language **C**
  - Design and implementation of a Database System with programming language **C**
  - Interactive animation using library **GLUT (Open GL)** and programming language **C**
  - Design and implementation of an exams taking and assessing webpage using **HTML**, **ASP.NET**, **C#** and **SQL Server**
  - Implementation of a simple multi threaded RDBMS using programming language **Java**.
  - Research on ways to estimate and predict hull and propeller efficiency based on daily performance reports in vessels using Data Mining Techniques and Statistical Methods (main application used: **RapidMiner**).
  - Designing and implementation of a system for communication between a vessel and its operating site for exchanging daily reports using **MS SQL**, **XML** and **VB.Net**.
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## General Interests

Photography, Video

Travelling, Bicycle, Long-distance running

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