Zisis M. Tsioumaras

Kourmouli 23, 11145
Athens, Greece

Date of Birth*: August 18, 1986*

Telephone*: +30 6933 027487*
Email*: z.tsioumaras@gmail.com*
LinkedIn: *www.linkedin.com/in/zisis-tsioumaras*
GitHub: *https://github.com/ZiTsi*

Work Experience

Helintec S.A. (helintec.com), Jun 2010 – now

Software Engineer

I am currently working as a .Net developer creating rich client applications based on Windows for the naval industry. I am involved in the whole process of designing, implementing and supporting enterprise maritime software. The main technologies I am working with are **.Net framework 3.5 and above**, **Visual Studio 2008 and 2015**, **SQL Server 2008 RC2 and above**.

The main projects I have worked on are:

- **Migration from VB6 to VB.Net** of ANKO, a windows form application.
    - Code refactoring of the core libraries to enable testability
    - Migration from VB6 to VB.Net
    - Implement custom test suite to ensure proper migration
    - Implement a significant part of the UI
    - Technologies Used: Windows Forms Application, .Net 3.5, Visual Studio 2008, SourceSafe

- **Specify, design and implement** SEMT, an application used for the daily reporting of vessels and offline communication to their managing offices
    - Gather the functional and non-functional specification of the product
    - Participate in the design decision for the implementation
    - Design the data schema and implement the data persistence functionality
    - Design and implement the UI
    - Design and participate in the implementation of the customer specific libraries and reports
    - Technologies Used: Windows Forms Application, .Net 3.5 / 4.6, Visual Studio 2008/2015, Git, SQL Server 2008 R2 / 2012.

- **Participate in the implementation of Fleet Manager**, an application used for vessel data gathering, performance analysis and emission reporting.
    - Implement the data import from SEMT to Fleet Manager
    - Participate in the design decision for the reporting subsystem
    - Implement the graph's engine
    - Implement part of the UI
    - Participate in data analysis to create global emission report, based on the fleet size of our customers.
    - Technologies Used: Windows Forms Application, .Net 3.5 / 4.6, Visual Studio 2008/2015, Git, SQL Server 2008 R2 / 2012.

ParkAround (parkaround.gr), Dec 2015 – May 2016

Web developer

I implemented part of the front end of the web application of ParkAround (a startup located in Athens dealing with parking slots) using ASP.NET MVC, Knockout.JS, Bootstrap and HTML.

Army General Staff, military service (Nov 2009 – Feb 2010)

Computerization Assistant in Career Military Personnel Division

---

Other Projects

- Design and implementation of an Embedded System on Chip for supporting signal and image processing operations on an FPGA, using **VHDL** and **MATLAB**
- Design, synthesis and implementation of a MIPS processor with a particular instructions subset with the use of Xilinx ISE WebPACK, **VHDL** και **Assembly**
- Research on ways to estimate and predict hull and propeller efficiency based on daily performance reports in vessels using **Data Mining Techniques** and **Statistical Methods** (main application used: **RapidMiner**).

Education

École Polytechnique Fédérale de Lausanne

Online Course (Coursera.org): Dec 2012

Functional Programming Principles in Scala

University of Athens, Department of Informatics and Telecommunications

MSc in Advanced Information Systems: *October 2010 – April 2014*

Thesis: Integration of WindNinja, an open source software for modeling and simulating spatially varying wind fields based on the area topography, with FLogA, a tool for realistic simulation to forecast and visualize wildfire spread, developed at the University of Athens, department of Informatics and Telecommunications. The programming languages used for the thesis was PHP for the web Interface and C++ for the integration of the two systems.

Overall GPA: 8 / 10

University of Athens, Department of Informatics and Telecommunications

Bachelor: 2004 – 2008, GPA: 8.12 / 10

Dissertation: Design and implementation of an Embedded System on Chip for supporting signal and image processing operations on an FPGA. The hardware description language used was VHDL and the implementation was based on a Xilinx Spartan-3 FPGA, residing on the Spartan-3 development Board by Digilent Inc. For data transmission between PC and board's SRAM, a library with MATLAB functions was developed. Finally, UML sequence diagrams and flowcharts were used to specify and describe the system.

Overall GPA: 8.12 / 10 - Graduation rank: 5th / 48.

20th High School of Athens, Grade: 18.7 / 20

General Interests

Photography, Video

Travelling, Bicycle, Long-distance running