(a) Circuit of Toffoli gate

(b) Matrix of Toffoli gate
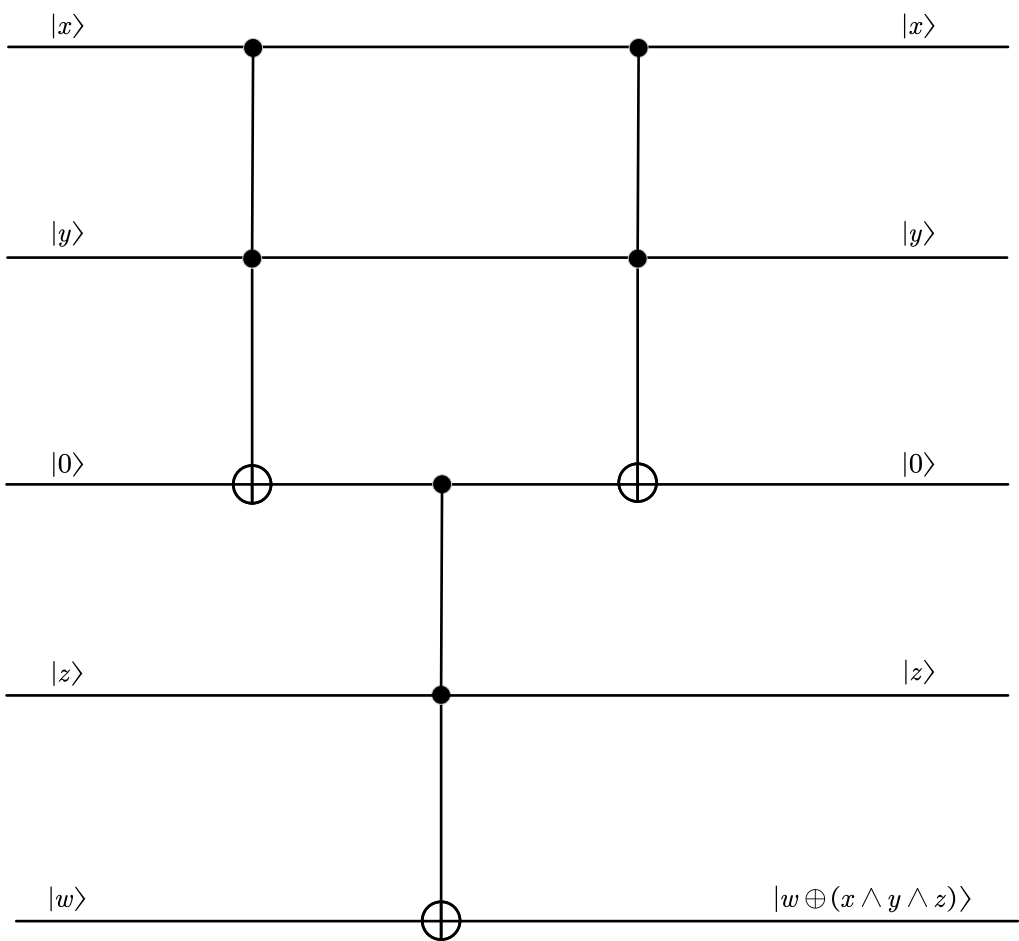
(c) Combination circuit with three controlling bits