

Imm[11:0]	rs 1	rd	funct3	opcode
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Offset[11:0] [ignored] base dest width H-LOAD

Imm[11:5]	rs2	rs 1	funct3	Imm[4:0]	opcode
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Offset[11:5] src [ignored] base width Offset[4:0] H-STORE