

Chip Name: Add16
Inputs: $a[16]$, $b[16]$
Outputs: $out[16]$
Function: $a + b$

Comment: Integer 2's complement addition
Overflow is neither detected nor handled

$$\begin{array}{r} \dots 1011a \\ \dots 0010b + \\ \hline \dots 1101 \end{array}$$

