Wand 2 Et 18 project 1 using Nand gates, implement the following w/ only primitive & composite gates created

Nant if a=b=1. Hen out=0 else out=1

Not if in=0 then out=1 else out=0

Yand if a=b=1 then out=1 else out=0

Or if a=b=0 then out=0 else out=1

IXor if a=b then out=1 else out=0

Mux if sel=0 then out=a else out=b

Mux if sel=0 then (a=in,b=0) else (a=0,b=in, Not16 for i=0...15 out[i]=Not(in(i))

Vind 16 for i=0...15 out[i]= And (a(i),b(i))

Vorló for i=0...15 out[i]=Or(a(i),b(i))

Vorló for i=0...15 out[i]=or(a(i),b(i))

Muxlb if sel=0 then for i=0...15 out(i)=a(i)

else for i=0...15 out(i)=b(i)

Or 8 way out=0r(in[o],in[1],...,in[7])

Muxlwayló see book!

Mux 8 word see book!