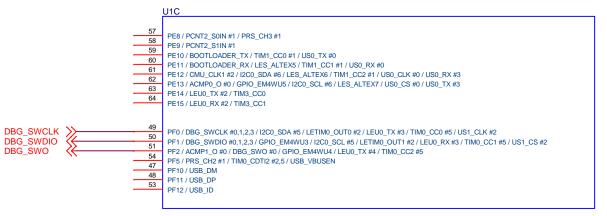
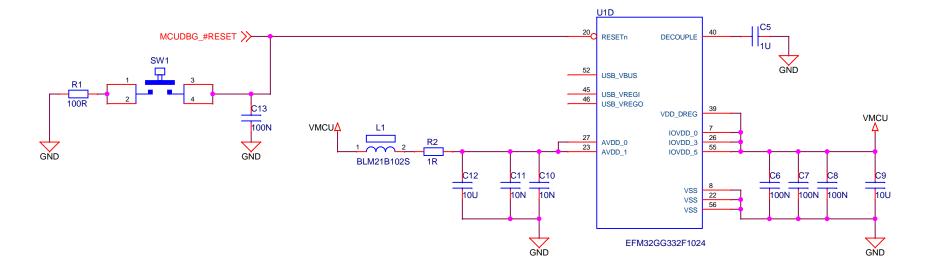
1 PA0 / GPIO_EM4WU0 / I2CO_SDA #0 / LEU0_RX #4 / PRS_CH0 / TIN
2 PA1 / CMU_CLK1 #0 / I2CO_SCL #0 / PRS_CH1 / TIMO_CC1 #0,1
4 PA2 / CMU_CLK0 #0 / ETM_TD0 #3 / TIMO_CC2 #0,1
5 PA4 / ETM_TD1 #3 / LES_ALTEX2 / TIMO_CDTI0 #0
PA4 / ETM_TD2 #3 / LES_ALTEX3 / TIMO_CDTI1 #0
PA5 / ETM_TD3 #3 / LES_ALTEX4 / LEU1_TX #1 / TIMO_CDTI2 #0
PA9 / TIM2_CC0 #0
PA1 / TIM2_CC1 #0
PA1 / TIM2_CC2 #0 PA0 / GPIO_EM4WU0 / I2C0_SDA #0 / LEU0_RX #4 / PRS_CH0 / TIM0_CC0 #0,1,4 LFXTAL_P KENTAL_N PB7 / LFXTAL_P / TIM1_CC0 #3 / US0_TX #4 / US1_CLK #0 PB8 / LFXTAL_N / TIM1_CC1 #3 / US0_RX #4 / US1_CS #0
PB11 / DAC0_OUT0/OPAMP_OUT0 / I2C1_SDA #1 / LETIM0_OUT0 #1 / TIM1_CC2 #3 HEXTAL P PB13 / HFXTAL_P / LEU0_TX #1 / US0_CLK #4,5 HEXTAL_N X PB14 / HFXTAL N / LEU0 RX #1 / US0 CS #4,5 EFM32GG332F1024

U1B PC0 / ACMP0_CH0 / DAC0_OUT0ALT/OPAMP_OUT0ALT #0 / I2C0_SDA #4 / LES_CH0 / PCNT0_S0IN #2 / PRS_CH2 #0 / TIM0_CC1 #4 / US0_TX #5 / US1_TX #0 PC0 / ACMP0_CH0 / DAC0_OUT0ALT/OPAMP_OUT0ALT #U / IZCU_SDA #4 / LES_CHU / PCNTU_SUIN #2 / FRS_CH3 #O / TIM0_CC1 #4 / US0_RX #5 / US1_RX #0 PC1 / ACMP0_CH1 / DAC0_OUT0ALT/OPAMP_OUT0ALT #1 / IZCO_SCL #4 / LES_CH1 / PCNT0_S1IN #2 / PRS_CH3 #O / TIM0_CC2 #4 / US0_RX #5 / US1_RX #0 PC2 / ACMP0_CH2 / DAC0_OUT0ALT/OPAMP_OUT0ALT #2 / LES_CH2 / TIM0_CDT10 #4 / US2_TX 11 PC2 / ACMP0_CH2 / DAC0_OUTOALT/OPAMP_OUTOALT #2 / LES_CH2 / TIM0_CDTI0 #4 / US2_TX 12 PC3 / ACMP0_CH3 / DAC0_OUTOALT/OPAMP_OUTOALT #3 / LES_CH3 / TIM0_CDTI1 #4 / US2_RX 13 PC4 / ACMP0_CH4 / DAC0_P0/OPAMP_P0 / I2C1_SDA #0 / LES_CH4 / LETIM0_OUTO #3 / PCNT1_S0IN / TIM0_CDTI2 #4 / US2_CLX 14 PC5 / ACMP0_CH5 / DAC0_N0/OPAMP_N0 / I2C1_SCL / LES_CH5 / LETIM0_OUTO #3 / PCNT1_S1IN / US2_CS 15 PC6 / ACMP0_CH6 / ETM_TCLK #2 / I2C0_SDA #2 / LES_CH6 / LEU1_TX #0 16 PC7 / ACMP0_CH7 / ETM_TD0 #2 / I2C0_SCL #2 / LES_CH7 / LEU1_RX 17 PC7 / ACMP0_CH7 / ETM_TD0 #2 / I2C0_SCL #2 / LES_CH7 / LEU1_RX 18 PC7 / ACMP0_CH7 / ETM_TD0 #2 / I2C0_SCL #2 / LES_CH7 / LEU1_RX 41 PC7 / ACMP1_CH0 / LS. CH8 / TIM2_CC0 #2 / US0_CS #2 PC9 / ACMP1_CH1 / GPIO_EMAWU2 / LES_CH9 / TIM2_CC1 #2 / US0_CLK #2 PC9 / ACMP1_CH1 / GPIO_EMAWU2 / LES_CH9 / TIM2_CC1 #2 / US0_CLK #2 43 PC10 / ACMP1_CH1 / GPIO_EM4VVU2 / LES_UTS/ | INITIA_CG1 # 44 PC11 / ACMP1_CH2 / LES_CH10 / TIM2_CG2 #2 / US0_RX #2 PC11 / ACMP1_CH3 / LES_CH11 / US0_TX #2 PD0 / ADC0_CH0 / DAC0_OUT0ALT/OPAMP_OUT0ALT #4 / DAC0_OUT2/OPAMP_OUT2 #1 / PCNT2_S0IN #0 / US1_TX #1 29 PD0 / ADC0_CH0 / DAC0_OUT0ALT/OPAMP_OUT0ALT #4 / DAC0_OUT2/OPAMP_OUT2/OPAMP_OUT2/OPAMP_OUT3/US1_RX #1 PD2 / ADC0_CH1 / DAC0_OUT1ALT/OPAMP_OUT1ALT #4 / DBG_SWO #2 / PCNT2_STIN #0 / TIM0_CC0 #3 / US1_RX #1 PD2 / ADC0_CH2 / DBG_SWO #3 / TIM0_CC1 #3 / US1_CLK #1 / USB_DMPU 31 PD2 / ADC0_CH2 / DBG_SWO #3 / TIM0_CC1 #3 / US1_CLK #1 / USB_DMPU 32 PD3 / ADC0_CH3 / DAC0_N2/OPAMP_N2 / ETM_TD1 #0,2 / TIM0_CC2 #3 / US1_CS #1 33 PD4 / ADC0_CH4 / DAC0_P3/OPAMP_N2 / ETM_TD1 #0,2 / TIM0_CC2 #3 / US1_CS #1 PD4 / ADC0 CH4 / DAC0 P2/OPAMP P2 / ETM_TD2 #0,2 / LEU0_TX #0 33 PD4 / ADCU_UH4 / DACU_PZUFANIF_F2 / LIM_102 #0/ETM_TD3 #0,2 / LEU0_RX #0 PD5 / ADC0_CH5 / DAC0_OUT2/OPAMP_OUT2 #0 / ETM_TD3 #0,2 / LEU0_RX #0 PD5 / ADC0_CH5 / DAC0_OUT2/OPAMP_OUT2 #0 / ETM_TD3 #0,2 / LEU0_RX #0 PD6 / ADC0_CH5 / DAC0_OUT2/OPAMP_OUT2 #0 / LEU0_RX #0 PD7 / ADC0_CH5 / DAC0_OUT2/OPAMP_OUT2 #0 / LEU0_RX #0 PD7 / ADC0_CH5 / DAC0_OUT2/OPAMP_OUT2 #0 / LEU0_RX #0 34 PD6 / ADCO_CH6 / DACO_OUT 2/OFAMP_OUT 2 #J / ETIM_T IDS #J / ETIM_T IDS #J / ELOU_CA #J PD6 / ACMPO_O #2 / ADCO_CH6 / DACO_PT/OPAMP_PT / ETM_TD0 #J / I2CO_SDA #T / LES_ALTEXO / LETIMO_OUT0 #J / PCNTO_SOIN #3 / TIM1_CC0 #4 / US1_RX #2 PD7 / ACMP1_O #2 / ADCO_CH7 / CMU_CLK0 #2 / DACO_NT/OPAMP_NT / ETM_TCLK #J / I2CO_SCL #T / LES_ALTEXT / LETIMO_OUT1 #J / PCNTO_STIN #3 / TIM1_CC1 #4 / US1_TX #2 36 PD8 / BU_VIN / CMU_CLK1 #1

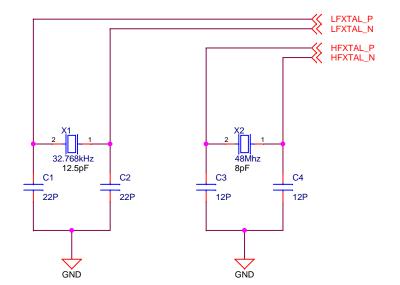
FFM32GG332F1024



EFM32GG332F1024



High/low frequency clock Refer to app note AN0016



Debug interface

