■ Moore, Gordon Earle

Cramming more components onto integrated circuits
Semiconductor, Fairchild
1965

■ Sugarman, Robert

<u>Does the country need α good \$20 microprocessor?</u>
1975

■ Moore, Gordon Earle

Progress in digital integrated electronics

Intel

1975

■ Waite, Mitchell

Computer Graphics Primer

Howard W. Sams \& Co., Inc.

1979

■ Veen, Arthur H.

<u>Dataflow machine architecture</u>

Surveys, A. C. M. Computing 1986

■ Freeman, Ross H.

Configurable electrical circuit having configurable logic elements and configurable interconnects

1989

■ Kahng, A. B. & Pati, Y. C.

<u>Subwavelength optical lithography, challenges and impact on physical design</u>

of Computer Science, U. C. L. A. Department & Numerical Technologies, Inc 1999

■ Schüler, E. & Helfers, Tim

XPP - eXtreme Processing Platform Technology for space applications

Astrium, P. A. C. T.

2001

■ Zoltan Baruch, Octavian Creţ & Pusztai, Kalman

Configurable processor

of Cluj-Napoca, Technical University 2002

■ Compton, Katherine & Hauck, Scott

Reconfigurable computing: a survey of systems and software

Surveys, A. C. M. Computing

2002

■ Gonzalez, Ricardo E.

<u>A Software Configurable Processor</u>

Inc, Stretch

2005

■ WEBER, Charles; BERGLUND, C. Neil & GABELLA, Patricia

Mask cost and profitability in photomask manufacturing, an empirical analysis

University, Portland State 2006

■ Carton, Olivier

Transistors et portes logiques

de Recherche en Informatique Fondamentale, Institut 2006

■ Prado, Daniel Francisco Gómez

Tutorial on FPGA routing

of Massachusetts, University

2006

■ Unknown

6502 Schematic

Unknown

2007

■ Museum, Computer History

Oral history panel on the development and promotion of the Motorola 68000

2007

■ Drepper, Ulrich

What every programmer should know about memory, Part 1

LWN

2007

■ Weisberg, David E.

The Engineering Design Revolution

2008

■ Wayne Wolf, Ahmed Amine Jerraya & Martin, Grant

Multiprocessor System-on-Chip (MPSoC) Technology

TFFF

2008

■ Articles, A. R. M. Technical Support Knowledge

What is the difference between a von Neumann architecture and a Harvard architecture?

Limited, A. R. M.

2008

■ Fouquet-Lapar, Matthias

The von Neumann Architecture and Alternatives

SGI

■ Museum, Computer History

Altera EP300 Design \& Development Oral History Panel 2009

■ McGrath, Dylan

FPGA startups stare down giants and ghosts

Times, Electronic Engineering 2009

■ Jeff Chase, Brent Nelson, John Bodily Zhaoyi Wei & Lee, Dah-Jye

<u>Real-Time Optical Flow Calculations on FPGA and GPU</u>
<u>Architectures: AComparison Study</u>

University, Brigham Young

2009

■ Kidd, Taylor IoT

Why P scales a C*V^2*f is so obvious

Zone, Intel Developer

2009

■ Conrad

Kit d'apprentissage de l'électronique pour débutants 2009

■ Jones, David H.; Powell, Adam; Bouganis, Christos-Savvas & Cheung, Peter Y. K.

GPU versus FPGA for high productivity computing

London, Imperial College

■ Kalarot, Ratheesh & Morris, John

<u>Comparison of FPGA and GPU implementations of Real-time</u>
<u>Stereo Vision</u>

of Auckland / IEEE, The University 2010

■ Cox, Russ

The MOS 6502 and the Best Layout Guy in the World 2011

■ Culver, John

How a CPU Microprocessor is made

Shack, The C. P. U.

2011

■ Feugey, David

Altera mise sur l'OpenCL pour révolutionner le monde des

FPGA

2011

■ Johnson, Jeff

Outsourcing FPGA design: pros and cons

2011

■ VERRY, Tim

<u>Apple's A6 processor uses hand drawn ARM cores to boost</u> <u>performance</u>

Perspective, P. C.

2012

■ Nenni, Daniel

A Brief History of FPGAs

2012

■ persons, Various

What are some examples of non-Von Neumann architectures?

StackOverflow

■ Fowers, Jeremy; Brown, Greg; Cooke, Patrick & Stitt, Greg

A performance and energy comparison of FPGAs, GPUs and

Multicores for sliding-window applications

of Florida, University

2012

An FPGA-based supercomputer for statistical physics: the weird case of Janus

2012

■ Wikipedia

P.A. Semi

2013

■ Electronics, Gould

Electrically Erasable Programmable Logic PEEL 18CV8
2013

■ Altera

<u>Implementing FPGA design with the OpenCL standard</u> 2013

■ Miller, Warren

Configurable processors as an alternative to FPGAs

Times, Electronic Engineering

2013

■ Higginbotham, Stacey

Why Microsoft is building programmable chips that specialize in search

GigaOM

2014

■ Hindriksen, Vincent

Why use OpenCL on FPGAs?

StreamHPC

■ VAndrei

Von Neumann vs Harvard architecture

StackOverflow

2014

■ Jones, Dr. Handel

Why migration to 20 nm bulk CMOS and 16/14 nm FinFets is not best approach for semiconductor industry

Strategies, International Business

2014

■ McMillan, John

PCB design then and now

Mentor, a Siemens Business

2015

■ Franz, Kaitlyn

History of the FPGA

Inc, Digilent

2015

■ Intel

<u>Intel completes acquisition of Altera</u>

2015

■ Higginbotham, Stacey

Why Intel will spend \$16.7 billion on Altera

Fortune

2015

■ Harris, Derrick

Microsoft is building fast, low-power neural networks with

FPGAs

GigaOM

■ Ovtcharov, Kalin; Ruwase, Olatunji; Kim, Joo-Young; Fowers, Jeremy; Strauss, Karin & Chung, Eric S.

<u>Accelerating deep convolutional neural networks using</u>
<u>specialized hardware</u>

Research, Microsoft 2015

■ et Technologie), U. N. I. T. (Université Numérique Ingénierie

Les grands mythes fondateurs" des nanos : la loi de Moore ou l'héritage du talk de Feynman de 1959"

2015

■ Kidd, Taylor IoT

Why P scales as $C^*V^2^*f$ is so obvious (pt 2)

Zone, Intel Developer

2015

■ Economist, The

The end of Moore's law

2015

■ Dettmers, Tim

Is implementing deep learning on FPGAs a natural next step after the success with GPUs?

2015

■ Ucamco, former Barco E. T. S.

Cilbr8tor Series

2016

■ HKallaher, Brandon

PAL vs. CPLD vs. FPGA

Blog, Digilent

■ Denisenko, Dmitry

OpenCL for FPGAs

Intel

2016

■ Eijkhout, Victor

Are there alternatives to the Von Neumann architecture?

Quora

2016

■ Vašut, Marek

Open-Source Tools for FPGA Development

Engineering, D. E. N. X. Software & Foundation, The Linux 2016

■ Economist, The

After Moore's law, the future of computing

2016

■ Processing, Berten Digital Signal

GPU vs FPGA Performance Comparison (white paper)

2016

■ Koch, Dirk; Hannig, Frank; Ziener, Daniel

FPGAs for Software Programmers

Springer

2016

■ Related, Embedded

When (and why) is it a good idea to use an FPGA in your embedded system design?

2017

■ Moore, Andrew & Wilson, Ron

FPGAs for Dummies

Intel

■ Staff, I. B. M. Research Editorial

IBM Scientifs Demonstrate In-memory Computing with 1
Million Devices for Applications in AI

Blog, I. B. M. Research 2017

■ Sebastian, Abu; Tuma, Tomas; Papandreou, Nikolaos; Gallo, Manuel Le; Kull, Lukas & Eleftheriou, Thomas Parnell \& Evangelos

<u>Temporal correlation detection using computational phase-</u> <u>change memory</u>

communications, Nature 2017

■ Sato, Kaz; Young, Cliff & Patterson, David

An in-depth look at Google's first Tensor Processing Unit (TPU)

Google

2017

■ Dean, Jeff & Hölzle, Urs

Build and train machine learning models on our new Google
Cloud TPUs

Google

2017

■ Wood, Lamont

CPU architecture after Moore's law: what's next?

Computerworld

2017

■ Hardwarebee

Field Programmable Gate Array (FPGA) History and Applications

■ Reese, Lynnette

<u>Comparing hardware for artificial intelligence: FPGAs vs. GPUs</u> vs. ASICs

Solutions, Embedded Intel

2018

■ Arrow

FPGA vs CPU vs GPU vs Microcontroller

2018

■ Dubuc, Damien

Afin de terminer notre série de billets, voici quelques réflexions et perspectives que nous ressortons de l'étude

Aneo

2018

■ Castells-Rufas, David

Workshop: programming FPGAs with OpenCL

Cephis

2018

■ Staff, I. B. M. Research Editorial

IBM Scientists Demonstrate Mixed-Precision In-Memory
Computing for the First Time; Hybrid Design for AI Hardware

Blog, I. B. M. Research

2018

■ Thornton, Scott

What's the difference between Von-Neumann and Harvard architectures?

Tips, Microcontroller

■ Arenas, Aaron

Introduction to FPGA design in Quartus

Intel

2018

■ Haff, Gordon

What comes after Moore's law

Project, The Enterprisers

2018

■ Gartenberg, Chaim

How Intel's 9th Gen chips show the way forward after Moore's

<u>Law</u>

Verge, The

2018

■ Rayome, Alison DeNisco

How programming will change over the next 10 years: 5 predictions

TechRepublic

2018

■ Burt, Jeffrey

FPGA make Xilinx says the future of computing if ACAP

Platform, The Next

2018

■ Waalsdorp, Museum

Computers for electronic and mechanical engineering

■ Altera, Intel

Cyclone V Device Handbook: Volume 1: Device Interfaces and Integration, Logic Array Blocks and Adaptive Logic Modules in Cyclone V Devices

2019

■ BitFusion

<u>BitFusion, the elastic AI infrastructure for multi-cloud</u> 2019

■ ViPress.net

<u>Infineon et NXP devant STMicroelectronics au 1er trimestre</u>
2019

2019

■ Altera, Intel

Cyclone V Device Datasheet

2019

■ Cantrill, Bryan

No Moore Left to Give

Joyent

2019

■ Suryavansh, Manu

<u>Google Coral Edge TPU Board Vs NVIDIA Jetson Nano Dev</u> <u>board - Hardware comparison</u>

Science, Towards Data

2019

■ Sterckval, Sam

Google Coral Edge TPU vs NVIDIA Jetson Nano : A quick deep dive into Edge AI performance

Noteworthy

■ Blog, Grus

<u>Comparison of two new machine learning accelerators, Coral</u> <u>and Jetson Nano</u>

2019

■ Feldman, Michael

With Agilex, Intel gets a coherent FPGA strategy

Platform, The Next