

Zihua Zhao

UNDERGRADUATE IN COMPUTER SCIENCE

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Education

Shanghai Jiao Tong University

HONORS BACHELOR OF SCIENCE(B.Sc. HONS) IN COMPUTER SCIENCE

- Member of ACM Class, which is an elite CS program for top 5% talented students.

Shanghai, China

September 2017-Present

Research Experiences

Research Interests

CURRENT RESEARCH INTERESTS AND RESEARCH FIELDS.

- Natural Language Processing, Machine Learning

July 2019 - PRESENT

Department of Computer Science and Engineering

UNDERGRADUATE RESEARCHER

- Advised by Prof. Hai Zhao.
- Research interests: Natural Language Processing, Machine Learning.

Shanghai Jiao Tong University

July 2019 - PRESENT

Capsule Network to replace Attention

USE CAPSULE NETWORK TO REPLACE ATTENTION.

- More MRC models adopt multiple attention layers to capture correlation, but a negative consequence is the explosion of parameters. So Capsule Network is used to replace attention layer.
- Work still working on in the lab.

Department of Computer Science
and Engineering

Present

Multi-Turn Conversation Project

MULTI-TURN CONVERSATION PROJECT

- Including launching a new dataset for multi-turn conversation, which is processed on a previous dataset, and a model trained on it.
- Work still working on in the lab.

Department of Computer Science
and Engineering

Present

Honors

Zhiyuan Honorary Scholarship

Shanghai Jiao Tong University

2017 & 2018 & 2019

Shanghai Jiao Tong University Elite Student Scholarship C Class

Shanghai Jiao Tong University

2017 & 2018 & 2019

The Second Award for Robomaster

ELECTRONIC CONTROLLER, MANIPULATOR

- Robomaster is a competition to design and control robots to fight against other teams.
- Robot of our team finally ranked the fifth and got the second reward for the competition.

Shanghai Jiao Tong University

Fall 2018

Selected Projects

Mx-Compiler

A COMPILER FOR Mx* LANGUAGE

- It is a compiler implemented in Java from scratch, and the source language is Mx*, which is a C-and-Java-like language.
- The compiler is implemented with Semantic Analysis, Dead Code Elimination, Register Allocation methods, etc.

<https://github.com/ZihuaZhao/Compiler>

Spring 2019

RISC-V

https://github.com/ZihuaZhao/RISC_V

A RISC-V CPU ON FPGA

Fall 2018

- Designed and implemented a FPGA-supported RISC-V CPU with 5-stage pipeline implemented in Verilog HDL.

Railway Management System

<https://github.com/ZihuaZhao/Railway-Management-System>

RAILWAY TICKET MANAGEMENT SYSTEM

May 2018

- Backend Development, including a B-plus tree.
- Group project for Data Structure 2018 (CS147), ACM Class, SJTU.

Text Classification

Machine Learning by Prof. Weinan Zhang

ENSEMBLED MODELS TO PREDICT WHETHER THE COMMENT IS POSITIVE OR NEGATIVE.

October 2019-Present

- Separating the sentences word by word performs the best. XGBoost, NN, CNN ensembled to predict the result.
- The model has exceeded the strong base line.
- Practising project for Machine Learning 2019 taught by Prof. Weinan Zhang, ACM Class, SJTU.

Skills

Skills Python, Java, C/C++