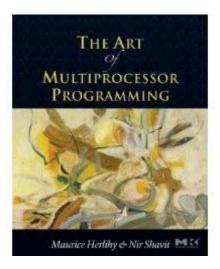
Spin Locks and Contention



Companion slides for
The Art of Multiprocessor Programming
by Maurice Herlihy & Nir Shavit

Today: Revisit Mutual Exclusion

- Performance, not just correctness
- Proper use of multiprocessor architectures
- A collection of locking algorithms...



What Should you do if you can't get a lock?

- Keep trying
 - "spin" or "busy-wait"
 - Good if delays are short
- Give up the processor
 - Good if delays are long
 - Always good on uniprocessor

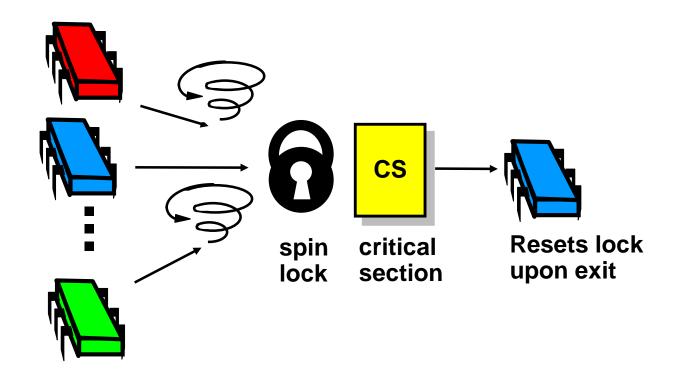


What Should you do if you can't get a lock?

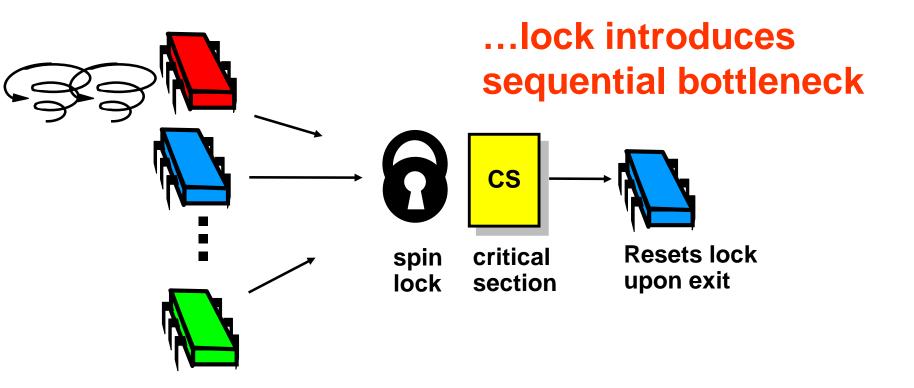
- Keep trying
 - "spin" or "busy-wait"
 - Good if delays are short
- Give up the processor
 - Good if delays are long
 - Always good on uniprocessor

our focus



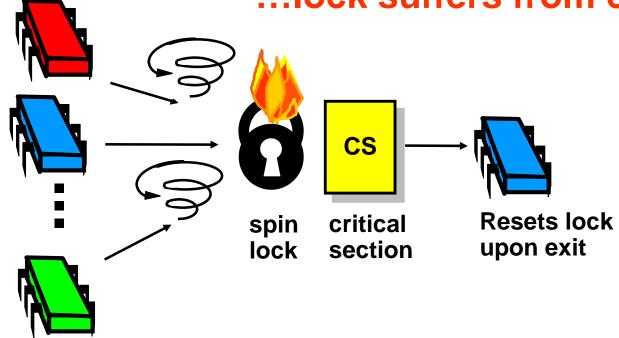






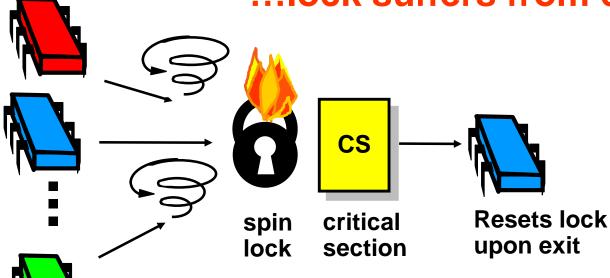


...lock suffers from contention





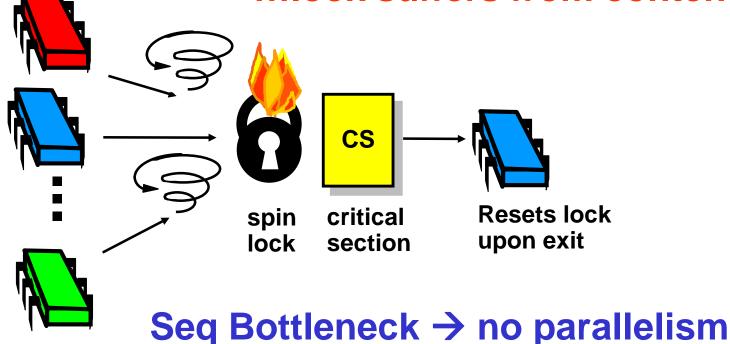




Notice: these are distinct phenomena

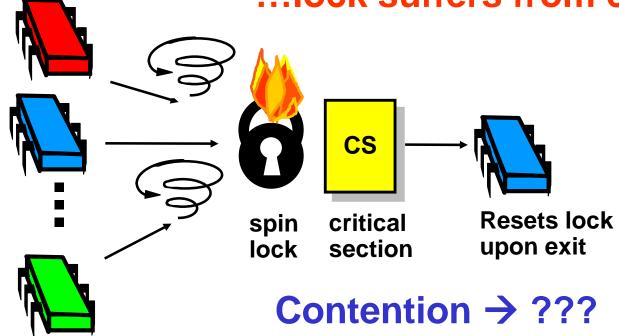


...lock suffers from contention











- Boolean value
- Test-and-set (TAS)
 - Swap true with current value
 - Return value tells if prior value was true or false
- Can reset just by writing false
- TAS aka "getAndSet"



```
public class AtomicBoolean {
  boolean value;

public synchronized boolean
  getAndSet(boolean newValue) {
   boolean prior = value;
   value = newValue;
   return prior;
  }
}
```



```
public class AtomicBoolean {
 public synchronized boolean
  getAndSet(boolean hewValue) {
   boolean prior = value;
   value = newValue;
   return prior;
                    Package
           java.util.concurrent.atomic
```



```
public class AtomicBoolean {
  boolean value;

public synchronized boolean
  getAndSet(boolean newValue) {
  boolean prior = value;
  value = newValue;
  return prior;
}
```

Swap old and new values





```
AtomicBoolean lock
= new AtomicBoolean(false)

boolean prior = lock.getAndSet(true)
```

Swapping in true is called "test-and-set" or TAS



- Locking
 - Lock is free: value is false
 - Lock is taken: value is true
- Acquire lock by calling TAS
 - If result is false, you win
 - If result is true, you lose
- Release lock by writing false



```
class TASlock {
AtomicBoolean state =
  new AtomicBoolean(false);
void lock() {
 while (state.getAndSet(true)) {}
void unlock() {
  state.set(false);
 } }
```



```
class TASlock
AtomicBoolean state =
 new AtomicBoolean(false);
void lock() {
 while (state.getAndSet(true)) {}
void unlock() {
 Lock state is AtomicBoolean
```



```
class TASlock {
AtomicBoolean state =
 new AtomicBoolean(false);
 while (state.getAndSet(true)) {}
void unlock() {
 Keep trying until lock acquired
```



```
class TA
         Release lock by resetting
AtomicB
                state to false
 new At
void lock() {
 while (state.getAndSet(true)) {}
 state.set(false);
```



Performance

- Experiment
 - n threads
 - Increment shared counter 1 million times
- How long should it take?
- How long does it take?



Graph no speedup because of sequential bottleneck ideal



threads





Test-and-Test-and-Set Locks

- Lurking stage
 - Wait until lock "looks" free
 - Spin while read returns true (lock taken)
- Pouncing state
 - As soon as lock "looks" available
 - Read returns false (lock free)
 - Call TAS to acquire lock
 - If TAS loses, back to lurking



Test-and-test-and-set Lock

```
class TTASlock {
AtomicBoolean state =
  new AtomicBoolean(false);
void lock() {
  while (true) {
   while (state.get()) {}
   if (!state.getAndSet(true))
    return;
```



Test-and-test-and-set Lock

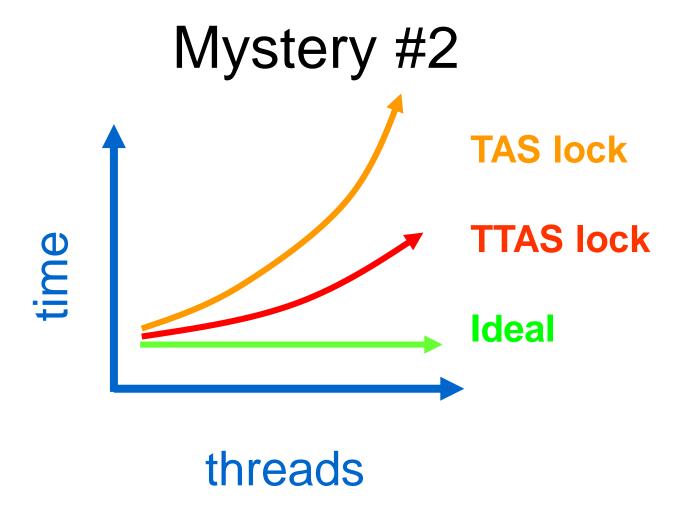
```
class TTASlock {
AtomicBoolean state =
  new AtomicBoolean(false);
void lock() {
  while (true)
  while (state.get()) {}
   if (!state.getAndSet(true))
    return;
            Wait until lock looks free
```



Test-and-test-and-set Lock

```
class TTASlock {
AtomicBoolean state =
  new AtomicBoolean(false);
                           Then try to
void lock() {
                            acquire it
  while (true) {
   while (state.get(
   if (!state.getAndSet(true))
    return;
```







Mystery

- Both
 - TAS and TTAS
 - Do the same thing (in our model)
- Except that
 - TTAS performs much better than TAS
 - Neither approaches ideal

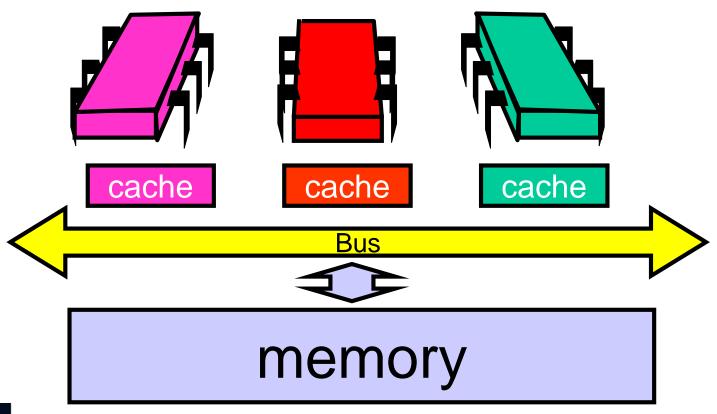


Opinion

- Our memory abstraction is broken
- TAS & TTAS methods
 - Are provably the same (in our model)
 - Except they aren't (in field tests)
- Need a more detailed model ...

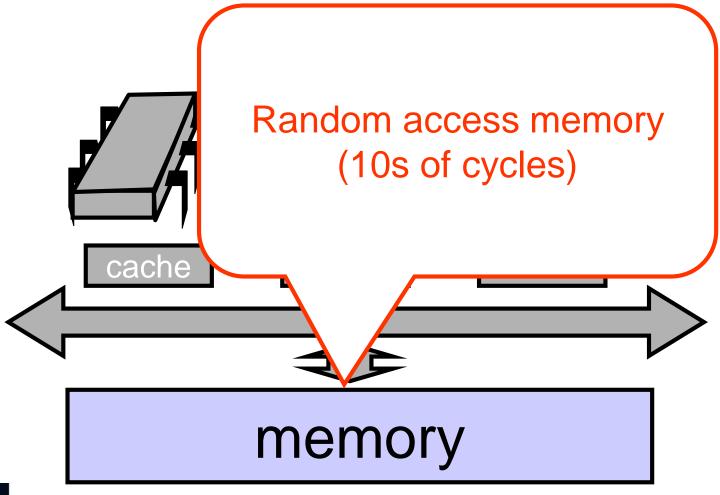


Bus-Based Architectures





Bus-Based Architectures

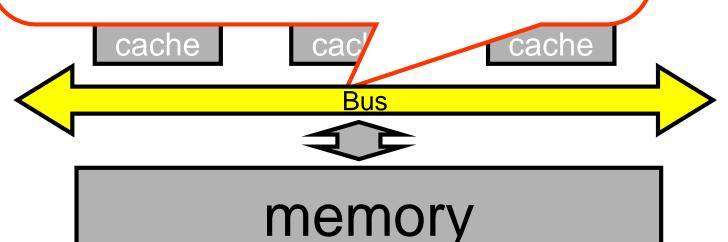




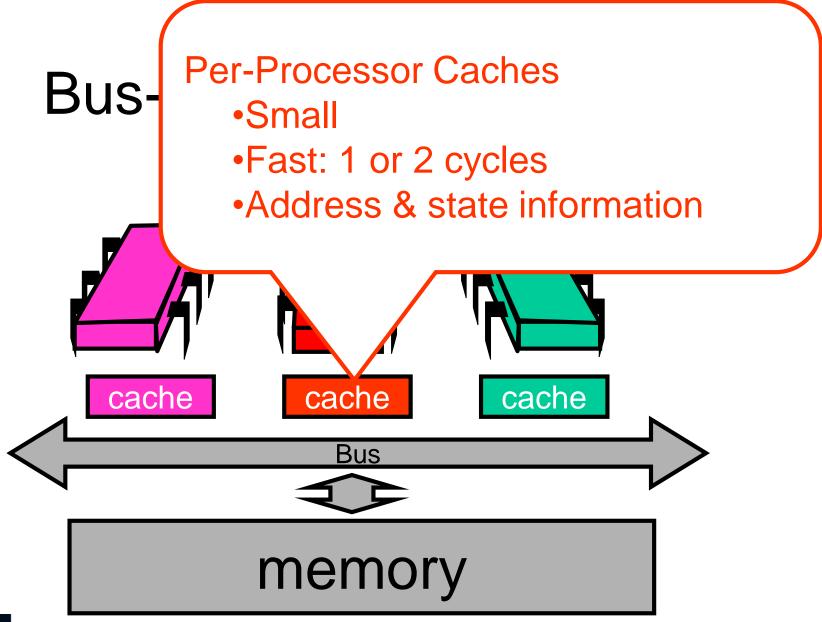
Rus-Rased Architectures

Shared Bus

- Broadcast medium
- One broadcaster at a time
- Processors and memory all "snoop"









Granularity

- Caches operate at a larger granularity than a word
- Cache line: fixed-size block containing the address (today 64 or 128 bytes)

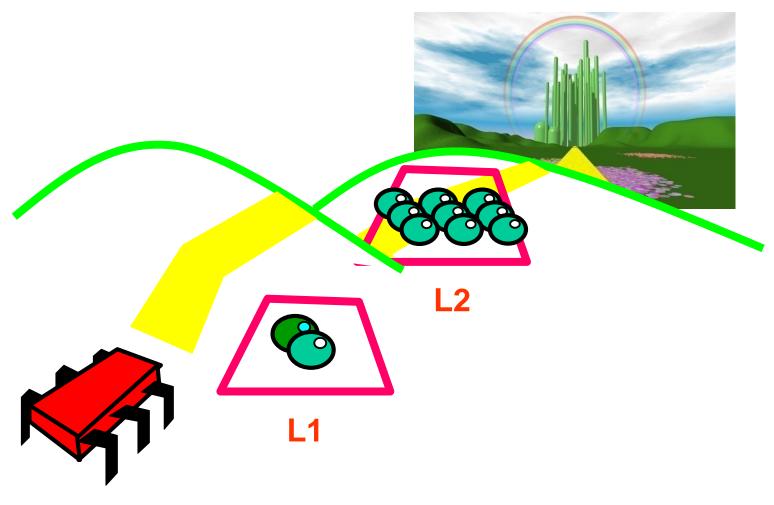


Locality

- If you use an address now, you will probably use it again soon
 - Fetch from cache, not memory
- If you use an address now, you will probably use a nearby address soon
 - In the same cache line

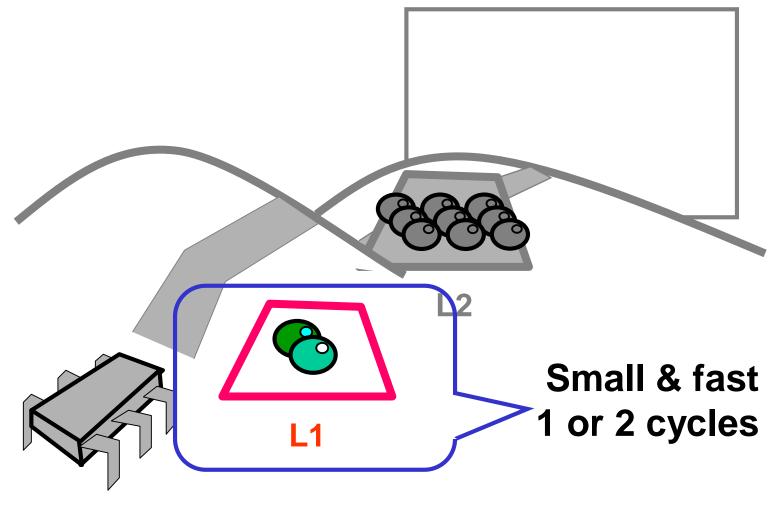


L1 and L2 Caches



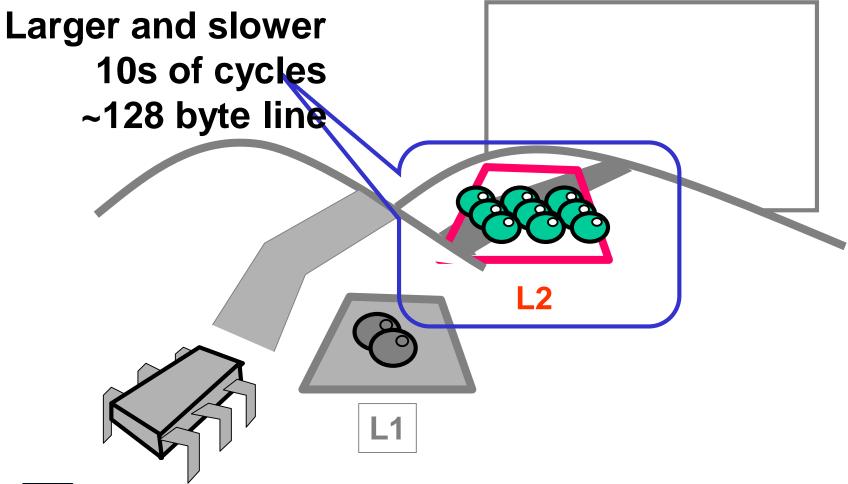


L1 and L2 Caches





L1 and L2 Caches





Jargon Watch

- Cache hit
 - "I found what I wanted in my cache"
 - Good Thing™



Jargon Watch

- Cache hit
 - "I found what I wanted in my cache"
 - Good Thing™
- Cache miss
 - "I had to shlep all the way to memory for that data"
 - Bad Thing™



Cave Canem

- This model is still a simplification
 - But not in any essential way
 - Illustrates basic principles
- Will discuss complexities later



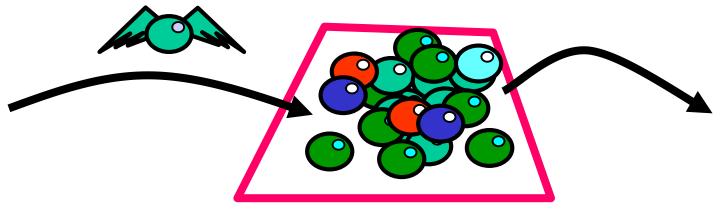
When a Cache Becomes Full...

- Need to make room for new entry
- By evicting an existing entry
- Need a replacement policy
 - Usually some kind of least recently used heuristic



Fully Associative Cache

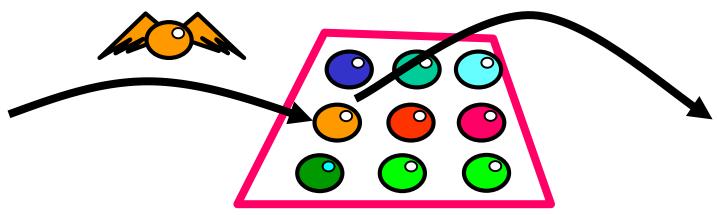
- Any line can be anywhere in the cache
 - Advantage: can replace any line
 - Disadvantage: hard to find lines





Direct Mapped Cache

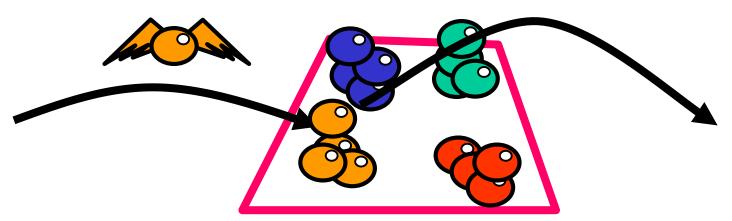
- Every address has exactly 1 slot
 - Advantage: easy to find a line
 - Disadvantage: must replace fixed line





K-way Set Associative Cache

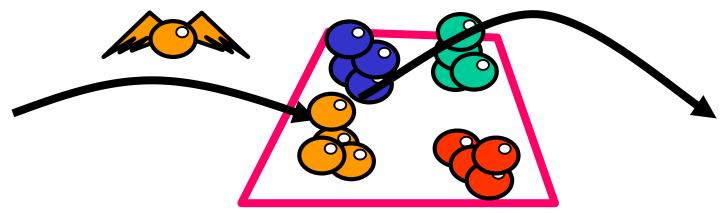
- Each slot holds k lines
 - Advantage: pretty easy to find a line
 - Advantage: some choice in replacing line





Multicore Set Associativity

- k is 8 or even 16 and growing...
 - Why? Because cores share sets
 - Threads cut effective size if accessing different data





Cache Coherence

- A and B both cache address x
- A writes to x
 - Updates cache
- How does B find out?
- Many cache coherence protocols in literature



- Modified
 - Have modified cached data, must write back to memory



- Modified
 - Have modified cached data, must write back to memory
- Exclusive
 - Not modified, I have only copy



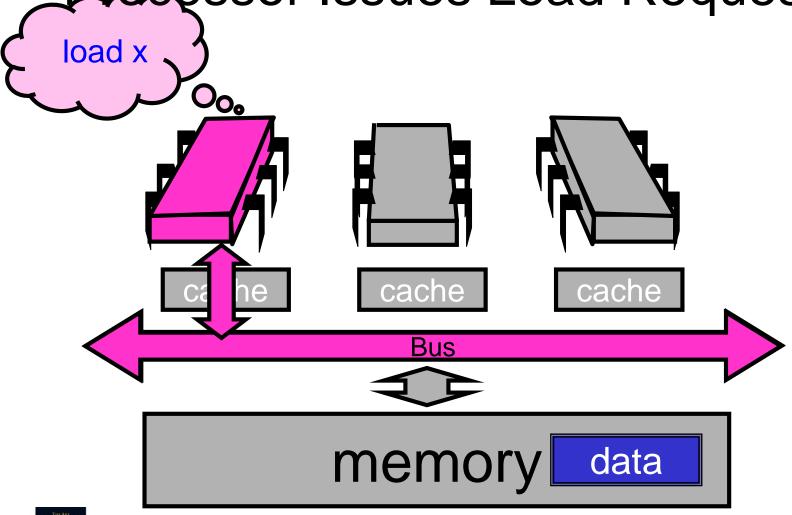
- Modified
 - Have modified cached data, must write back to memory
- Exclusive
 - Not modified, I have only copy
- Shared
 - Not modified, may be cached elsewhere



- Modified
 - Have modified cached data, must write back to memory
- Exclusive
 - Not modified, I have only copy
- Shared
 - Not modified, may be cached elsewhere
- Invalid
 - Cache contents not meaningful

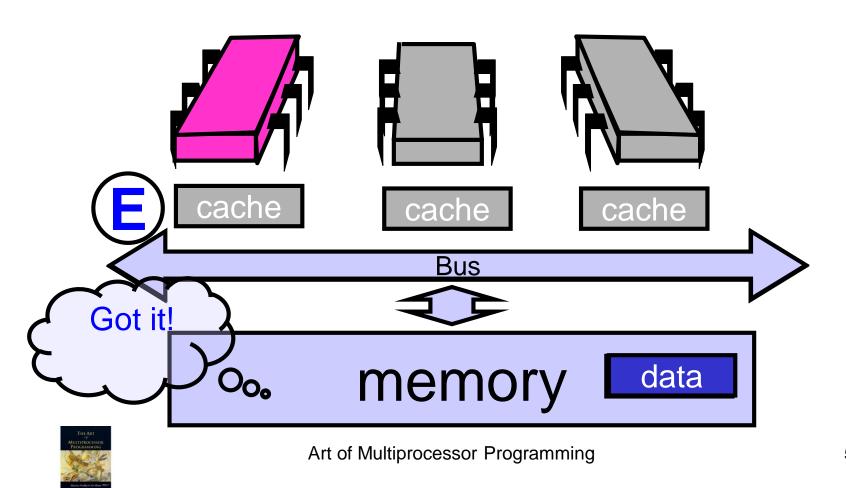


Precessor Issues Load Request

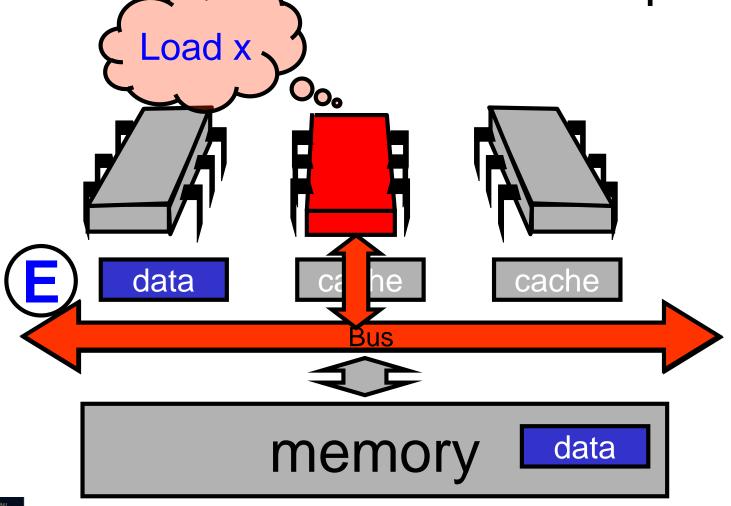




Memory Responds



Processor Issues Load Request

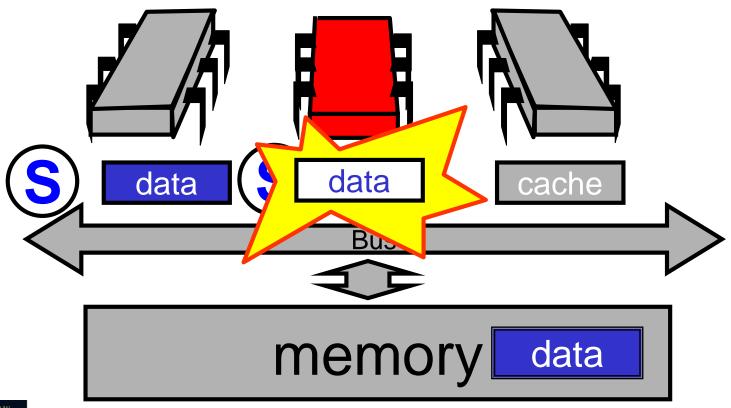




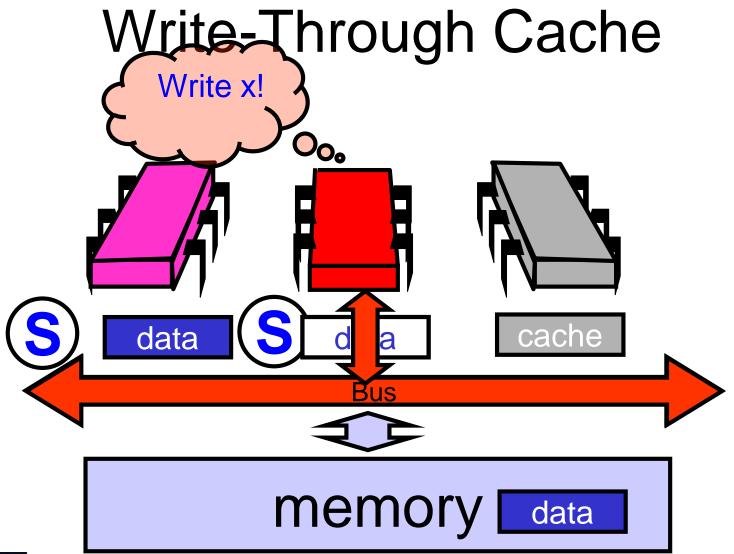
Other Processor Responds Got it cache cache Bus data memory



Modify Cached Data









Write-Through Caches

- Immediately broadcast changes
- Good
 - Memory, caches always agree
 - More read hits, maybe
- Bad
 - Bus traffic on all writes
 - Most writes to unshared data
 - For example, loop indexes ...



Write-Through Caches

- Immediately broadcast changes
- Good

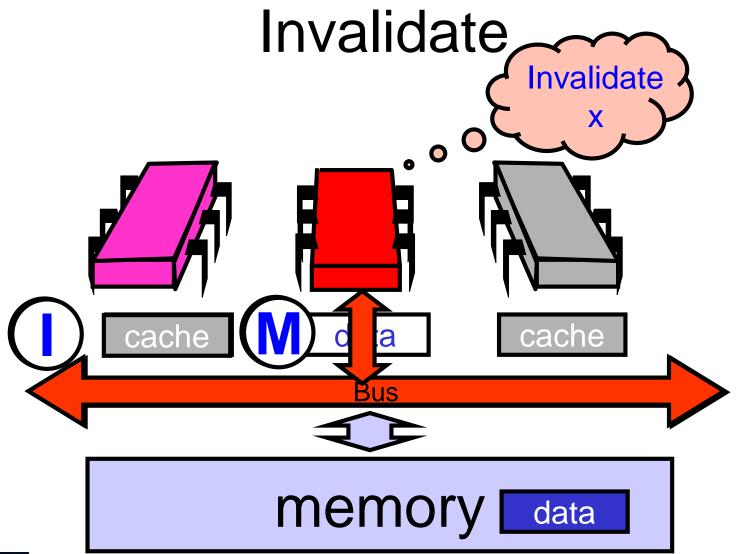
- "show stoppers"
- Memory, caches always agree
- More read hits, maybe
- Bad
 - Bus traffic on all writes
 - Most writes to unshared data
 - For example, loop indexes ...



Write-Back Caches

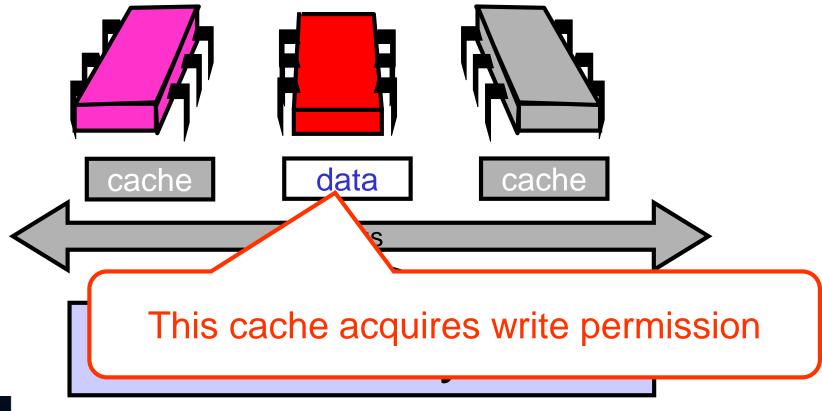
- Accumulate changes in cache
- Write back when line evicted
 - Need the cache for something else
 - Another processor wants it







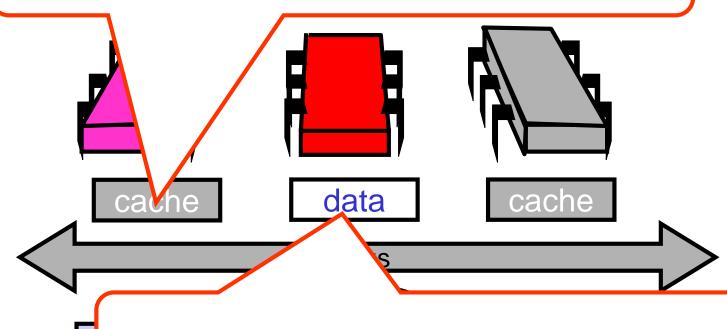
Invalidate





Invalidate

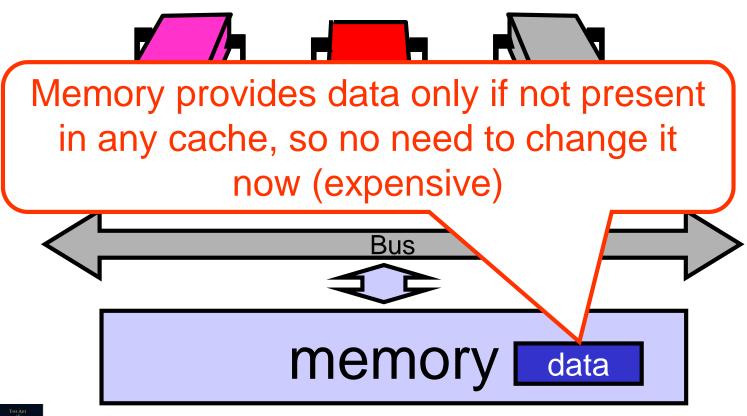
Other caches lose read permission



This cache acquires write permission



Invalidate





Mutual Exclusion

- What do we want to optimize?
 - Bus bandwidth used by spinning threads
 - Release/Acquire latency
 - Acquire latency for idle lock



Simple TASLock

- TAS invalidates cache lines
- Spinners
 - Miss in cache
 - Go to bus
- Thread wants to release lock
 - delayed behind spinners



Test-and-Test-and-Set Locks

- Lurking stage
 - Wait until lock "looks" free
 - Spin while read returns true (lock taken)
- Pouncing state
 - As soon as lock "looks" available
 - Read returns false (lock free)
 - Call TAS to acquire lock
 - If TAS loses, back to lurking



Test-and-test-and-set Lock

```
class TTASlock {
AtomicBoolean state =
  new AtomicBoolean(false);
void lock() {
  while (true) {
   while (state.get()) {}
   if (!state.getAndSet(true))
    return;
```



Test-and-test-and-set Lock

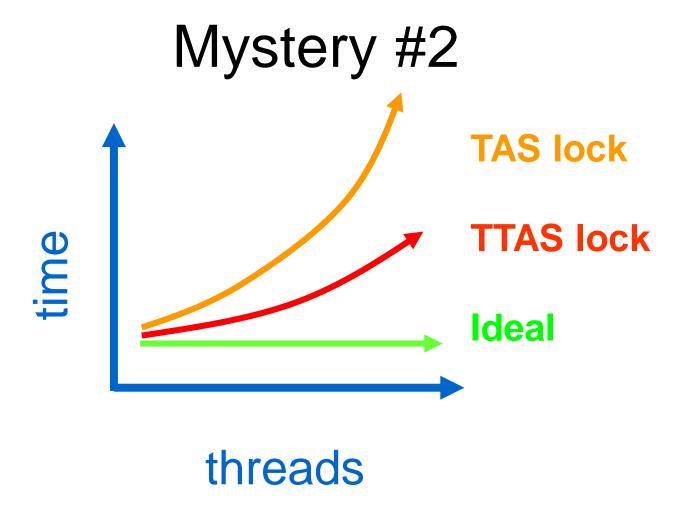
```
class TTASlock {
AtomicBoolean state =
  new AtomicBoolean(false);
void lock() {
  while (true)
  while (state.get()) {}
   if (!state.getAndSet(true))
    return;
            Wait until lock looks free
```



Test-and-test-and-set Lock

```
class TTASlock {
AtomicBoolean state =
  new AtomicBoolean(false);
                           Then try to
void lock() {
                            acquire it
  while (true) {
   while (state.get(
   if (!state.getAndSet(true))
    return;
```





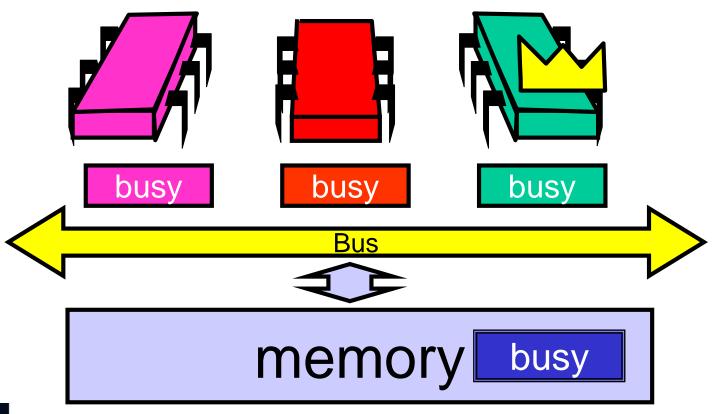


Test-and-test-and-set

- Wait until lock "looks" free
 - Spin on local cache
 - No bus use while lock busy
- Problem: when lock is released
 - Invalidation storm ...

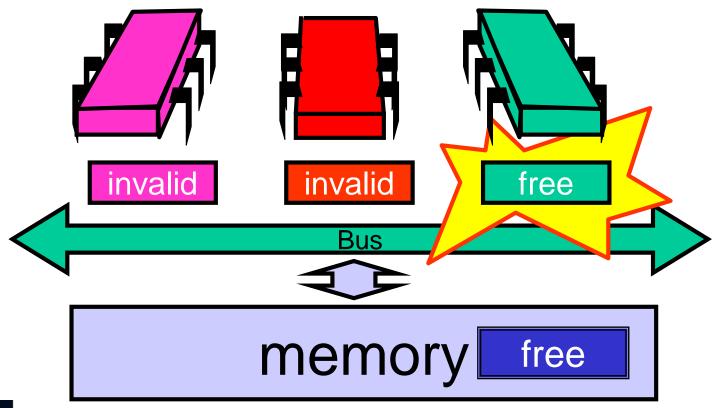


Local Spinning while Lock is Busy





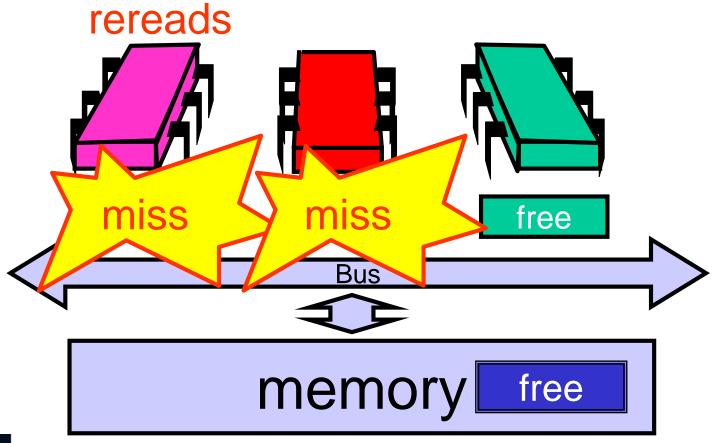
On Release





On Release

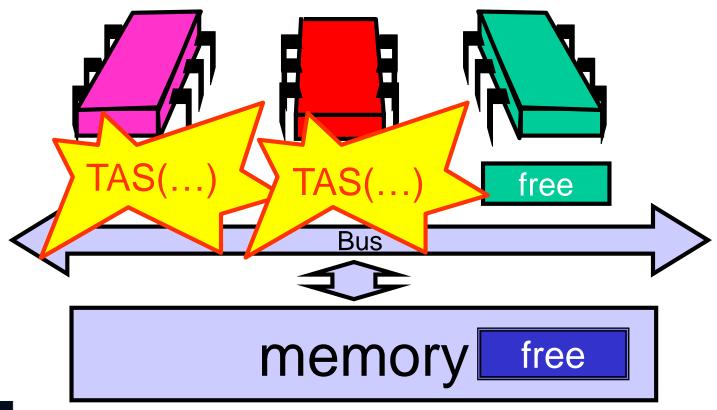
Everyone misses,





On Release

Everyone tries TAS



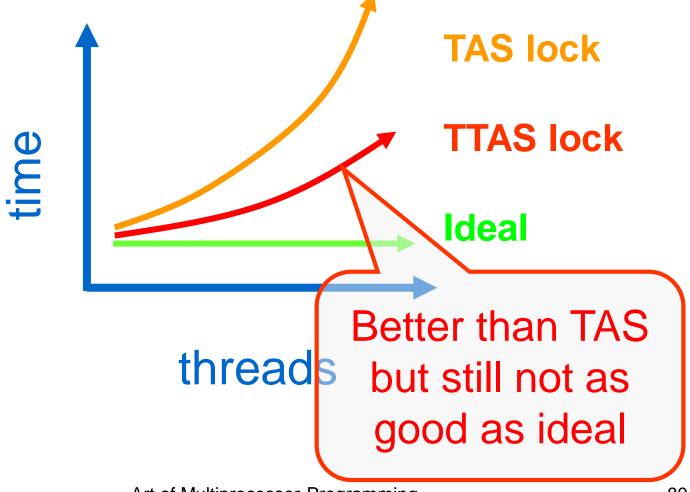


Problems

- Everyone misses
 - Reads satisfied sequentially
- Everyone does TAS
 - Invalidates others' caches
- Eventually quiesces after lock acquired
 - How long does this take?



Mystery Explained

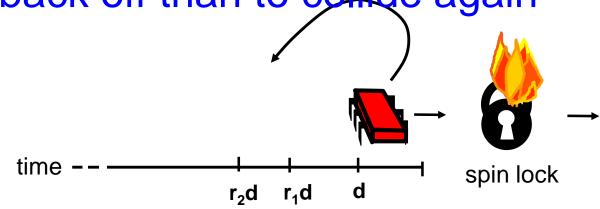




Solution: Introduce Delay

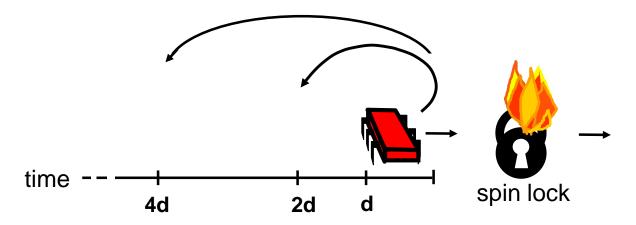
- If the lock looks free
 - But I fail to get it
- There must be contention

Better to back off than to collide again





Dynamic Example: Exponential Backoff



If I fail to get lock

- Wait random duration before retry
- Each subsequent failure doubles expected wait



```
public class Backoff implements lock {
 public void lock() {
  int delay = MIN DELAY;
  while (true) {
   while (state.get()) {}
   if (!lock.getAndSet(true))
    return;
   sleep(random() % delay);
   if (delay < MAX DELAY)</pre>
    delay = 2 * delay;
 }}}
```



```
public class Backoff implements lock {
 public void lock()
 int delay = MIN DELAY;
  while (true) {
   while (state.get ())
   if (!lock.getAndSet(true))
    return;
   sleep(random() % delay)
   if (delay < MAX DELAY)
   delay = 2 * delay:
Fix minimum delay
 } } }
```



```
public class Backoff implements lock {
 public void lock() {
  int delay = MIN DELAY;
  while (true)
  while (state.get()) {}
   if (!lock.getAndSet(true))
    return;
   sleep(random() % delay
   if (delay < MAX DELAY)
    delay = 2
               Wait until lock looks free
 } } }
```



```
public class Backoff implements lock {
 public void lock() {
  int delay = MIN DELAY;
  while (true) {
   while (state.get()) {}
   if (!lock.getAndSet(true))
    return;
   sleep(random() % delay)
   if (delay < MAX DELAY)
    delay = 2 * delay;
                        If we win, return
```



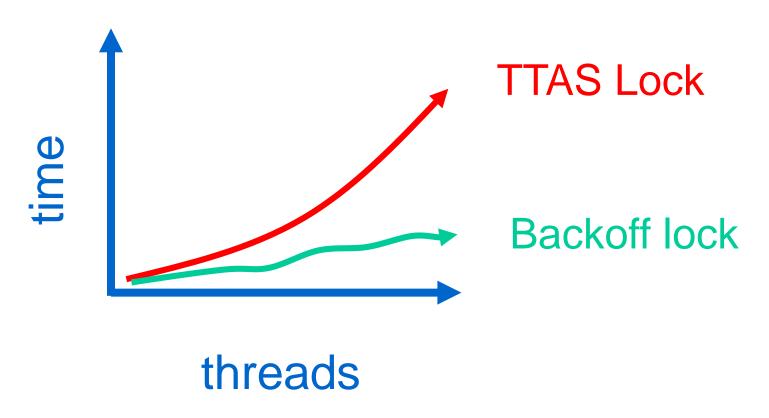
```
public
        Back off for random duration
 public
  int delay = MIN DELAY;
  while (true) {
   while (state.get(
   if (!lock.getAndSet(true))
   sleep(random() % delay);
    delay = 2 * delay;
 } } }
```



```
Double max delay, within reason
int delay = MIN DELAY;
while (true) {
while (state.get
 if (!lock.getAndSet(true))
  return;
 sleep(random()
 if (delay < MAX DELAY)
  delay = 2 * delay;
```



Spin-Waiting Overhead





Backoff: Other Issues

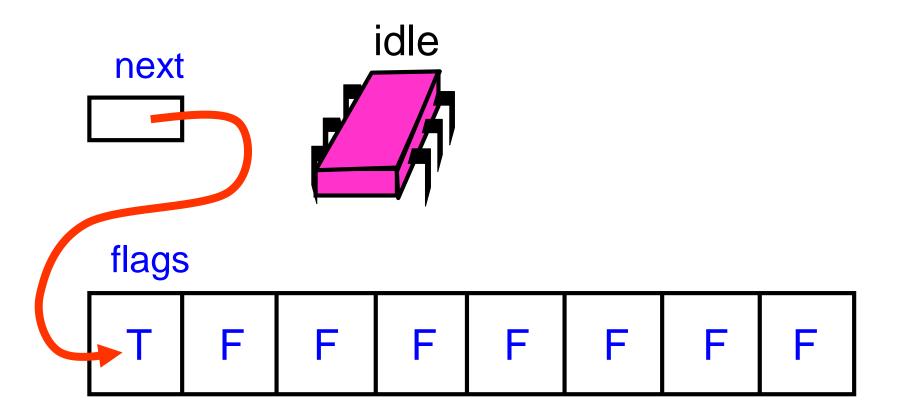
- Good
 - Easy to implement
 - Beats TTAS lock
- Bad
 - Must choose parameters carefully
 - Not portable across platforms



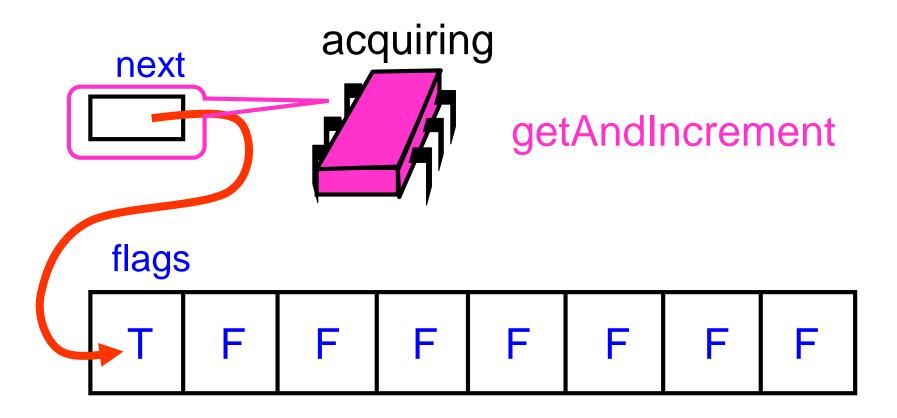
Idea

- Avoid useless invalidations
 - By keeping a queue of threads
- Each thread
 - Notifies next in line
 - Without bothering the others

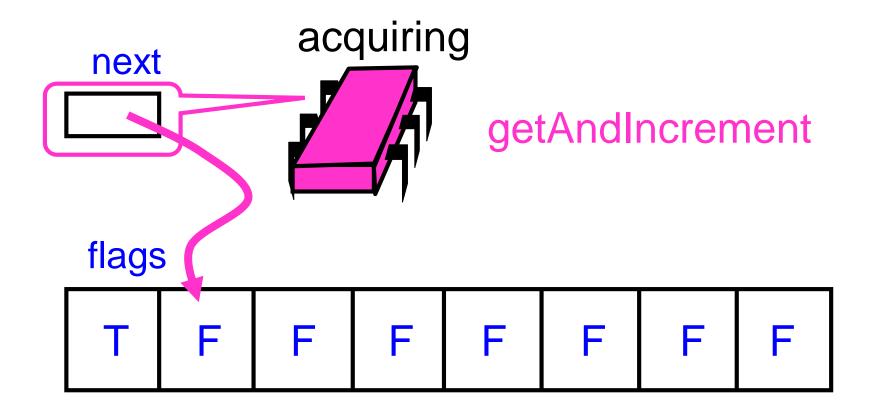




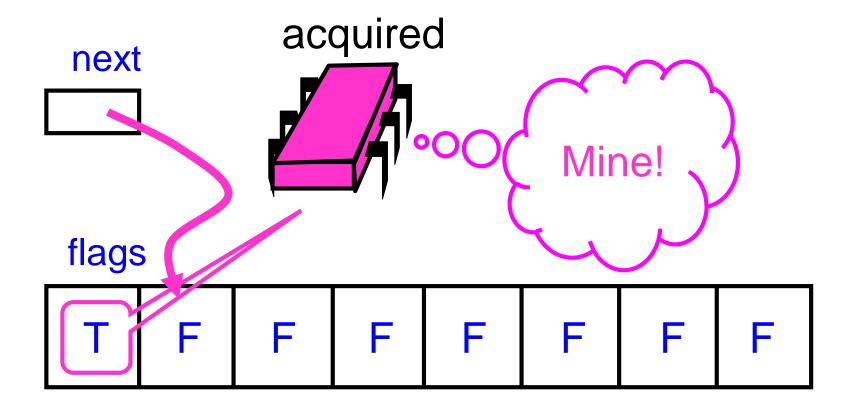




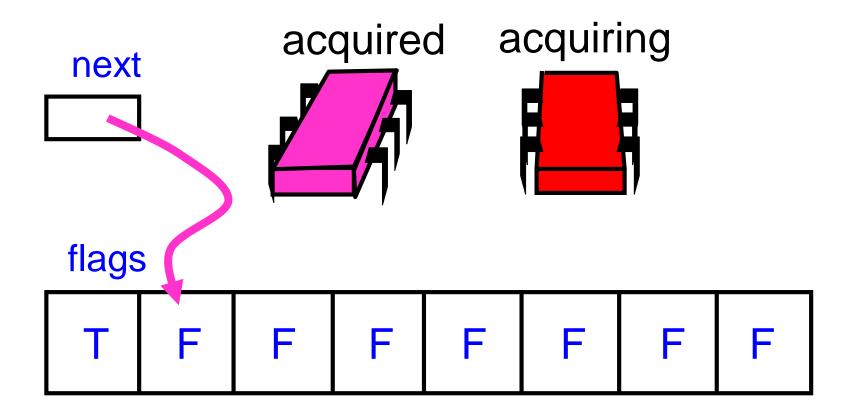




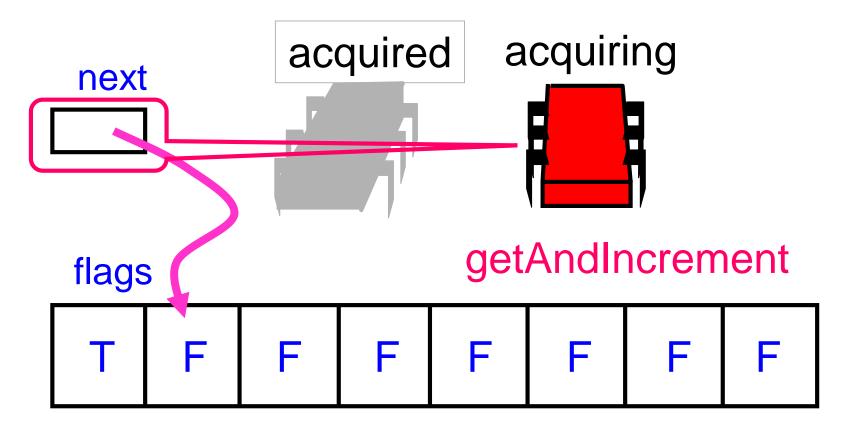




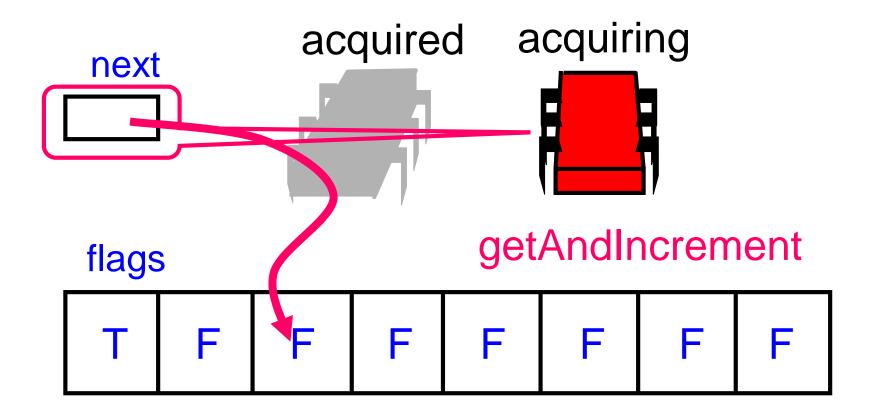




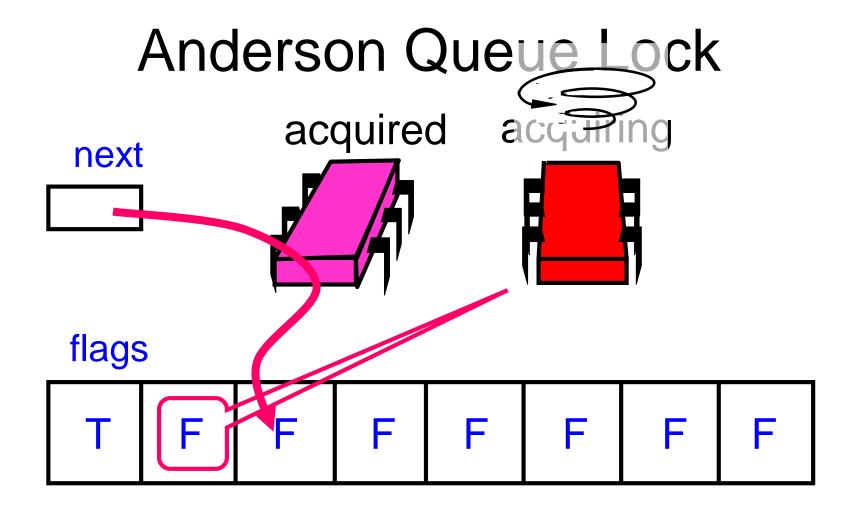




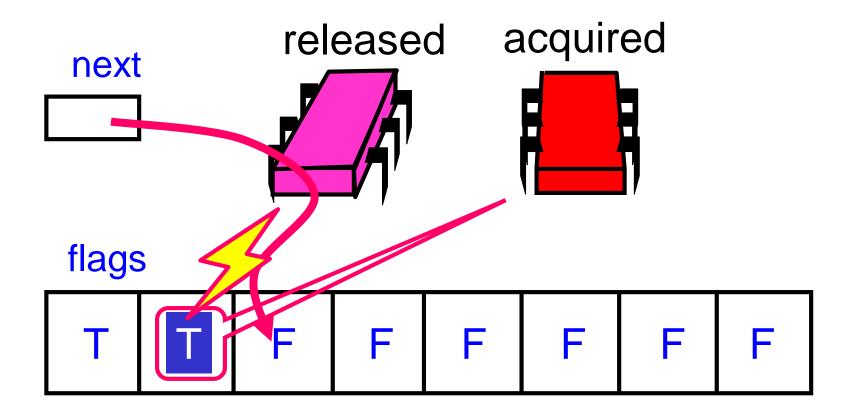




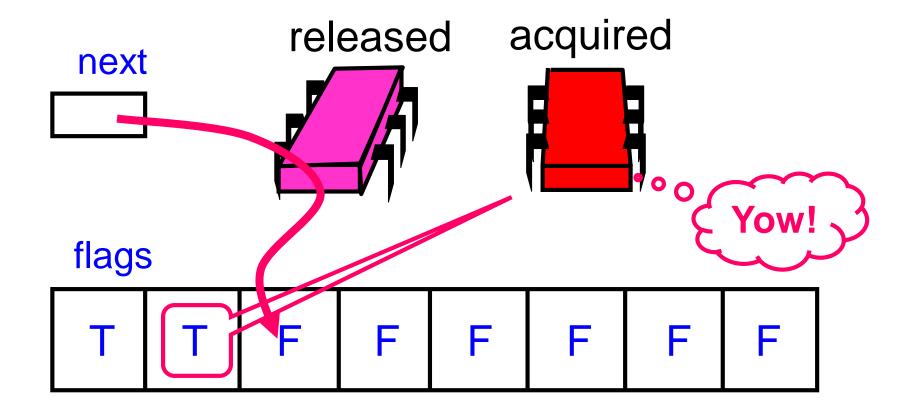














```
class ALock implements Lock {
  boolean[] flags={true,false,...,false};
  AtomicInteger next
  = new AtomicInteger(0);
  ThreadLocal<Integer> mySlot;
```



One flag per thread



```
class ALock implements Lock {
  boolean[] flags={true,false,...,false};
AtomicInteger next
  = new AtomicInteger(0);
ThreadLocal<Integer> mySlot;
```

Next flag to use



```
class ALock implements Lock {
boolean[] flags={true,false,...,false};
AtomicInteger next
 = new AtomicInteger(0);
ThreadLocal<Integer> mySlot;
            Thread-local variable
```



```
public lock() {
mySlot = next.getAndIncrement();
while (!flags[mySlot % n]) {};
 flags[mySlot % n] = false;
public unlock() {
 flags[(mySlot+1) % n] = true;
```



```
public lock() {
mySlot = next.getAndIncrement();
 while (!flags[mySlot % n])
 flags[mySlot % n] = false;
public unlock() {
 flags[(mySlot+1) % n]
                        Take next slot
```



```
public lock() {
mySlot = next.getAndIncrement();
while (!flags[mySlot % n]) {};
 flags[mySlot % n] = false;
public unlock() {
 flags[(mySlot+1)
                  Spin until told to go
```



Anderson Queue Lock

```
public lock() {
myslot = next.getAndIncrement();
 while (!flags[myslot % n]) {};
flags[myslot % n] = false;
public unlock() {
 flags[(myslot+1) % n] = true;
               Prepare slot for re-use
```

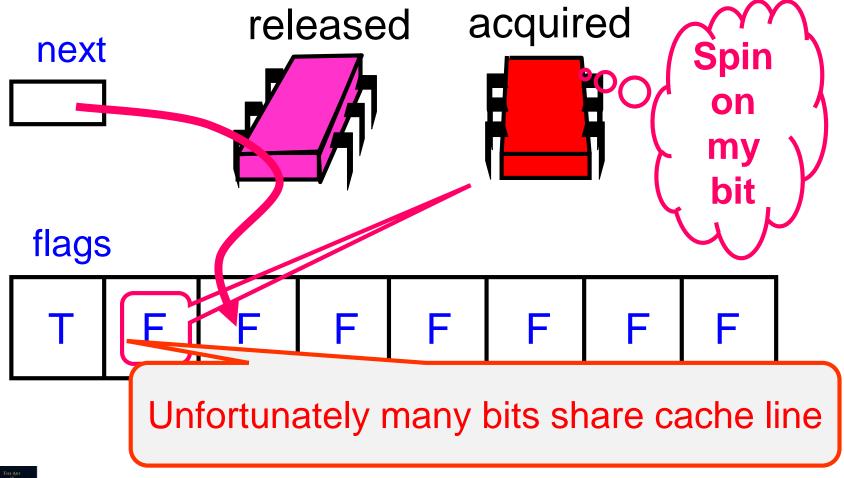


Anderson Queue Lock

```
public lock() { Tell next thread to go
 mySlot = next.getAndincrement();
 while (!flags[mySlot %
 flags[mySlot % n] = false
    ic unlock
 flags[(mySlot+1) % n] = true;
```

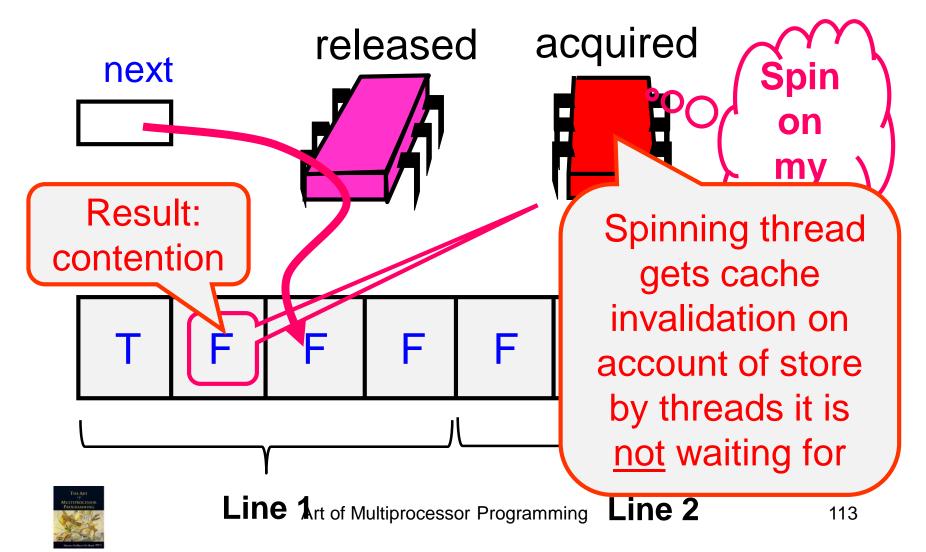


Local Spinning

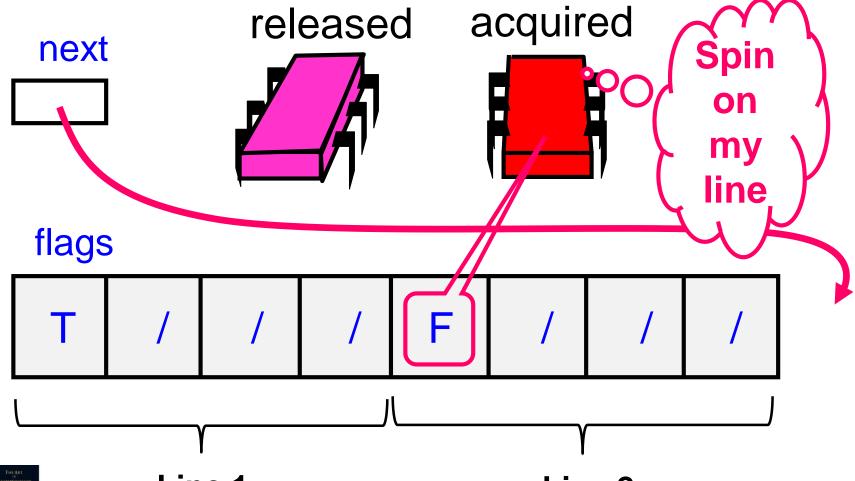




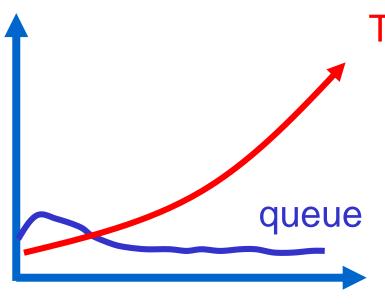
False Sharing



The Solution: Padding



Performance



TTAS

- Shorter handover than backoff
- Curve is practically flat
- Scalable performance



Anderson Queue Lock

Good

- First truly scalable lock
- -Simple, easy to implement
- Back to FCFS order (like Bakery)



Anderson Queue Lock

Bad

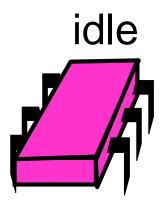
- –Space hog…
- One bit per thread → one cache line per thread
 - What if unknown number of threads?
 - What if small number of actual contenders?

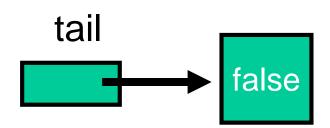


CLH Lock

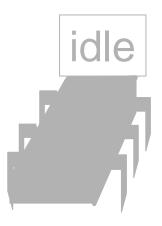
- FCFS order
- Small, constant-size overhead per thread

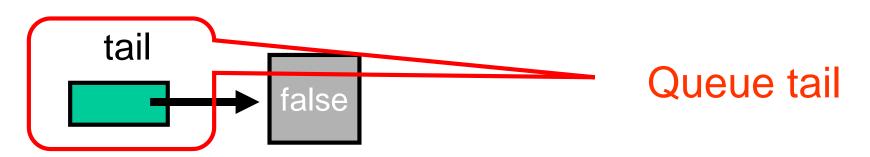




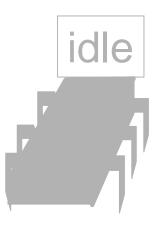


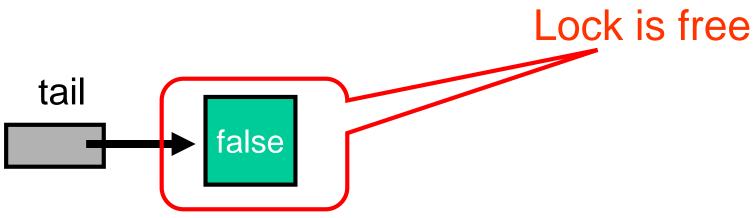


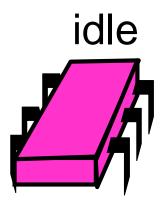


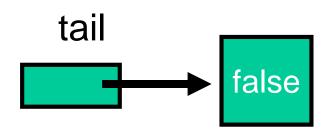










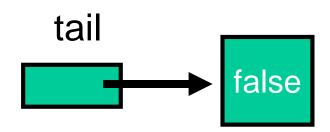




Purple Wants the Lock

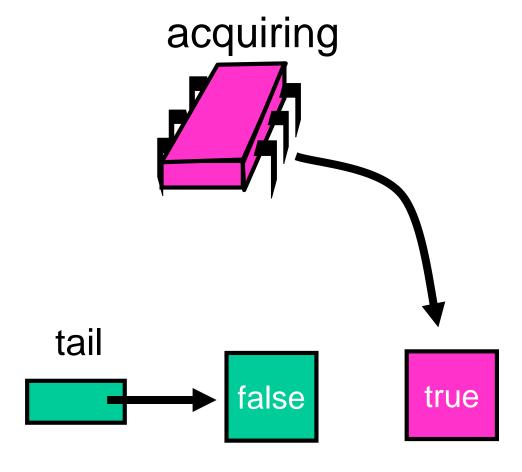
acquiring





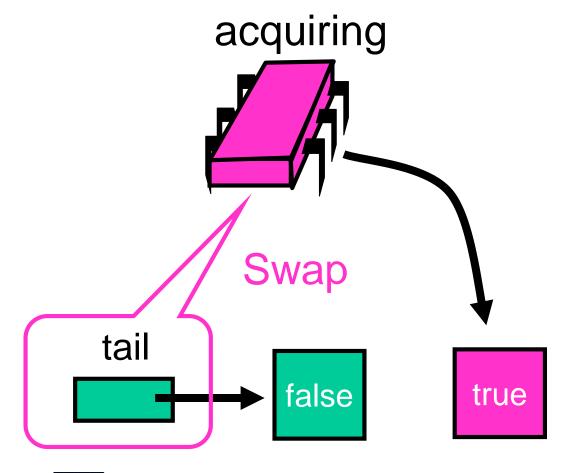


Purple Wants the Lock



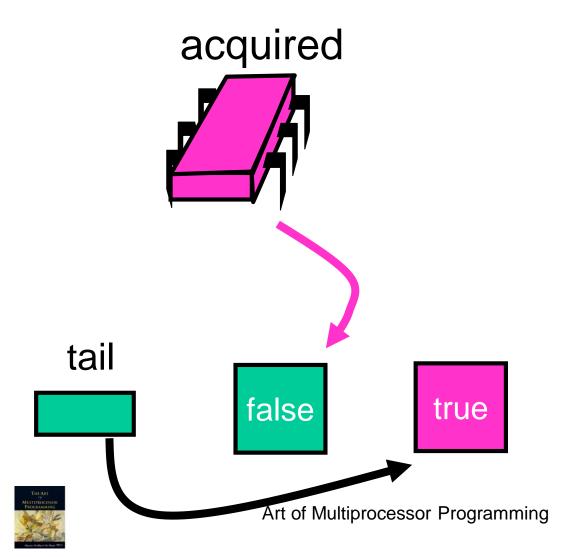


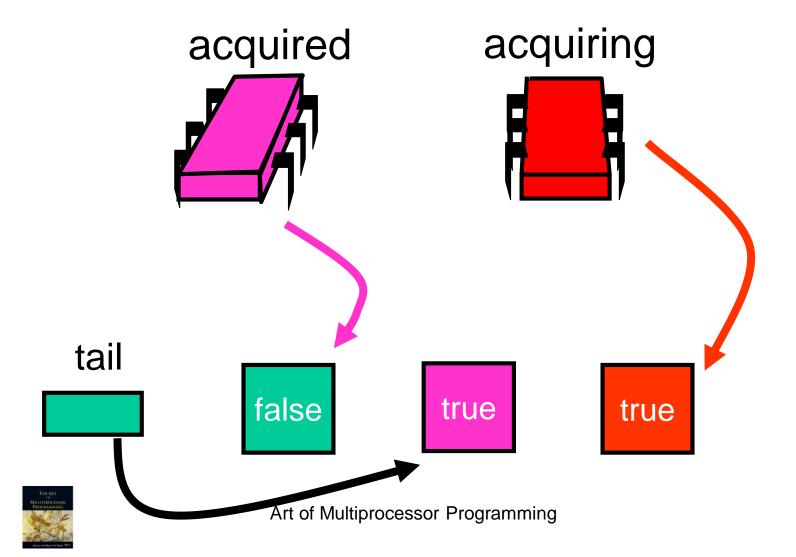
Purple Wants the Lock

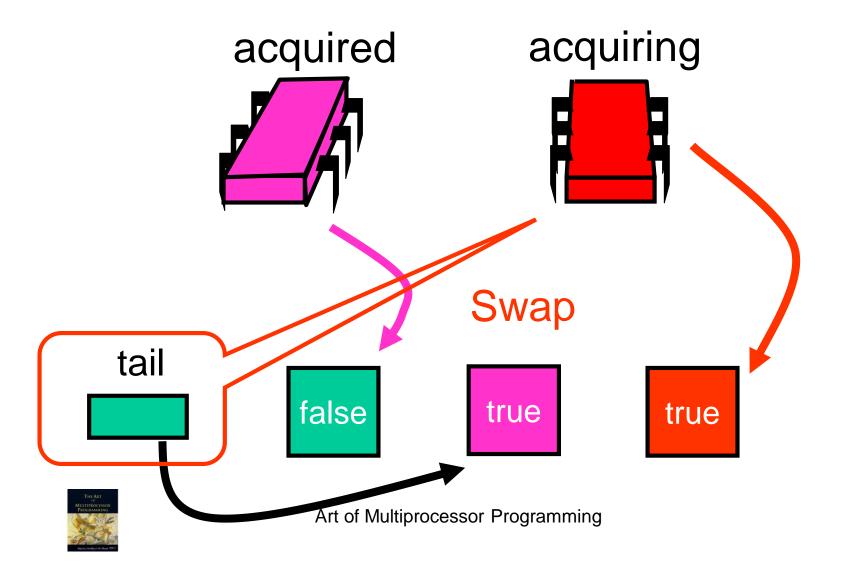


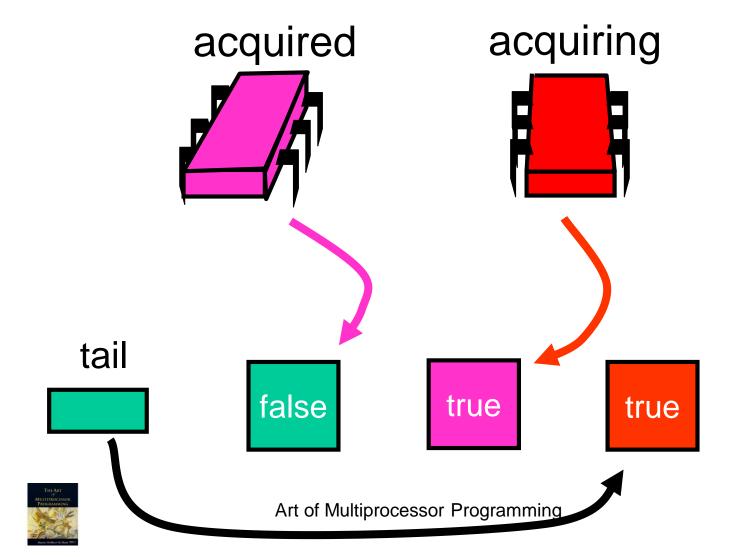


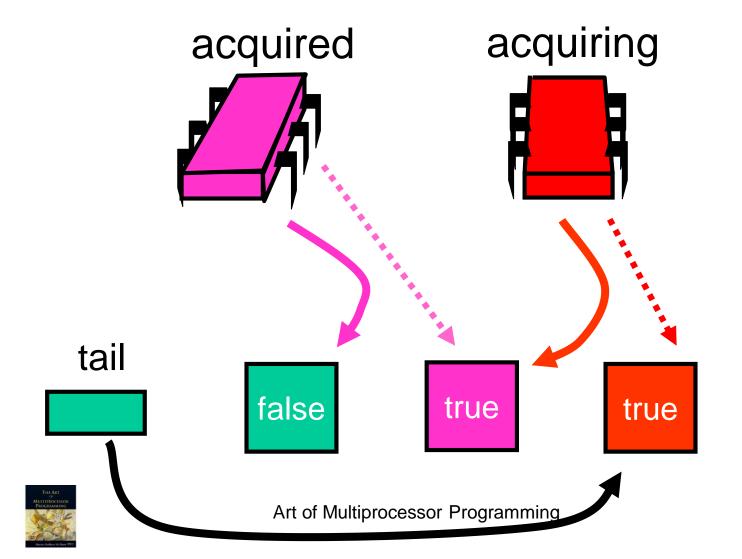
Purple Has the Lock

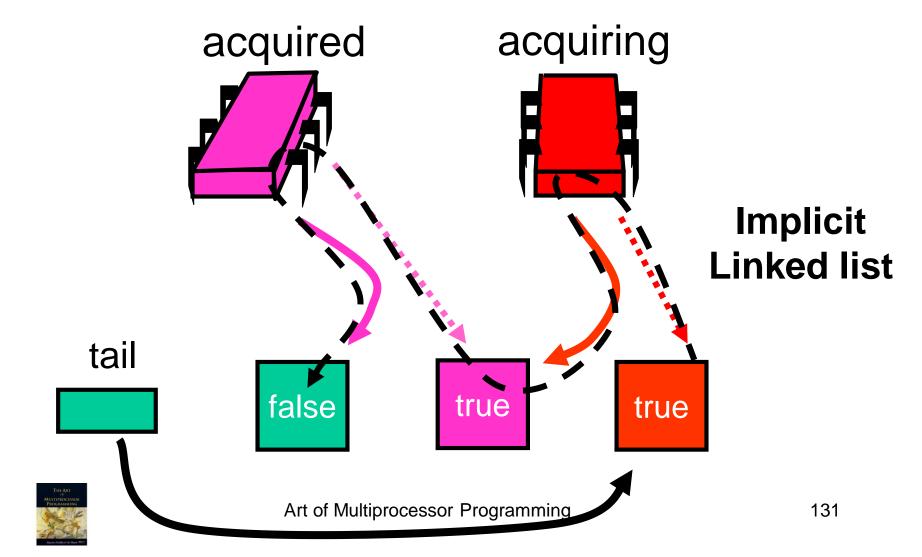


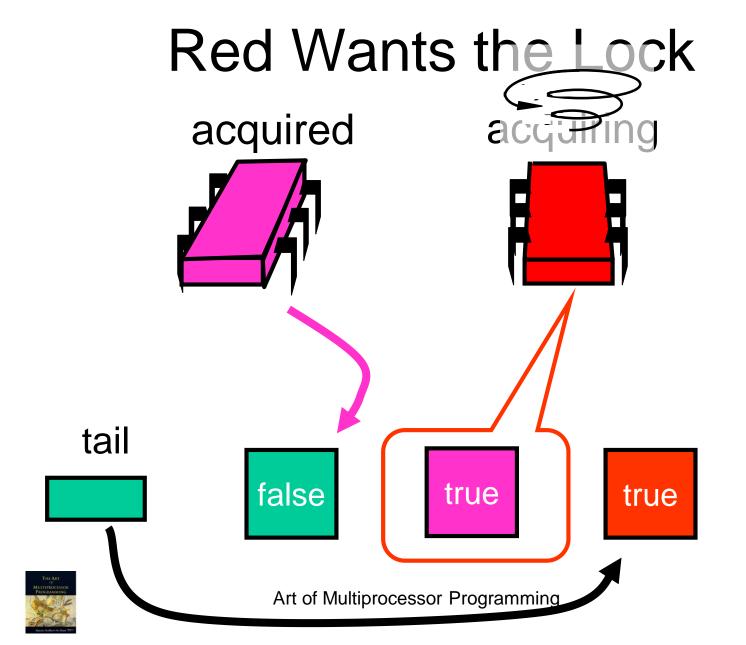


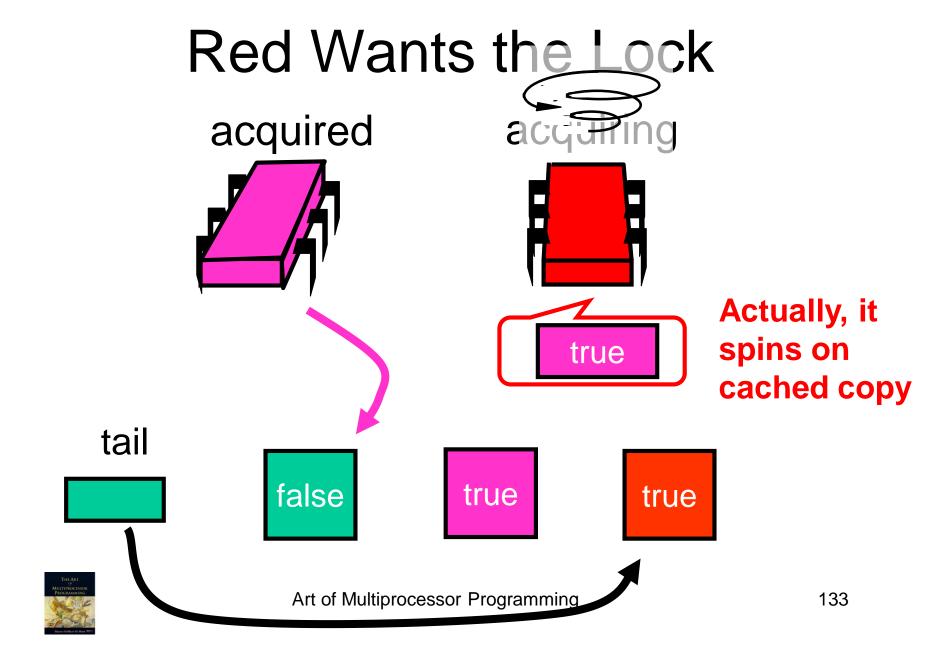




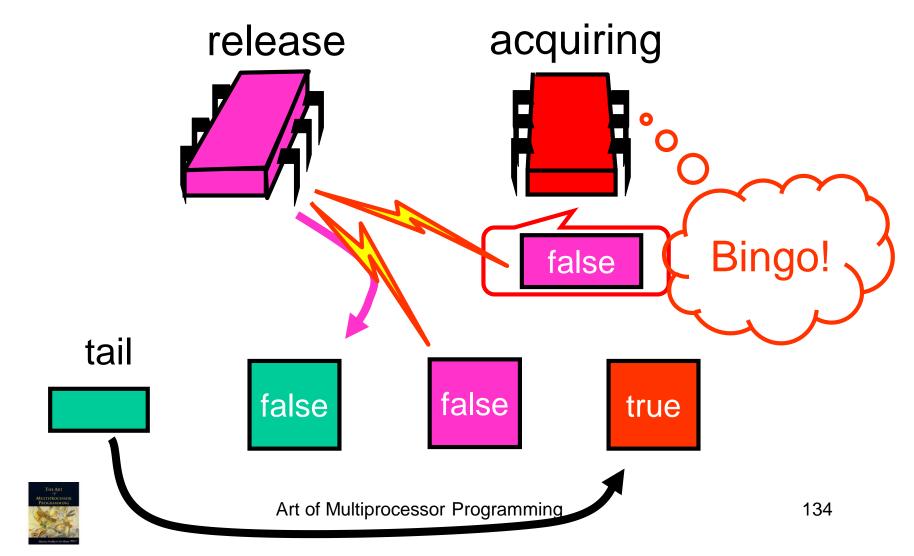








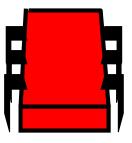
Purple Releases

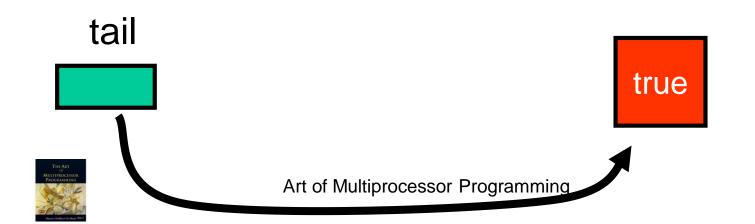


Purple Releases

released







Space Usage

- Let
 - L = number of locks
 - -N = number of threads
- ALock
 - -O(LN)
- CLH lock
 - -O(L+N)



```
class QNode {
  AtomicBoolean locked =
   new AtomicBoolean(true);
}
```



```
class QNode {
   AtomicBoolean locked =
   new AtomicBoolean(true);
}
```

Not released yet



```
class CLHLock implements Lock {
AtomicReference<QNode> tail;
 ThreadLocal<QNode> myNode
    = new QNode();
public void lock() {
  QNode pred
    = tail.getAndSet(myNode);
 while (pred.locked) {}
 } }
```



```
class CLHLock implements Lock {
AtomicReference<QNode> tail;
ThreadLocal<QNode> myNode
    = new QNode();
public void lock()
  QNode pred
    = tail.getAndSet(myNode);
 while (pred.locked) {}
 } }
                         Queue tail
```



```
class CLHLock implements Lock {
AtomicReference<QNode> tail;
 ThreadLocal<QNode> myNode
    = new QNode();
public void lock(
  QNode pred
    = tail.getAndSet(myNode);
 while (pred.locked) {
 } }
                       Thread-local QNode
```



```
class CLHLock implements Lock {
AtomicReference<QNode> tail;
 ThreadLocal<QNode> myNode
                           Swap in my node
    = new QNode();
public void lock()
  QNode pred
    = tail.getAndSet(myNode);
 while (pred.locked) {}
 } }
```



```
class CLHLock implements Lock {
AtomicReference<QNode> tail;
 ThreadLocal<QNode> myNode
                        Spin until predecessor
    = new QNode();
                            releases lock
public void lock() {
  QNode pred
    = tail.getAndSet(myNode);
 while (pred.locked) {}
```



```
Class CLHLock implements Lock {
    ...
    public void unlock() {
        myNode.locked.set(false);
        myNode = pred;
    }
}
```



```
Class CLHLock implements Lock {
public void unlock() {
 myNode.locked.set(false)
 myNode = pred;
                    Notify successor
```



CLH Queue Lock

```
Class CLHLock implements Lock {
    ...
    public void unlock() {
        myNode.locked.set(false);
        myNode = pred;
    }
}
```

Recycle predecessor's node



CLH Queue Lock

```
Class CLHLock implements Lock {
    ...
    public void unlock() {
        myNode.locked.set(false);
        myNode = pred;
    }
}
```

(we don't actually reuse myNode. Code in book shows how it's done.)



CLH Lock

- Good
 - Lock release affects predecessor only
 - Small, constant-sized space
- Bad
 - Doesn't work for uncached NUMA architectures

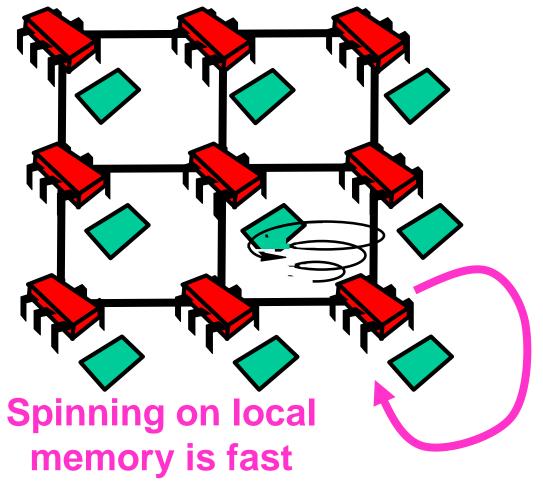


NUMA and cc-NUMA Architectures

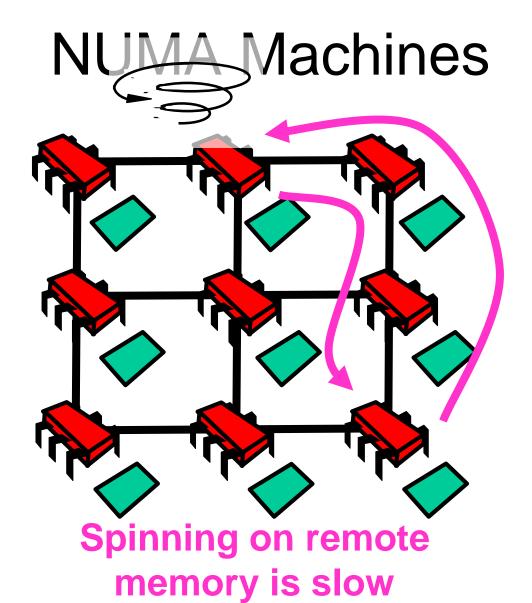
- Acronym:
 - Non-Uniform Memory Architecture
 - ccNUMA = cache coherent NUMA
- Illusion:
 - Flat shared memory
- Truth:
 - No caches (sometimes)
 - Some memory regions faster than others



NUMA Machines









CLH Lock

- Each thread spins on predecessor's memory
- Could be far away

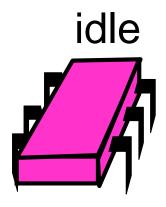


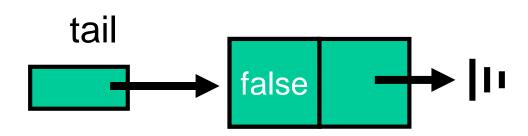
MCS Lock

- FCFS order
- Spin on local memory only
- Small, Constant-size overhead

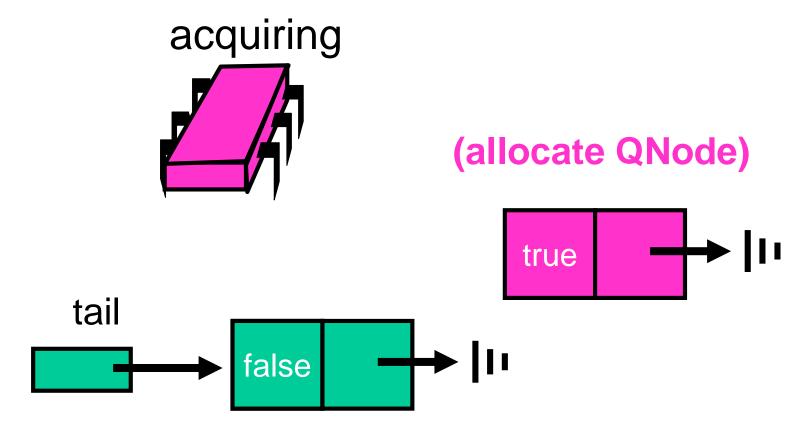


Initially

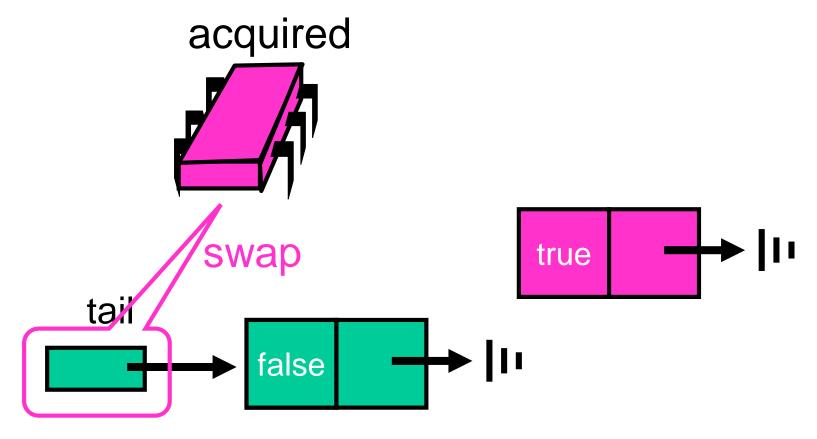




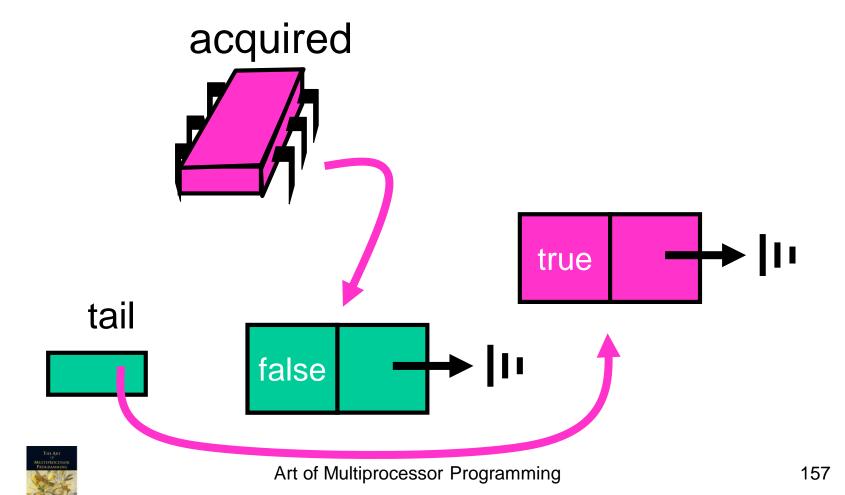




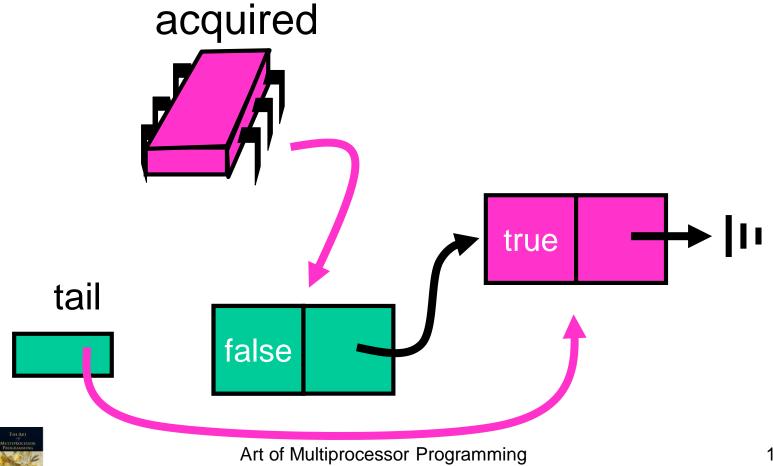


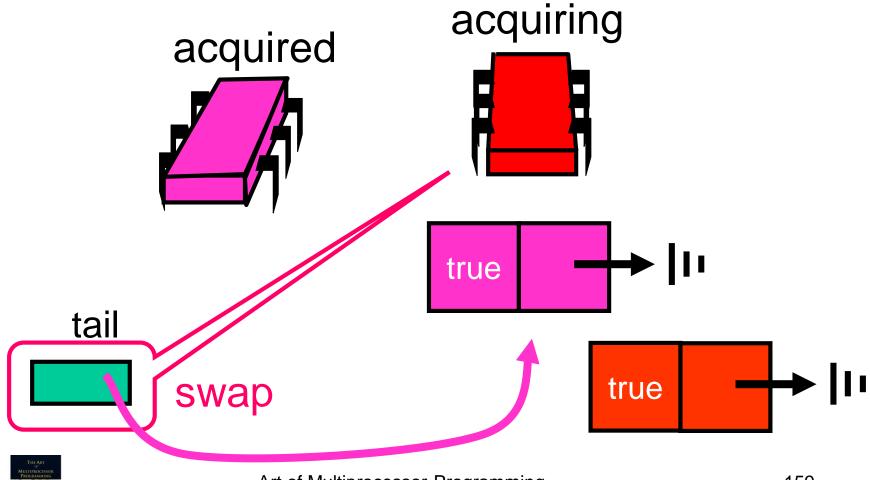


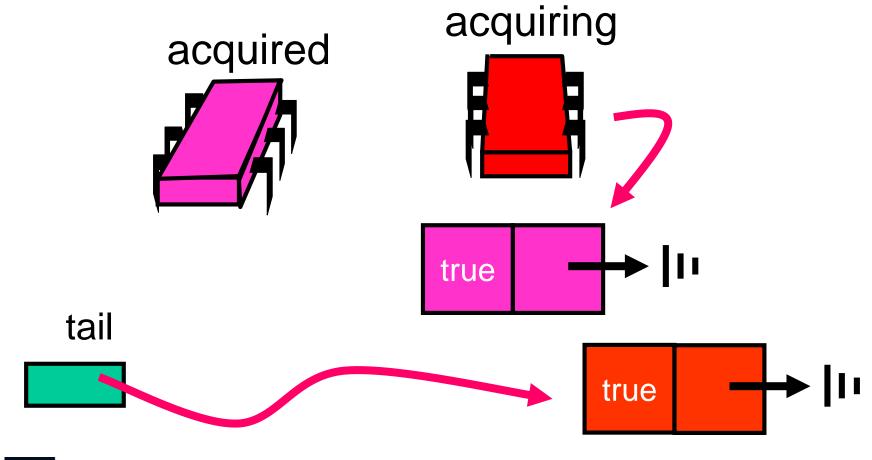




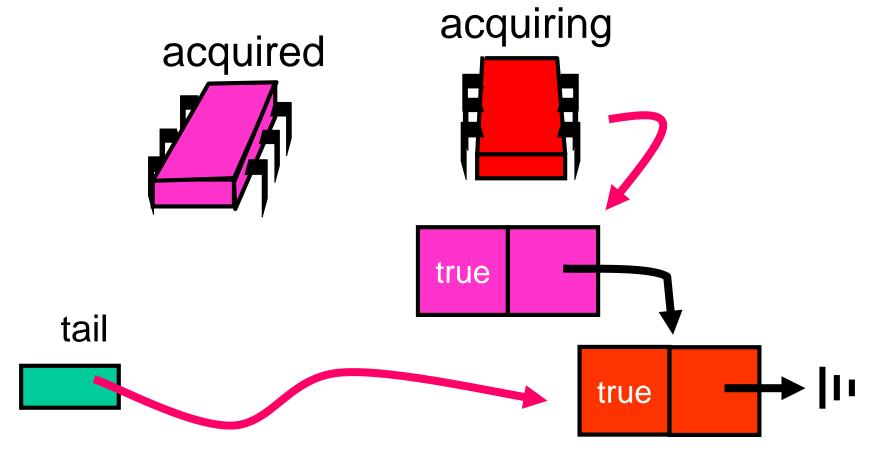
Acquired



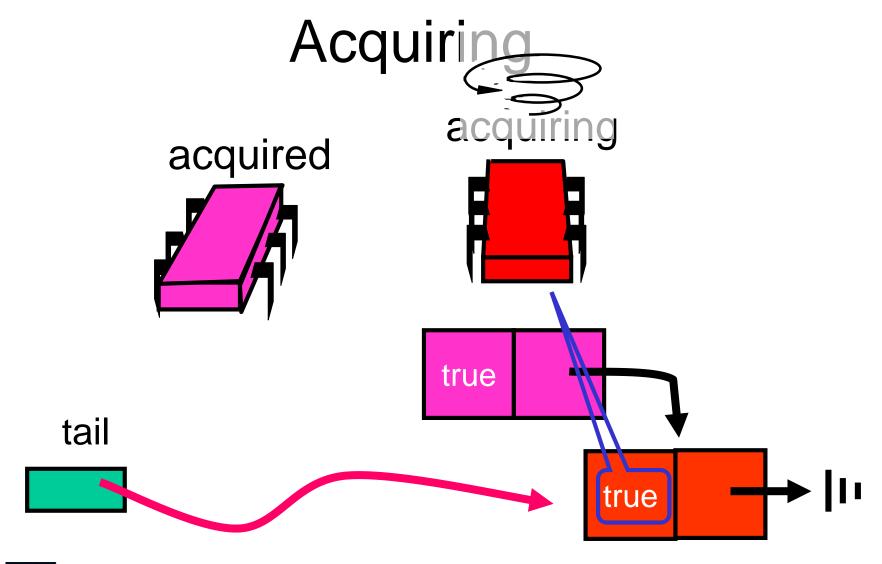




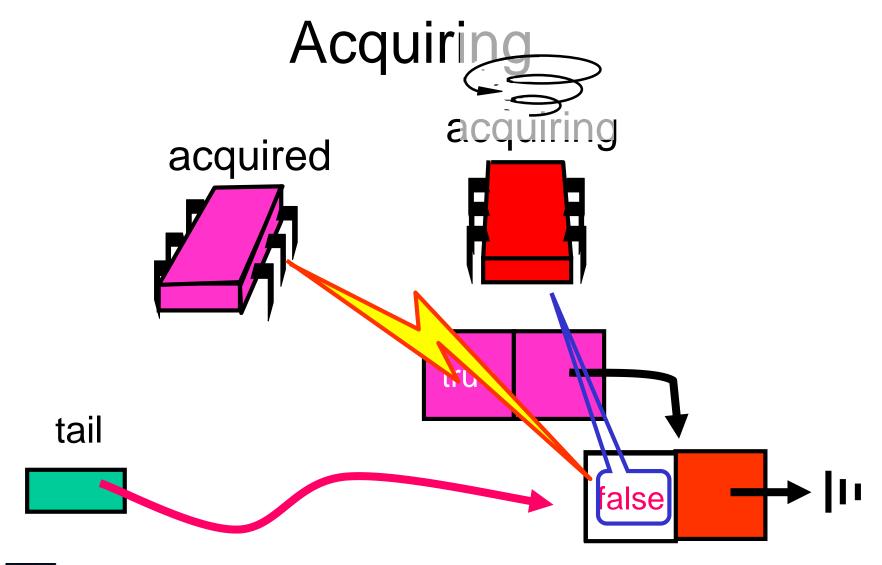




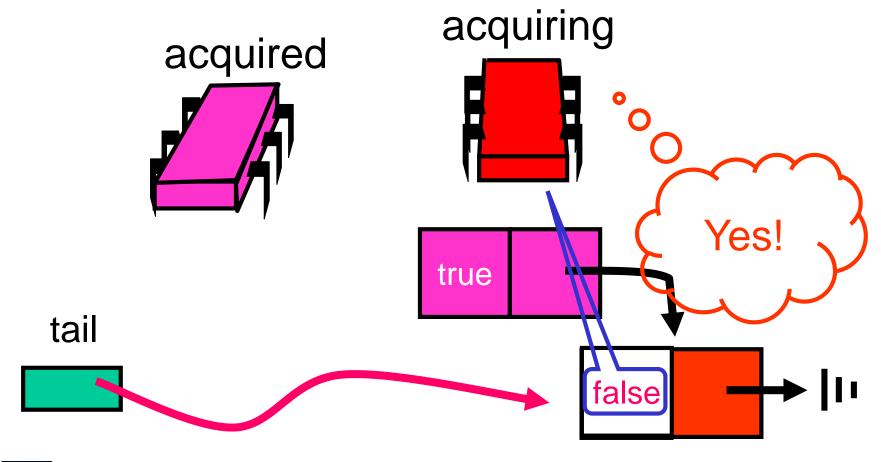














```
class QNode {
  volatile boolean locked = false;
  volatile qnode    next = null;
}
```



```
class MCSLock implements Lock {
AtomicReference tail;
public void lock() {
  QNode qnode = new QNode();
  QNode pred = tail.getAndSet(qnode);
  if (pred != null) {
   qnode.locked = true;
  pred.next = qnode;
  while (qnode.locked) {}
  }}}
```



```
class MCSLock implements Lock {
                                  Make a
AtomicReference tail;
                                  QNode
public void lock()
 QNode qnode = new QNode();
  QNode pred = tail.getAndSet(qnode);
  if (pred != null) {
   qnode.locked = true;
  pred.next = qnode;
  while (qnode.locked) {}
```



```
class MCSLock implements Lock {
AtomicReference tail;
public void lock() {
  QNode qnode = new QNode();
  QNode pred = tail.getAndSet(qnode);
 if (pred != null) {
  qnode.locked = true; add my Node to
  pred.next = qnode;
                          the tail of
  while (qnode.locked) {}
```

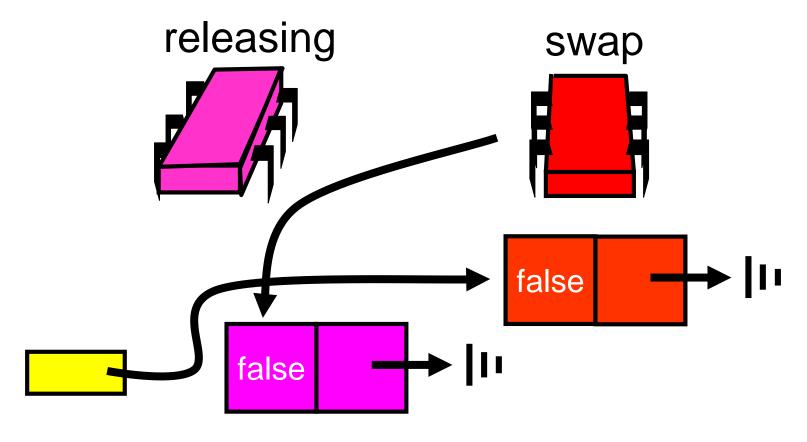


```
class MCSLock implements Lock
                          Fix if queue was
AtomicReference tail;
                             non-empty
public void lock() {
  QNode qnode = new
 QNode pred = tail.getAndSet(qnode);
  if (pred != null)
   qnode.locked = true;
   pred.next = qnode;
   while (qnode.locked) {}
  } } }
```

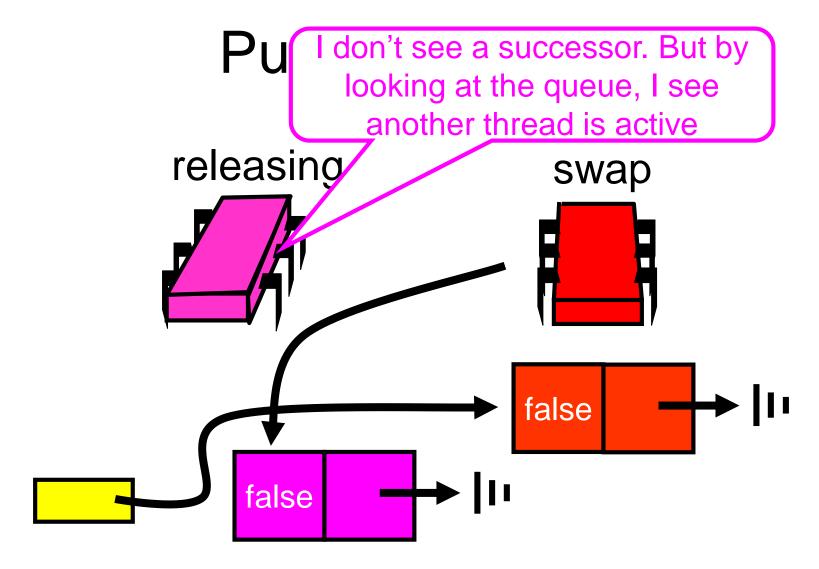


```
class MCSLock implements Lock {
                           Wait until
AtomicReference tail;
public void lock() {
                           unlocked
  QNode qnode = new QNode();
 QNode pred = tail.getAndSet(qnode);
  if (pred != null) {
   qnode.locked = true;
  pred.next = qnode;
  while (qnode.locked) {}
  } } }
```

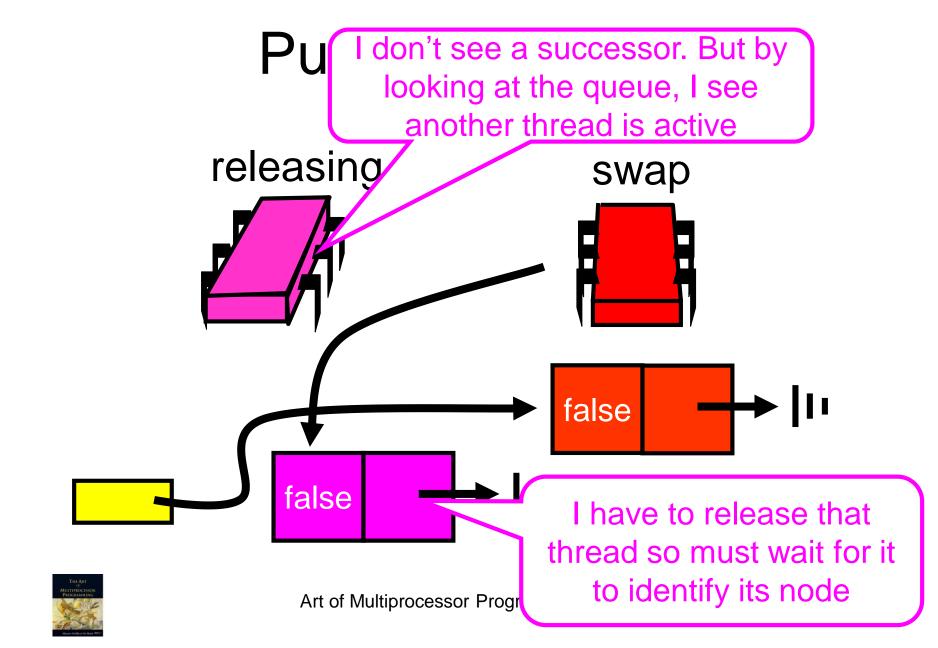


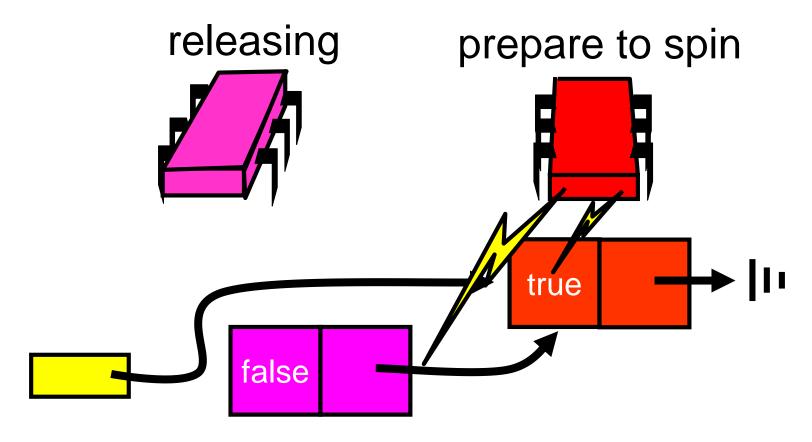




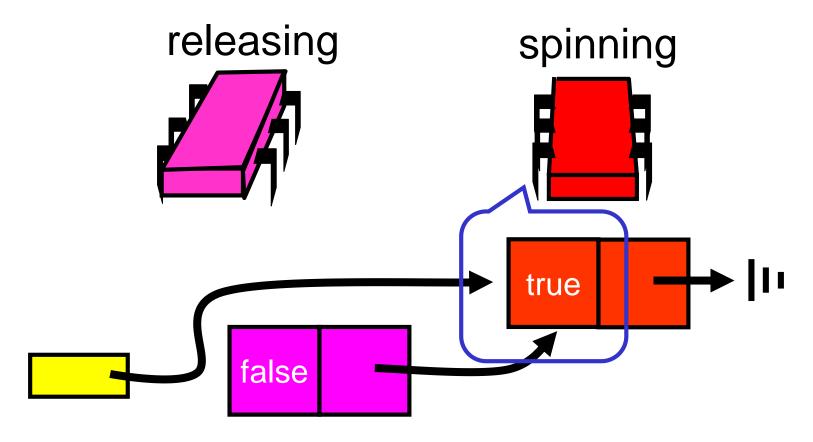




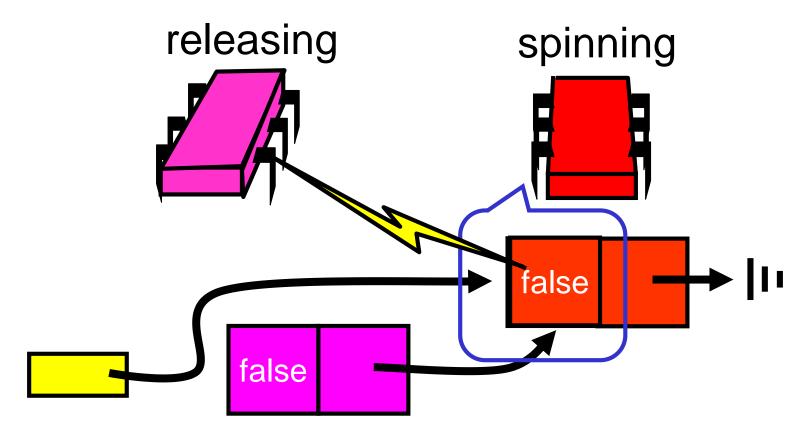




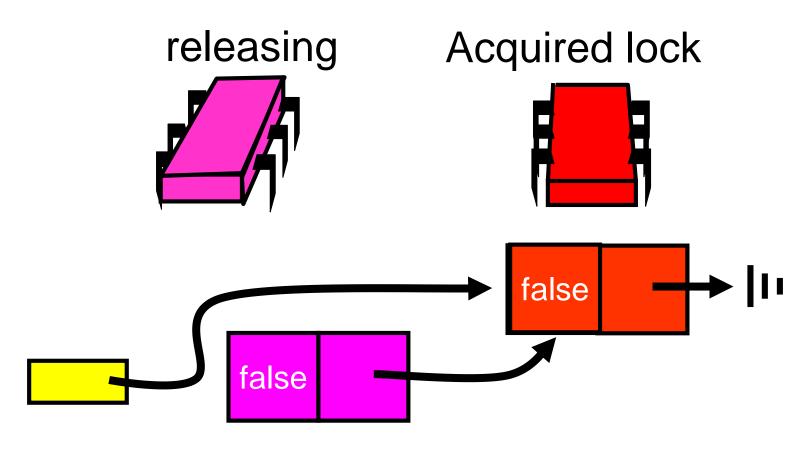














MCS Queue Unlock

```
class MCSLock implements Lock {
AtomicReference tail;
public void unlock() {
  if (qnode.next == null) {
   if (tail.CAS(qnode, null)
    return;
   while (qnode.next == null) {}
 qnode.next.locked = false;
} }
```



```
class MCSLock implements Lock {
AtomicReference tail;
public void unlock() {
  if (qnode.next == null) {
   if (tail.CAS(qnode,
    return;
   while (qnode.next == nu
                             Missing
 qnode.next.locked = false;
                            successor
```



```
:k {
 If really no successor,
          return
 if (qnode.next ==
  if (tail.CAS(qnode, null)
   return;
  while (qnode.next == null) {}
qnode.next.locked = false;
```



```
:k {
   Otherwise wait for
 successor to catch up
if (qnode.next == null) {
 if (tail.CAS (qnode,
   return;
 while (qnode.next == null) {}
qnode.next.locked = false;
```



MCS Queue Lock

```
class MCSLock implements Lock {
AtomicRefe
public vo: Pass lock to successor
 if (qnode.next == null) {
  if (tail.CAS(qnode, null)
    return;
  while (qnode.next == null) {}
qnode.next.locked = false;
```



Abortable Locks

- What if you want to give up waiting for a lock?
- For example
 - Timeout
 - Database transaction aborted by user



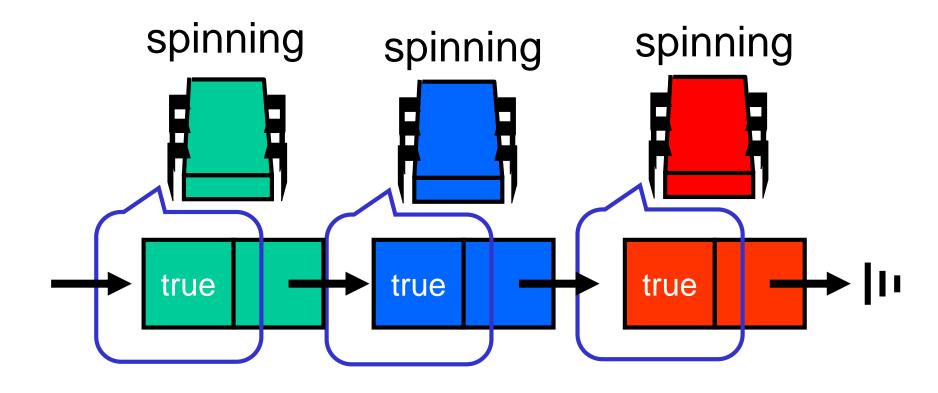
Back-off Lock

- Aborting is trivial
 - Just return from lock() call
- Extra benefit:
 - No cleaning up
 - Wait-free
 - Immediate return

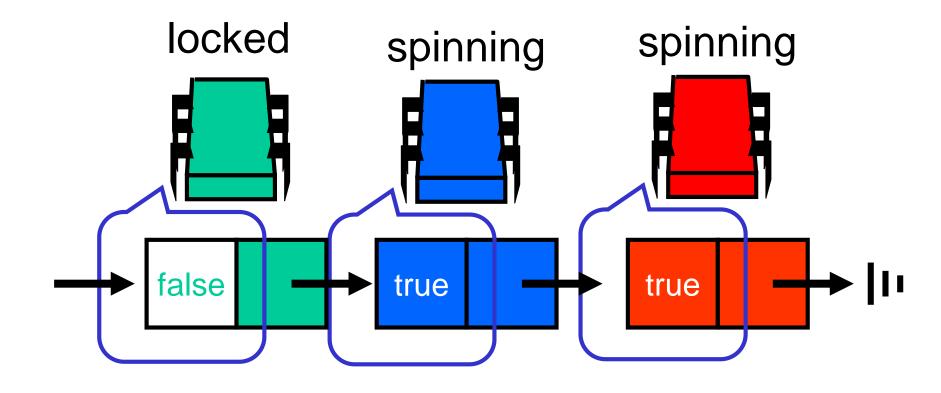


- Can't just quit
 - Thread in line behind will starve
- Need a graceful way out

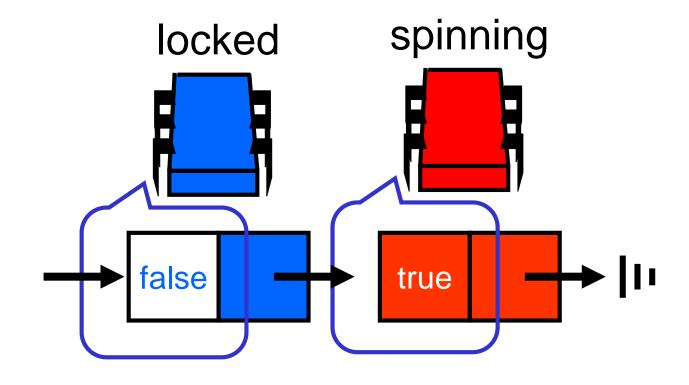




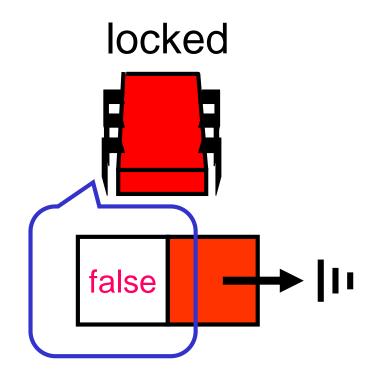




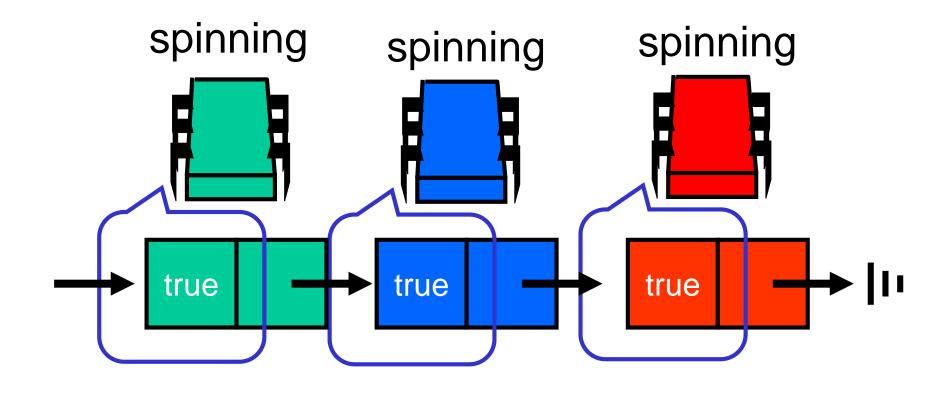




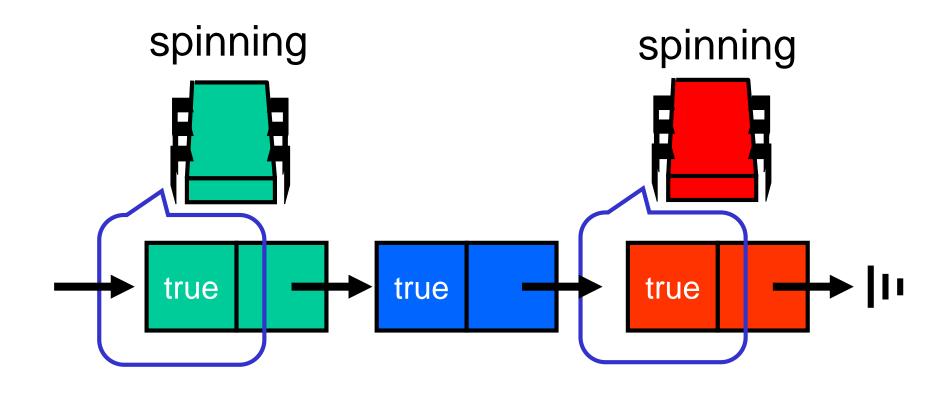




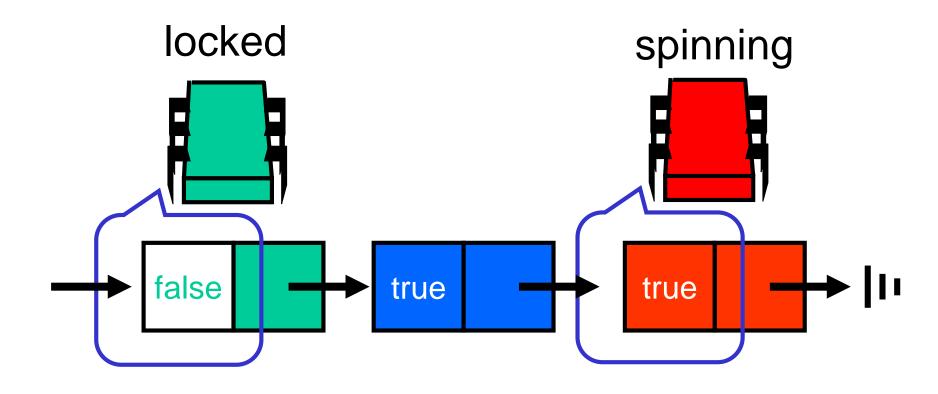




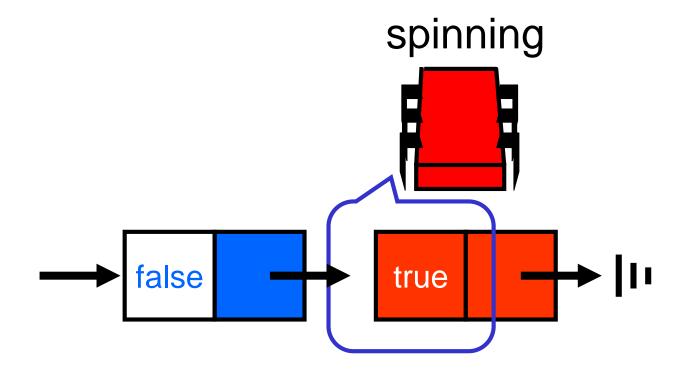




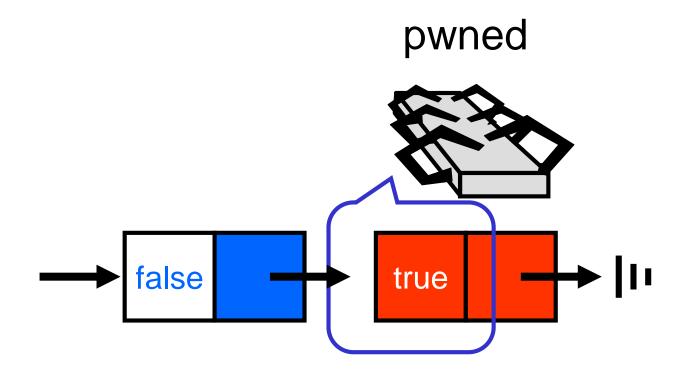












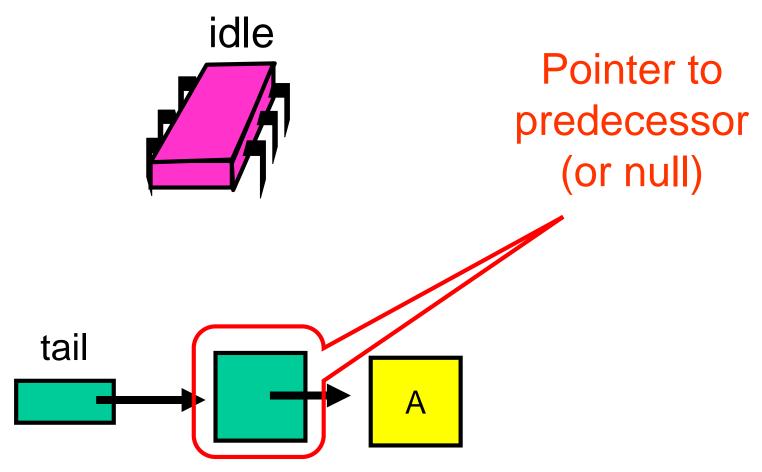


Abortable CLH Lock

- When a thread gives up
 - Removing node in a wait-free way is hard
- Idea:
 - let successor deal with it.

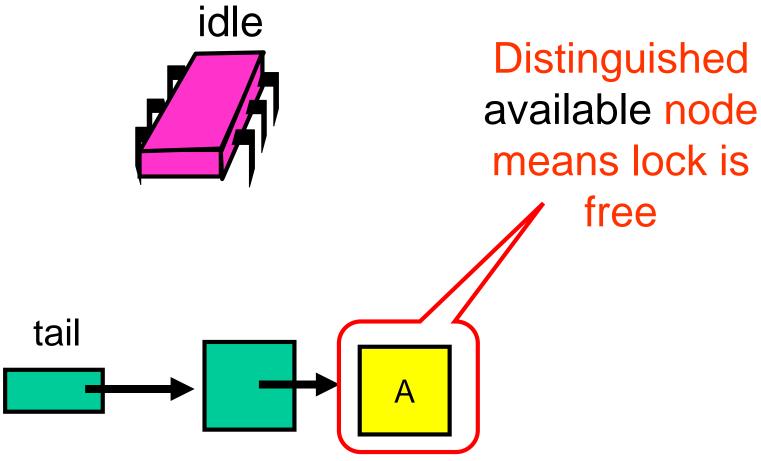


Initially



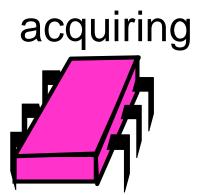


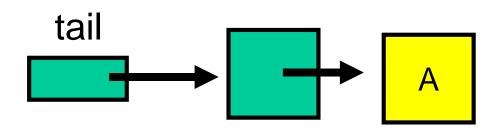
Initially



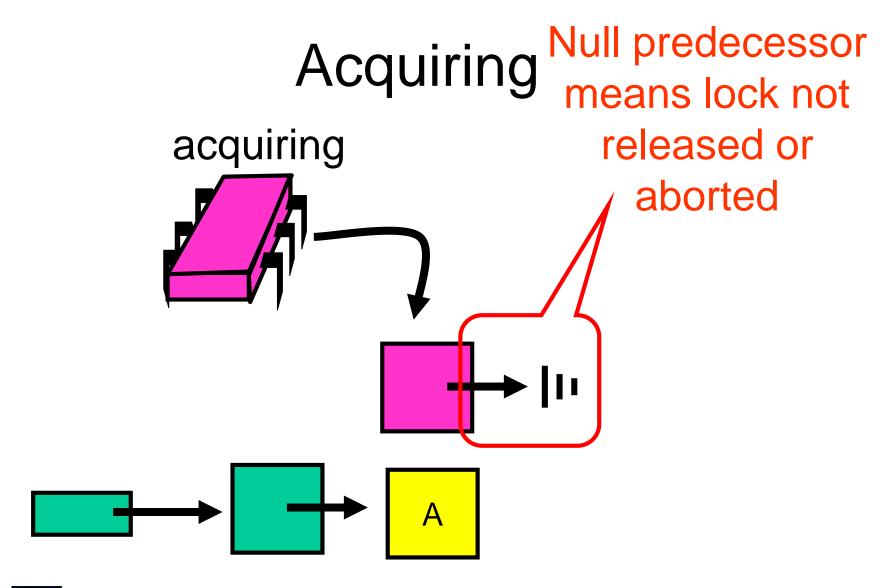


Acquiring



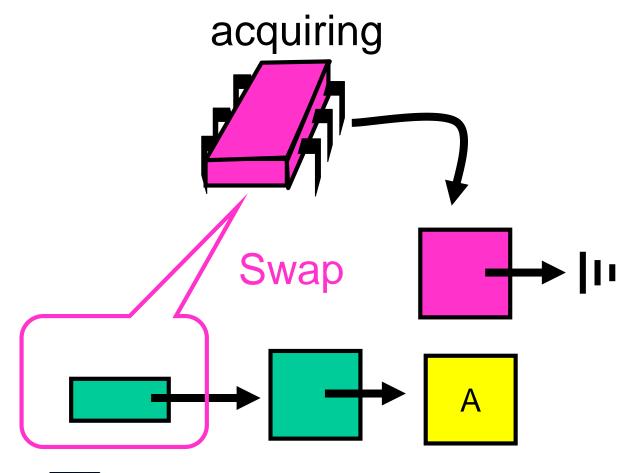






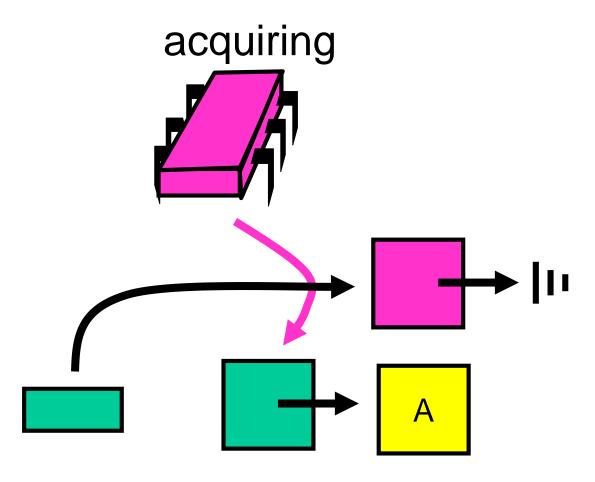


Acquiring

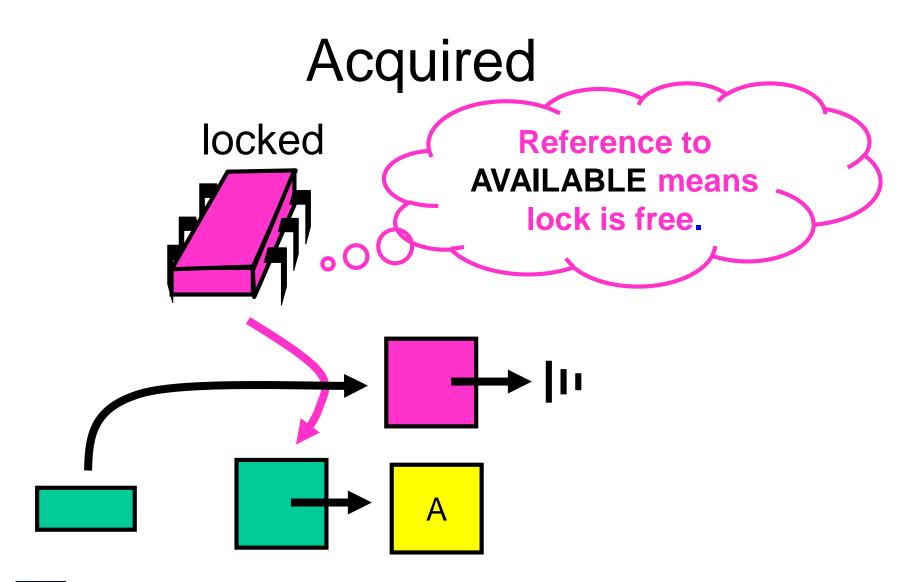




Acquiring

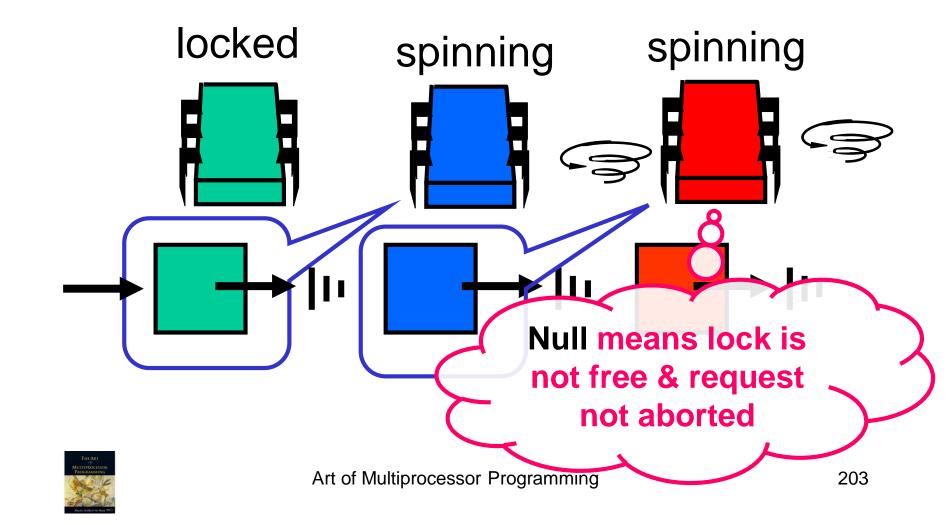




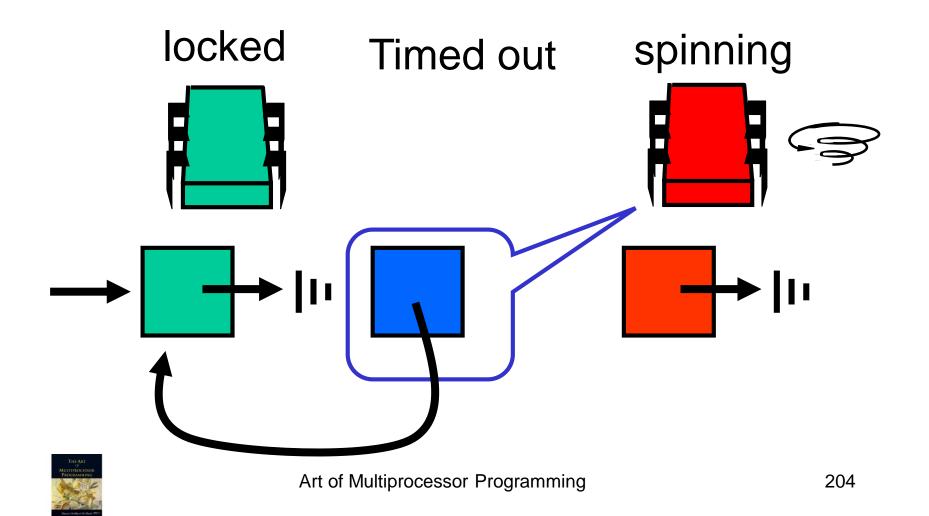




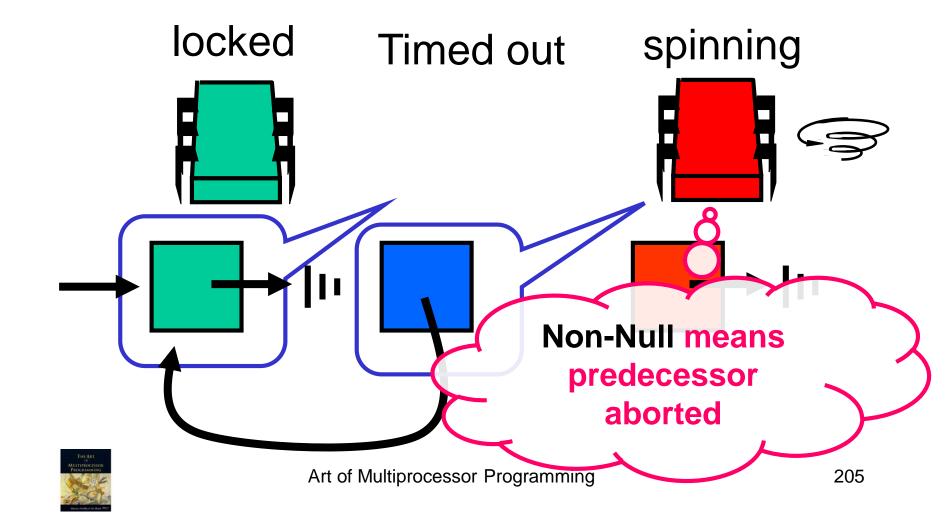
Normal Case



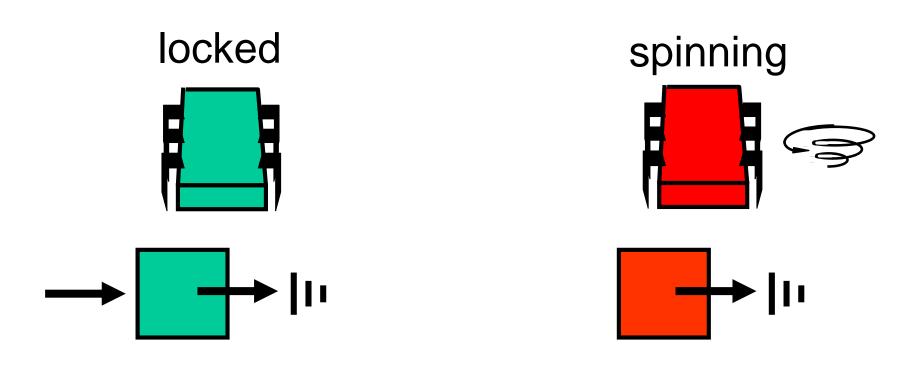
One Thread Aborts



Successor Notices

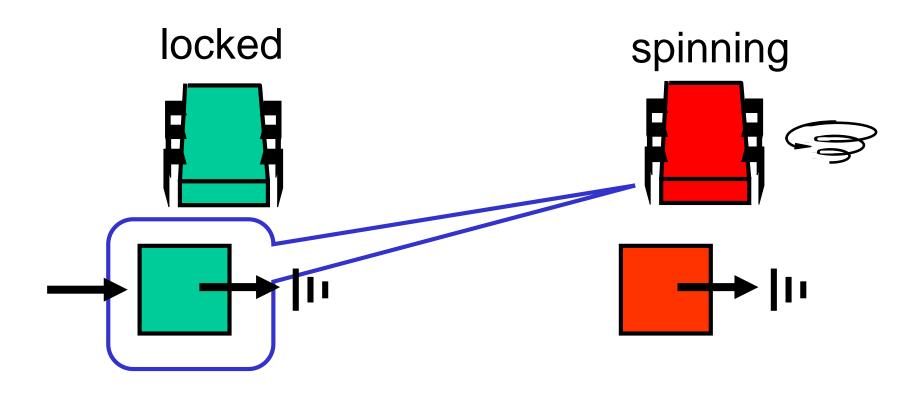


Recycle Predecessor's Node



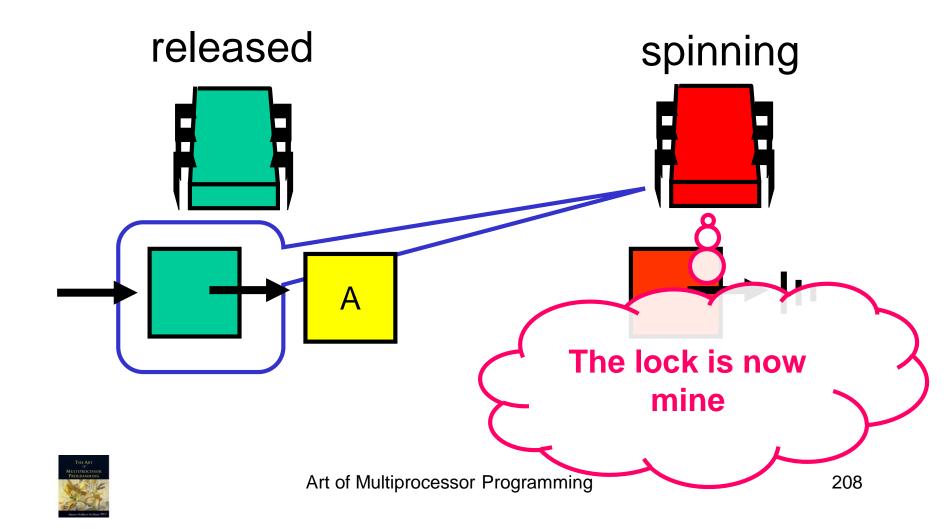


Spin on Earlier Node





Spin on Earlier Node



```
public class TOLock implements Lock {
   static QNode AVAILABLE
   = new QNode();
   AtomicReference<QNode> tail;
   ThreadLocal<QNode> myNode;
```



```
public class TOLock implements Lock {
    static QNode AVAILABLE
    = new QNode();
    AtomicReference<QNode> tail;
    ThreadLocal<QNode> myNode;
```

AVAILABLE node signifies free lock



```
public class TOLock implements Lock {
  static QNode AVAILABLE
    = new QNode();
  AtomicReference<QNode> tail;
  ThreadLocal<QNode> myNode;
      Tail of the queue
```



```
public class TOLock implements Lock {
  static QNode AVAILABLE
    = new QNode();
  AtomicReference<QNode> tail;
  ThreadLocal<QNode> myNode;
```

Remember my node ...



```
public boolean lock(long timeout) {
  QNode qnode = new QNode();
  myNode.set(qnode);
  qnode.prev = null;
  QNode myPred = tail.getAndSet(qnode);
  if (myPred== null
      || myPred.prev == AVAILABLE) {
      return true;
```



```
public boolean lock(long timeout) {
  QNode qnode = new QNode();
 myNode.set(qnode);
  qnode.prev = null;
  QNode myPred = tail.getAndSet(qnode);
  if (myPred == null
      || myPred.prev == AVAILABLE) {
      return true;
```

Create & initialize node



```
public boolean lock(long timeout) {
  QNode qnode = new QNode();
  myNode.set(qnode);
  QNode myPred = tail.getAndSet(qnode);
  if (myPred == null
                        AVAILABLE) {
      || myPred.prev ==
      return true;
```

Swap with tail



```
public boolean lock(long timeout) {
  QNode qnode = new QNode();
  myNode.set(qnode);
  qnode.prev = null;
  ONode myPred = tail getAndSet(gnode);
  if (myPred == null
      || myPred.prev == AVAILABLE)
      return true;
```

If predecessor absent or released, we are done



```
spinning
                                            spinning
          Time-out Lo
long start = now();
while (now() - start < timeout) {</pre>
  QNode predPred = myPred.prev;
  if (predPred == AVAILABLE) {
    return true;
  } else if (predPred != null) {
    myPred = predPred;
```



```
long start = now();
while (now() - start < timeout)</pre>
  QNode predPred = myPred.prev;
  if (predPred == AVAILABLE)
    return true;
  } else if (predPred != null) {
    myPred = predPred;
              Keep trying for a while
```



```
long start = now();
while (now() - start < timeout) {</pre>
 QNode predPred = myPred.prev;
 if (predPred == AVAILABLE)
    return true;
  } else if (predPred != hull) {
   myPred = predPred;
          Spin on predecessor's
                  prev field
```



```
long start = now();
while (now() - start < timeout) {</pre>
  QNode predPred = myPred.prev;
  if (predPred == AVAILABLE)
    return true;
  } else if (predPred != hull) {
    myPred = predPred;
    Predecessor released lock
```



```
long start = now();
while (now() - start < timeout) {</pre>
  QNode predPred = myPred.prev;
  if (predPred == AVAILABLE) {
    return true;
   else if (predPred != null)
   myPred = predPred;
             Predecessor aborted,
                  advance one
```



```
if (!tail.compareAndSet(qnode, myPred))
    qnode.prev = myPred;
    return false;
}
```

What do I do when I time out?



```
if (!tail.compareAndSet(qnode, myPred))
    qnode.prev = myPred;
    return false;
}
```

Do I have a successor?

If CAS fails, I do.

Tell it about myPred



```
if (!tail.compareAndSet(qnode, myPred))
    qnode.prev = myPred;
    return false;
}
```

If CAS succeeds: no successor, simply return false



Time-Out Unlock

```
public void unlock() {
   QNode qnode = myNode.get();
   if (!tail.compareAndSet(qnode, null))
      qnode.prev = AVAILABLE;
}
```



Time-out Unlock

```
public void unlock() {
    QNode qnode = myNode.get();

if (!tail.compareAndSet(qnode, null))
    qnode.prev = AVAILABLE;
}
```

If CAS failed: successor exists, notify it can enter



Timing-out Lock

```
public void unlock() {
   ONode gnode = myNode.get();
   if (!tail.compareAndSet(qnode, null))
      qnode prev = AVAILABLE;
}
```

CAS successful: set tail to null, no clean up since no successor waiting





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