

Fig 1) CIRCUIT DIAGRAM

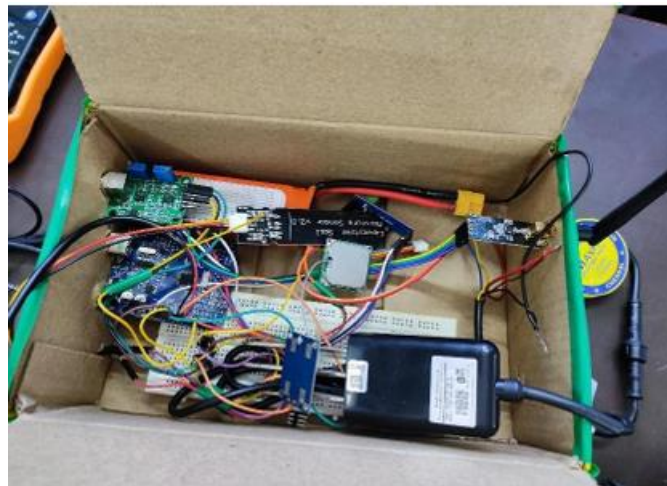


Fig 2) CIRCUIT CONNECTION

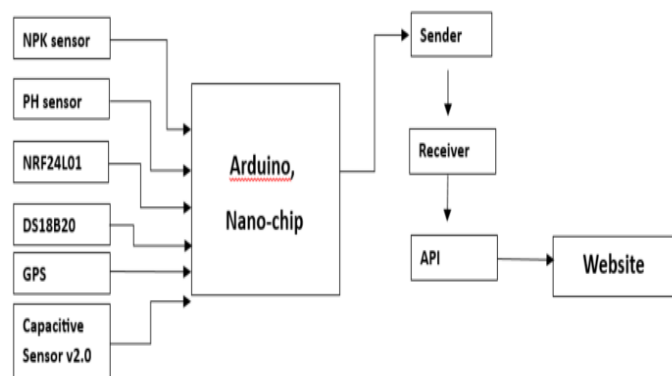


Fig 3) BLOCK DIAGRAM