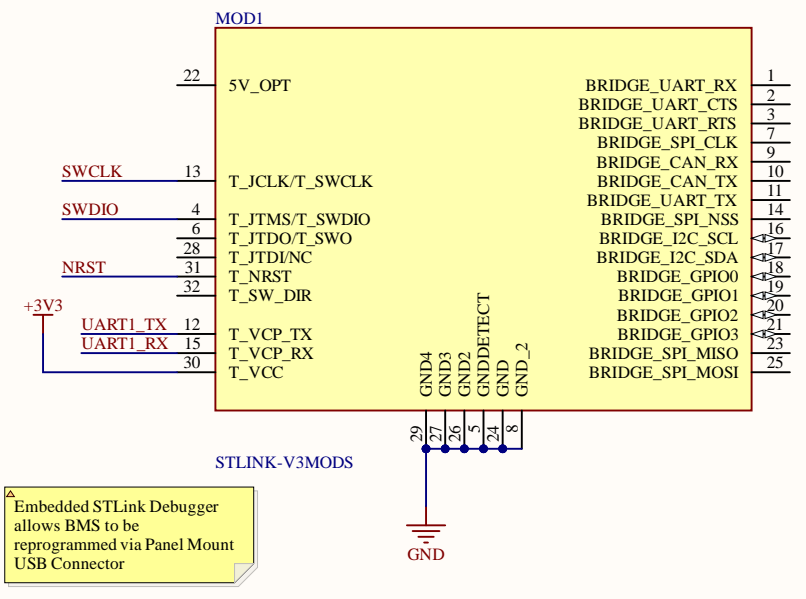
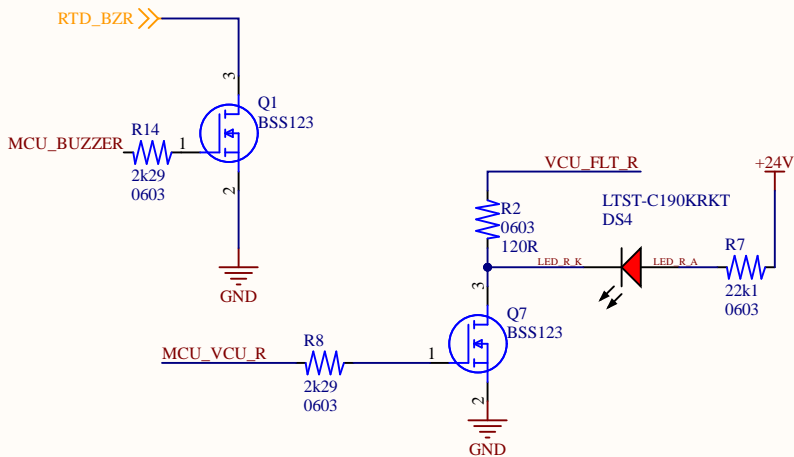


A	1	2	3	4	REVISION		DATE	DRAWN BY	COMMENT	
					AA		5/17/2024	Ethan Zi fzal	Initial Release of Schematic	
B										
C					Notes					

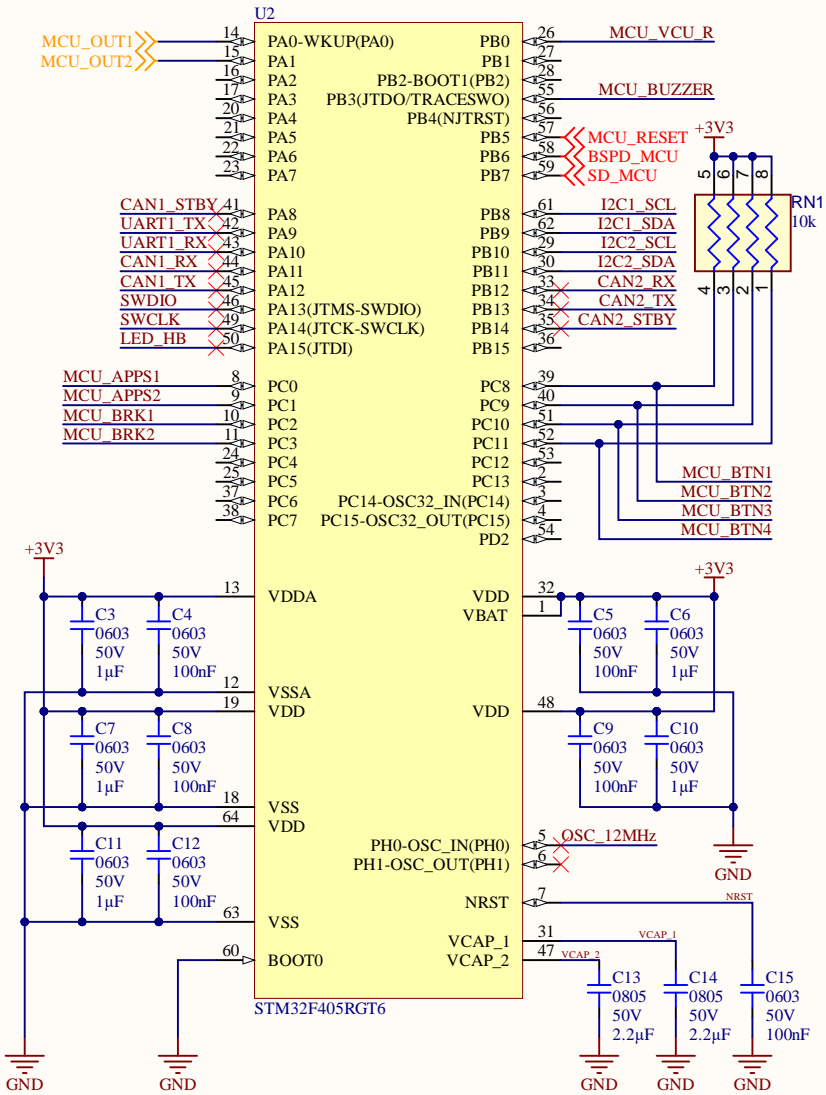
Embedd ST-Link Debugger



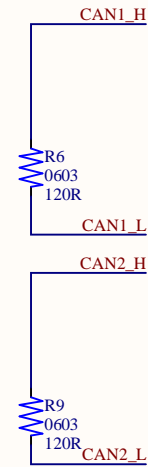
Buzzer and Indicator



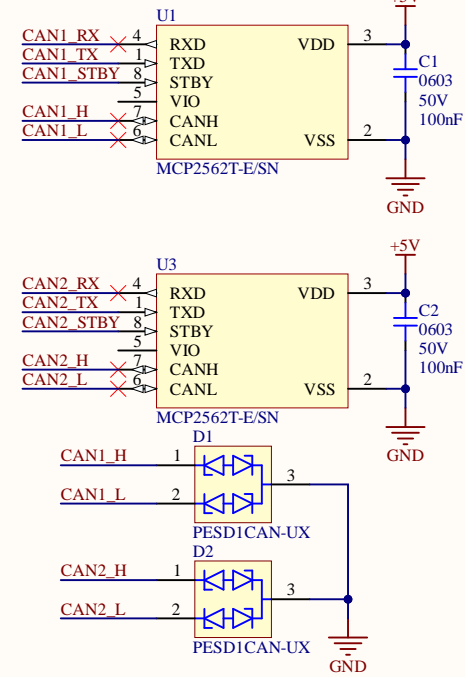
STM32 Microcontroller



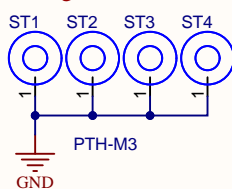
Termination



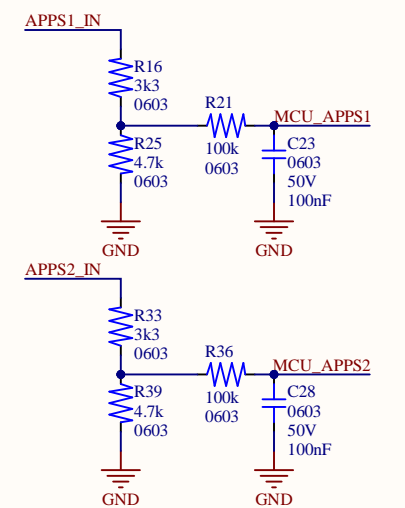
CAN Transcievers



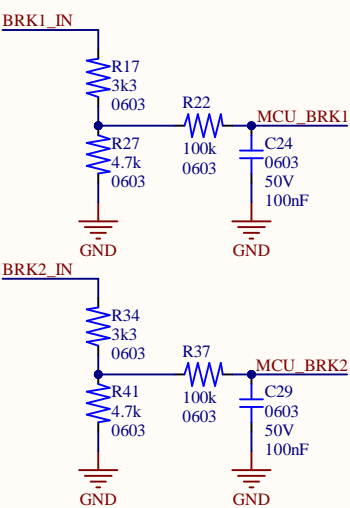
Mounting Holes



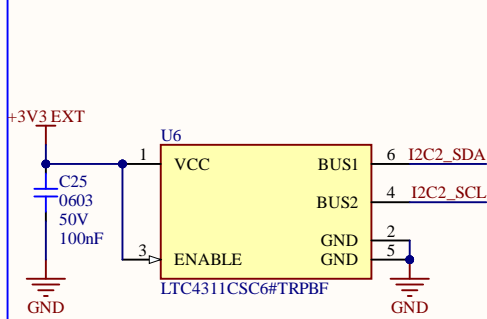
Pedal Inputs



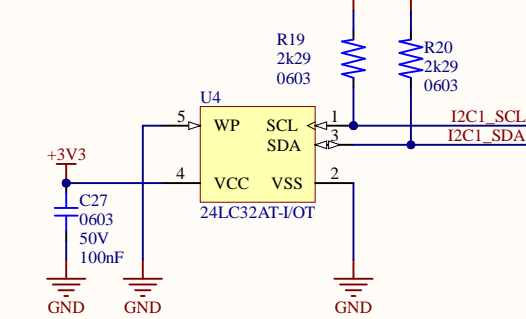
Brake Inputs



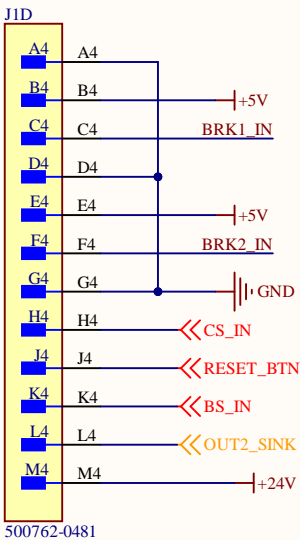
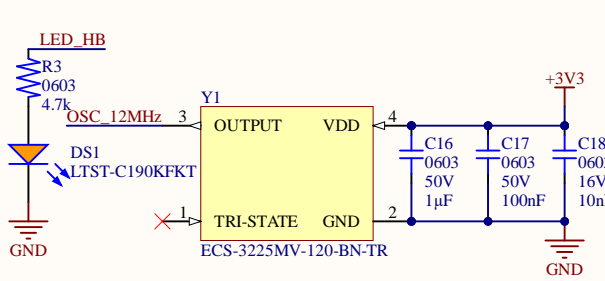
BUS Accelerator




EEPROM

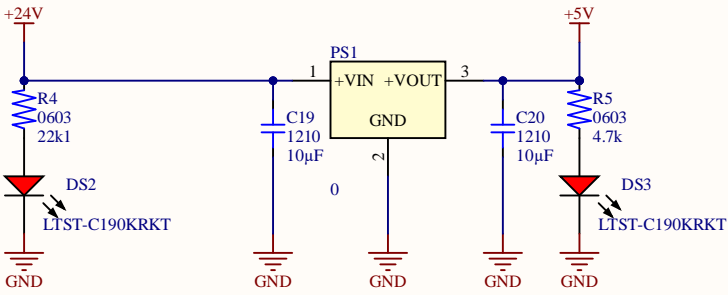


STM Accessory

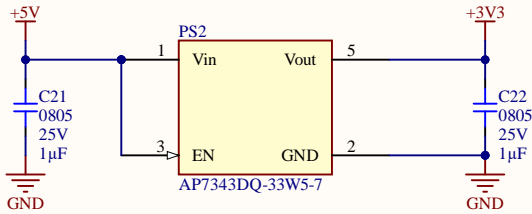


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	SHEET SIZE	TITLE		PART NUMBER	REVISION
	ANSI B	STM32F405 MCU Template		EL#####	AA
		LAST UPDATE	12/6/2025 7:17:07 PM	SHEET 2 of 4	

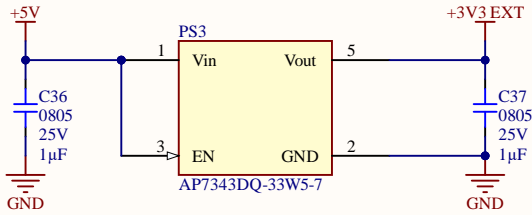
+5V Regulation (8V - 28V) @ 1A



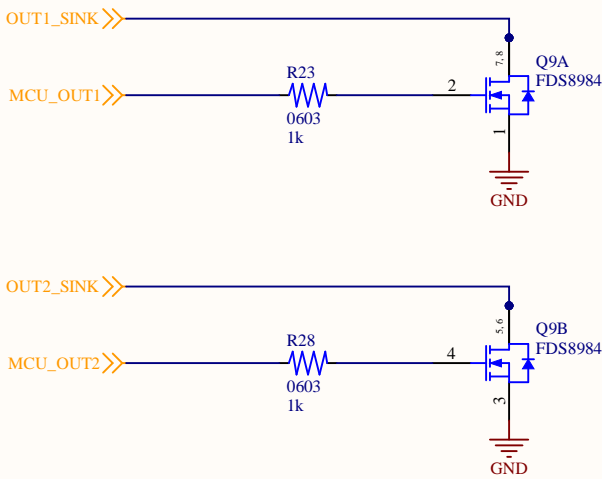
+3V3 Regulation



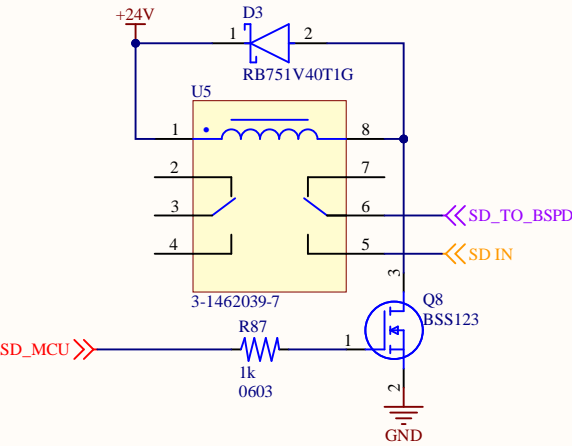
+3V3 External




Power Output Control

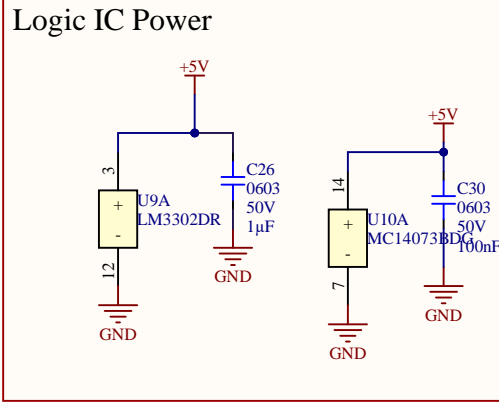
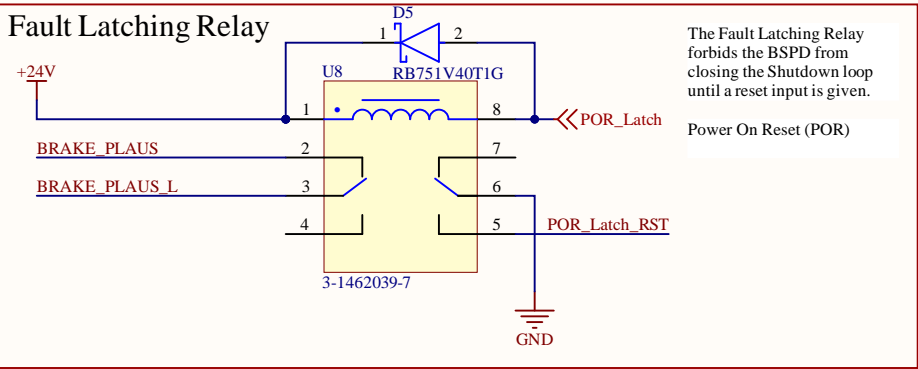
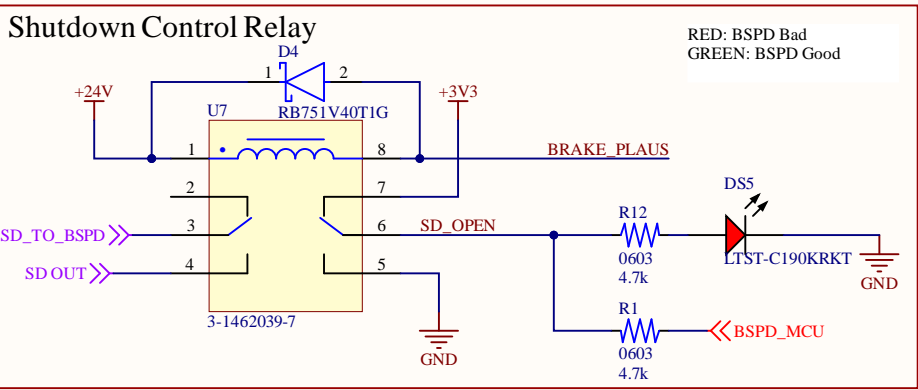
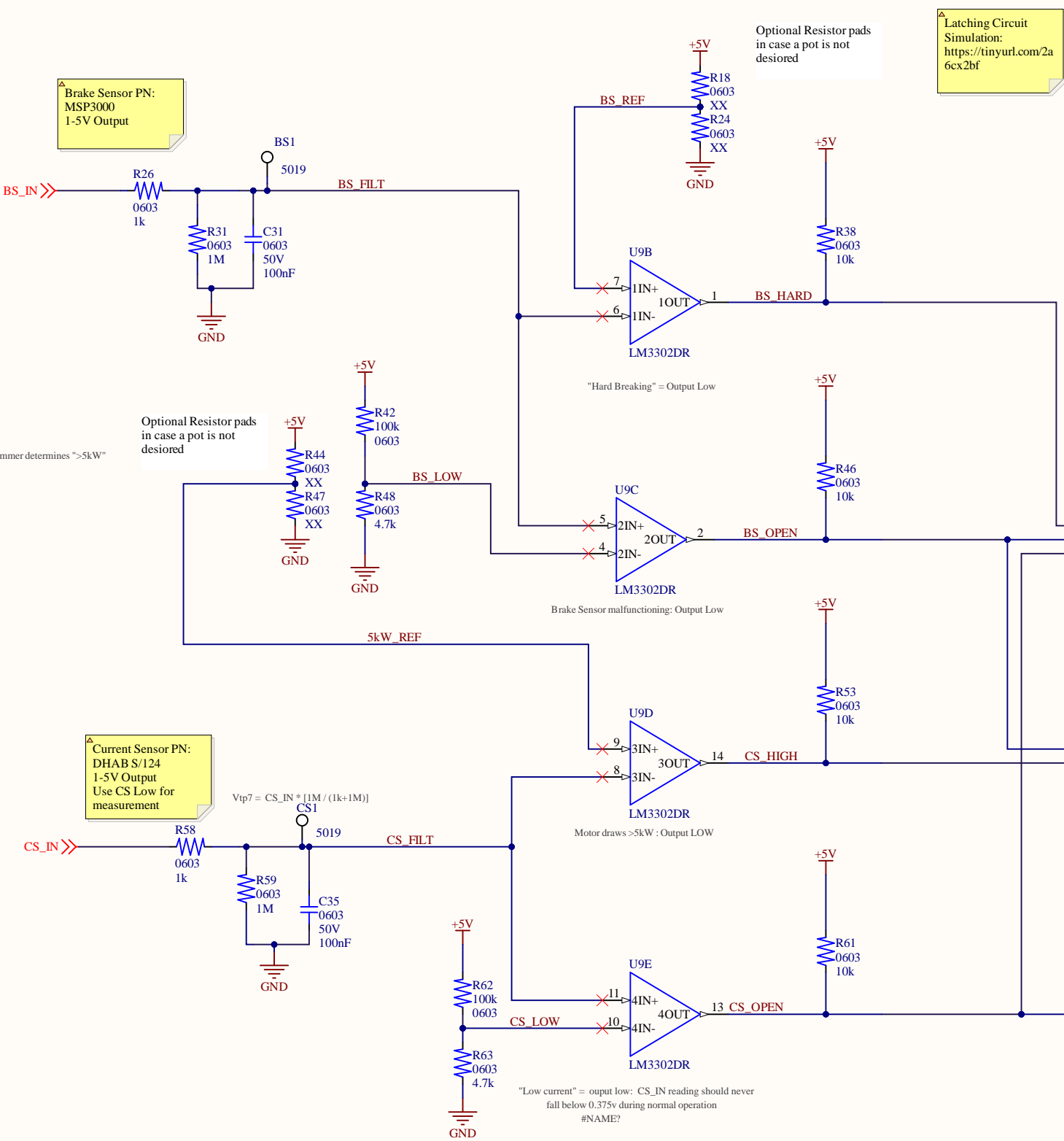


Shutdown Loop Control



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	SHEET SIZE	TITLE	PART NUMBER	REVISION
	ANSI B	STM32F405 MCU Template	EL#####	AA
	LAST UPDATE	12/6/2025 7:17:07 PM	SHEET 3 of 4	

Brake Plausibility Circuit



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ZIPS RACING ELECTRIC THE UNIVERSITY OF AKRON	SHEET SIZE	TITLE	PART NUMBER	REVISION
	ANSI B	STM32F405 MCU Template	EL#####	AA
		LAST UPDATE 12/6/2025 7:17:07 PM	SHEET 4 of 4	