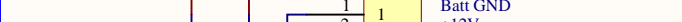


[illegible]





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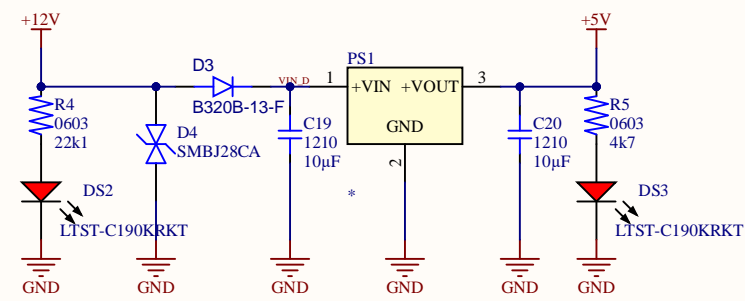




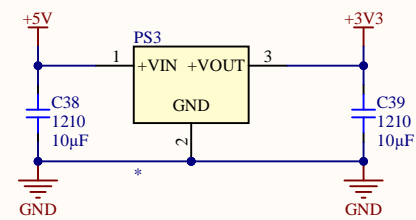





+5V Regulation (8V - 28V) @ 1A

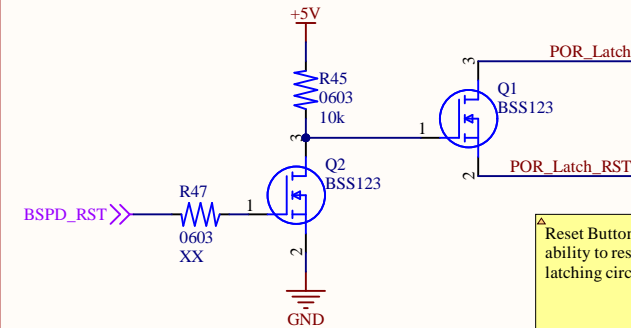


+3V3 Regulation



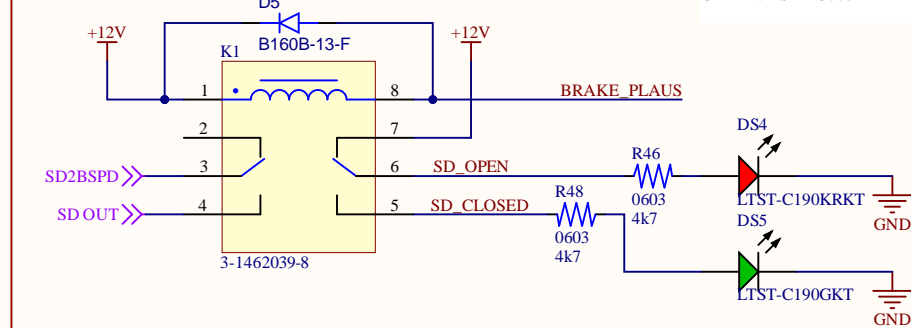
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	SHEET SIZE	TITLE	PART NUMBER	REVISION
	ANSI B	STM32F405 MCU Template	EL#####	AA
	LAST UPDATE	9/19/2024 11:16:04 PM	SHEET 3 of 5	

BSPD Reset

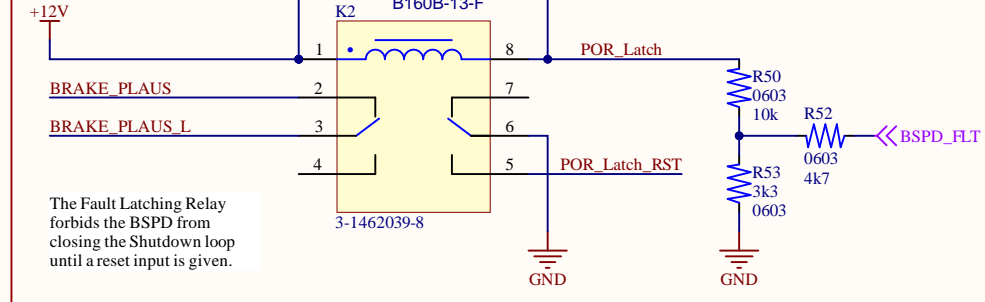


Latching Circuit Simulation:
<https://tinyurl.com/2a6cx2bf>

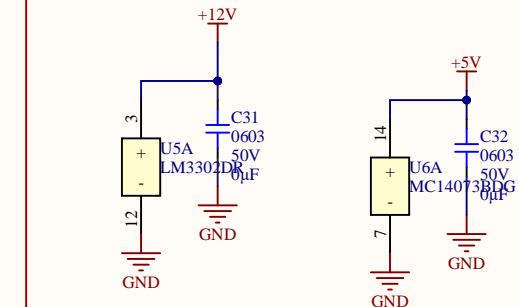
Shutdown Control Relay



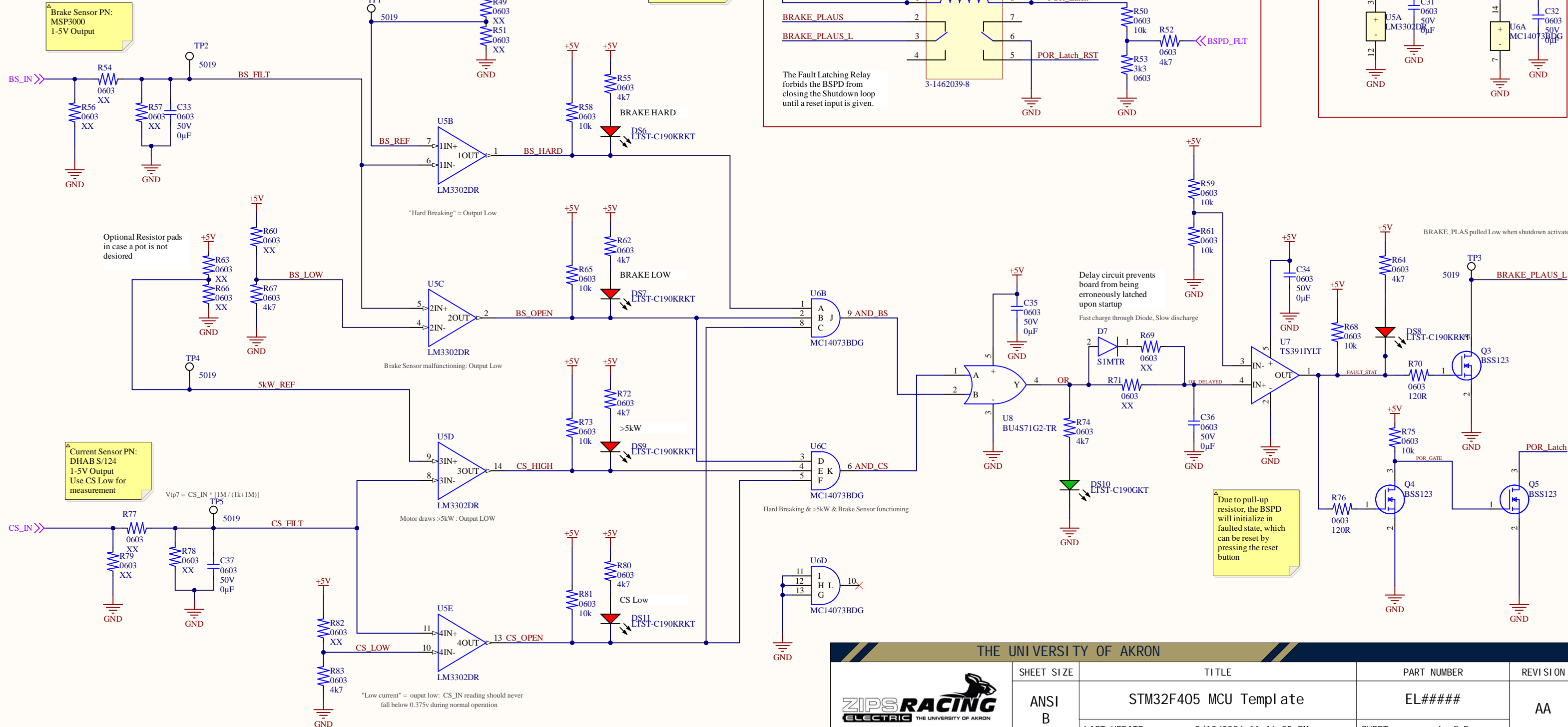
Fault Latching Relay



Logic IC Power



Brake Plausibility Circuit



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