

Simulation Output.

Simulation is run in Xilinx Vivado Xsim and the how, what and where is what is described in this document.

Using a different simulator is possible, it probably will require the same steps as described in this document but most likely other command/scripts/TCL files will be required.

REMARK: The best, most complete and comprehensive simulation of the design is obtained by implementing a minimal version of the design and control it through the VIO while gathering visual, wave forms, information in a ILA.

The result of a simulation run ends up in this directory.

This directory contains the compiled libraries in the */xsim.dir* sub-directory and further contains all files that are result of the opening an saving of the Xsim waveform viewer.

