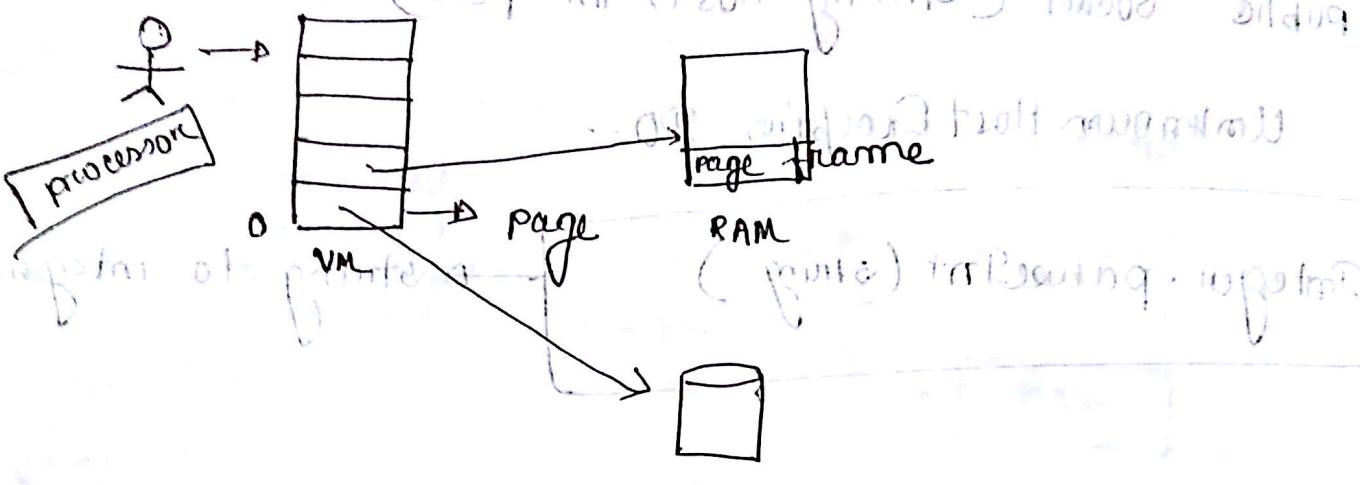


Nachos - 3



* RAM യാൽ എൻ്റെ, disk യാൽ എൻ്റെ, RAM യാൽ കൂടാതു എന്ത്?

Index

Page Table			disk യാൽ RAM യാൽ
rpn	valid	ppn	
0	True	0	
1	False	-	

Page fault: RAM യാൽ എൻ്റെ

Exception Handler (which) {

if (which == PageFaultException)

{ int add = machine → ReadRegister (39);

pagefaulthandler (add); ← disk (എന്നു RAM
എംഗേജ്)

void pagefault handler (int addr) {

init vpn = addr / PageSize;

Translation Entry

* pte = & machine → pageTable [vpn];

राखा - process की page

table

pte → physicalPage = memoryManager → AllocPage(pte,

Current Thread → pid);

at any cost रखा n page

परेंट, अंतर्गत page table की

save करूँ दिए

pte → valid = TRUE;

pte → dirty = FALSE;

pte → use = FALSE; → यहाँ का page का

access करूँ शर्त, just

प्रियंका

if(vpn's first time fault){

Code segment କୁଣସାମ୍ବ ଆର୍ କାଲିପ

R3, code segment off base
address

CS = current Thread → space

$\rightarrow \text{moffH}.\text{code} \cdot \text{virAddr};$

$\text{CS}_{\text{vars}} = \dots \rightarrow \text{code_size}$

ids = ... →... initData.virAddr;

ids কর size o এল, ids

☞ garbage value ☞,

idss = → init Data.size;

if ($x >= c_s$ & & $x <= c_s + c_{ss} - 1$)

{ in code segment.
}

disk → a collection of files, OR

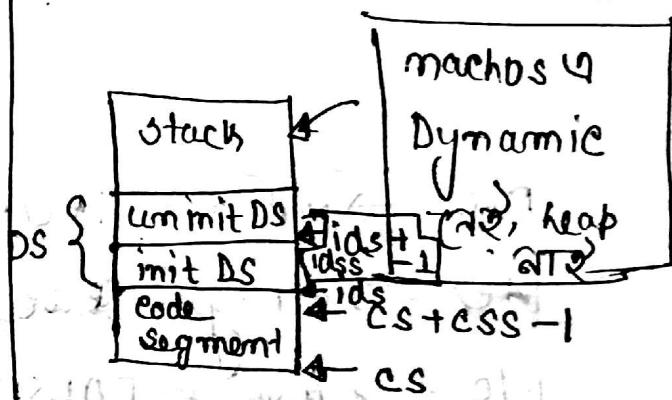
page ଟର କୋଣାର୍କ
ପାତ୍ର (disk ପ୍ରେ)
ଆମେ ଏବେ, ଏହି

ଆଯୁଜନ୍ୟ → Swap
Space

executable file.

Page fault એવાનુ

एक executable file
में शामिल (Let)



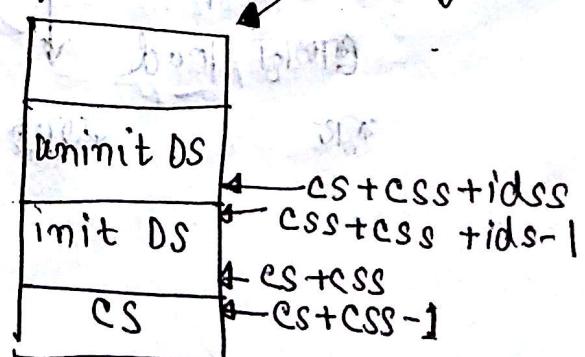
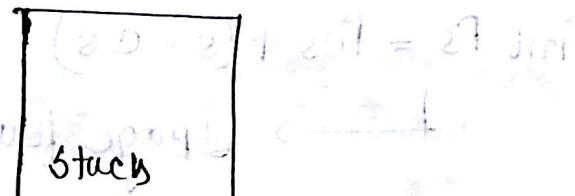
charome UI memory?
layout.

```
int b=10;  
int a;
```

```
int main()  
{  
    ?
```

Data segment (global
Variable 27B,
Variable 27B,

initialized DS, b=10
un " ", a



• गोली segment का size 0

एक उत्तर उन्हीं attribute का

मान एक garbage value है,
वह उत्तर size check कर

attribute use करते हैं।

otherwise

एक उत्तर है।

uninit DS

init DS

CS

```
int S = vpn * PageSite;
```

```
int e = s + pagesize - 1;
```

 **fault**

• २०१ ८०८(प्र०५), ७९८ ending
add (virtual add)

// C# case এর অব্য. condition

ନିଷ୍ଠାପନ କର,

if (case 1) {

$$\text{int } fs = fcs + (s - cs)$$

\rightarrow page fault ହେଲ୍‌ଡି,

ଏକ exe file ଥିଲୁ ଶାର୍ଟ

current thread → space

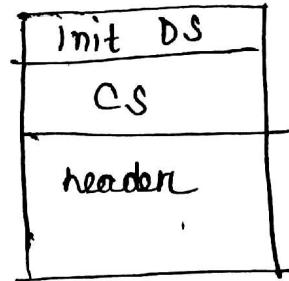
→ load(s, ^{TAKE} pagesize, fs)

ଶ୍ରୀଯତ୍ରି load

ରୂପୀରୁ

ପ୍ରତିକ୍ରିୟା

४७



executable file

→ એવાંકી uninitials કરો

ଅର୍ଦ୍ଧ ମାତ୍ରା କିମ୍ବା କିମ୍ବା

ભારતી પેજ ટેબલ

କାନ୍ତି ପିତ



ଏକୁଳି ରେଣ୍ଡିଗ୍ରେ ୦,

segment, page size एवं
multiple ली,

CASE 1: $s \rightarrow cs$, $e \rightarrow cs$

Case 2: $s \rightarrow cs, e \xrightarrow{\text{insertion}}$

initData

Case 3: $s \rightarrow cs$, $e \rightarrow$
a limit/starkly

else if (case 2)

$$\text{int } fs = fcs + (s - cs);$$

$$\text{int size} = (ce - s) + 1; \quad (H-2-03)$$

current Thread \rightarrow space \rightarrow

load (s, size, fs);

~~fs = fids~~

size = pagesize - size;

Current Thread \rightarrow space \rightarrow

load (ids, size, fs);

init data segment

start (VM seg)

else if (case 3)

$$\text{int } fs = fcs + (s - cs);$$

$$\text{int size} = (ce - s) + 1;$$

Current Thread \rightarrow space \rightarrow

load (s, size, fs)

Case 4: $s \rightarrow \text{initData}$, $fs \rightarrow \text{initData}$

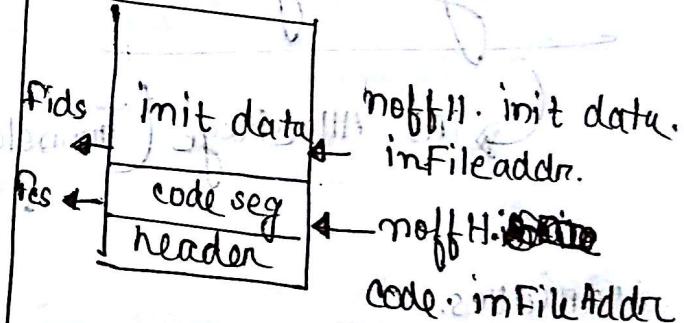
$e \rightarrow \text{initData}$

Case 5: $s \rightarrow \text{initData}$, $e \rightarrow \text{uninitData}$

$\rightarrow \text{uninitData/Stack}$

Case 6: $s \rightarrow \text{uninitData}$, $e \rightarrow \text{uninit/Stack}$

$\rightarrow \text{uninit/Stack}$



moff H. init data

address space \rightarrow current

instance space (0-255)

organizing current Thread \rightarrow

space \rightarrow moff H.

virtual address (segment)

1376 seg space

$fs = \text{fids}.$ $ce \rightarrow \text{PageSite}$

$\text{size} = \text{idss};$

current Thread \rightarrow space \rightarrow load. ($\text{idss}, \text{size}, fs$);

Want to size \rightarrow idss

~~size = PageSite - (ce - s + 1) - idss;~~

current Thread \rightarrow space \rightarrow clear ($cs + css + \text{idss}, \text{size}$);

clear space (0)

Want to size \rightarrow 0

clear (0)

Want to 0

Memory Manager:

int AllocPage (Translation *pte, int pid)

differs \rightarrow what will

Variables: pid

int *pidarr; H-H30

virtual address contains
constructor

for num Phy Page

(Translation Entry *pte);

some pte;

* page id (process id) will be

* virtual page (pte)

page frame id (pte)
(store address)

(H-H30) = 9512

single absent frame

(21-31) 2000

$ft[0] = 0$

$\downarrow \rightarrow$ page free area.

আর্টেক কনস্ট্রুক্টর $G : 0$ এর ft ,

$ft[0] \neq 0 \rightarrow$ page free area

int AllocPage (
 Block)

{ for (int i=0; i < NumPhysPages; i++)

{ if ($ft[i] == 0$) {

$ft[i] = pte;$

$pidarr[i] = pid;$

return i;

}

int f = frameToReplace

Thread *t = processTable \rightarrow Get ($pidarr[f]$);

$\downarrow \rightarrow$ Thread এর pointer return

original process Q_i

new file

→ swap space
free memory

pidarr[0]

t → space → bs → Page Out

(ft[f])

② pageframe ki sogn, pageframe swap

space aajayega,
ft[f] = pte;

pidarr[f] = pid;

return p;

}

void Release (int f) { → SC-EKT U
call &P.

ft[f] = 0;

}

(ET) \rightarrow global memory = 1st memory

1st memory = local memory

Backing store: \rightarrow അംഗീകാരിക്കപ്പെട്ടത്

OpenFile * f;

BitMap * map; \rightarrow അംഗീകാരിക്കപ്പെട്ടത്

swap space എന്നതാണ്

അംഗീകാരിക്കപ്പെട്ടത്

Backing store (int numPage)

\rightarrow അംഗീകാരിക്കപ്പെട്ടത്

അംഗീകാരിക്കപ്പെട്ടത്

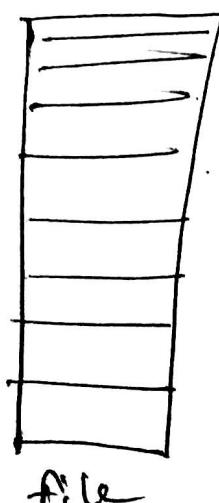
ഡയൽ ബിറ്റ് Map initialization;

ഡയൽ Initially 0

swap space 0

void PageOut

(Translation Entry * pte).



file

$\text{pte} \rightarrow \text{valid} = \text{FALSE}$

if (!isPresent(pte)) || $\text{pte} \rightarrow \text{dirty}$)

swap space ↗

copy from.

data copy ↗ f → writeAt($\text{pte} \rightarrow \text{PhysicalPage}$)

* Page size, Page sizes

(minimum 4KB)

base add

$\text{pte} \rightarrow \text{VirtualPage}$

* Page size

कायर समै

mapping,

(file एवं अ

no offset वै?)

map → Mark ($\text{pte} \rightarrow \text{VirtualPage}$).

swap ↗

प्रति विलेव्वी



```

void PageIn (Translation-Entry *pte) {
    f → readAt (Physical Page →
        &machine → main Memory [pte →
        Physical Page * Page Size], Page Size,
        pte → virtualPage * PageSize);
}

```

bool isPresent (Translation Entry *pte)

{

AddressSpace:

variables:

Openfile * executable;

Noff Headers (noff);

Backing store bs;

IT + FANOD = WALL ← JSB ? () statement

Constructor

Executable → ~~Allocation zone~~ of size 2MB,
// Noff Header noff H;
} be @ initialise;

void load (int to, int size, int infileAddn)

{

Executable → Read At (& machine → MainMemory

[pagetable [to/page size]. Physical Page →

PageSize + to % PageSize], size, infileAddn);

}

Clear () { bzero };

pageNum

LRU

int count = 0;

Read Mem, Write Mem (Translator @ (count++).
Translatable cc @ (0))

Translate () {pte → LRU = count++;