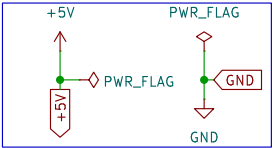


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Simple Power Lable

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The diagram shows an LM555 timer (U2) configured as an astable multivibrator. The timer is connected to a +5V supply and ground. The timing network consists of a 1μF capacitor (C1) and a 1MΩ potentiometer (RV2) in parallel, connected to pins 1, 2, and 6. A 0.01μF capacitor (C2) is connected between pins 5 and VCC. The output (pin 3) is labeled 'AstableMode'.

The diagram shows a monostable multivibrator circuit. A 5V supply is connected to the VCC pin (pin 8) of the LM555 timer (U6). A 0.01µF capacitor (C5) is connected between VCC and GND. The timing network consists of two 1K resistors (R6 and R7) and a switch (SW3, SW\_SPDT). The switch is connected to the RST pin (pin 4) and the DISCH pin (pin 7). The timing network is also connected to the THRES pin (pin 6) and the TRIG pin (pin 2). The output of the timer (OUT, pin 3) is labeled MonoStableMode. The GND pin (pin 1) is connected to the common ground.

The logic circuit diagram shows the implementation of a 3-bit counter using three 74LS10 (hex inverters), one 74LS04 (hex inverters), and one 74LS104 (monostable multivibrator). The circuit is powered by a 5V supply and ground. The inputs are AstableMode, MonoStableMode, and BiStableMode. The outputs are CLK and CLK. The circuit includes a 10k resistor (R1) and a 100nF capacitor (C1) connected to the CLK output. The circuit is labeled with component names and pin numbers.

A circuit diagram showing a 5V DC voltage source (represented by a red arrow pointing up) connected to ground (represented by a red triangle pointing down) through a capacitor labeled C6 with a value of 0.1  $\mu\text{F}$ .

