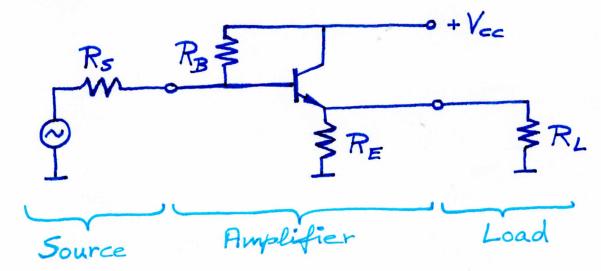
### Common - collector circuit

#### => Emitter follower

Connect source and load



AG coupling capacitors (to decouple source and load from DC bias of transistor

$$\mathbb{R}_{S} \longrightarrow \mathbb{R}_{C} \longrightarrow \mathbb{R}_{L}$$

DG analysis = Quiescent point (Q-point)

$$KVL \qquad I_{B}R_{B} + V_{BE} + I_{E}R_{E} = V_{cc}$$

$$(\beta+1)I_{B}$$

=0 One egn. with one unknown (IB)

$$\Rightarrow Solve for I_{B}$$

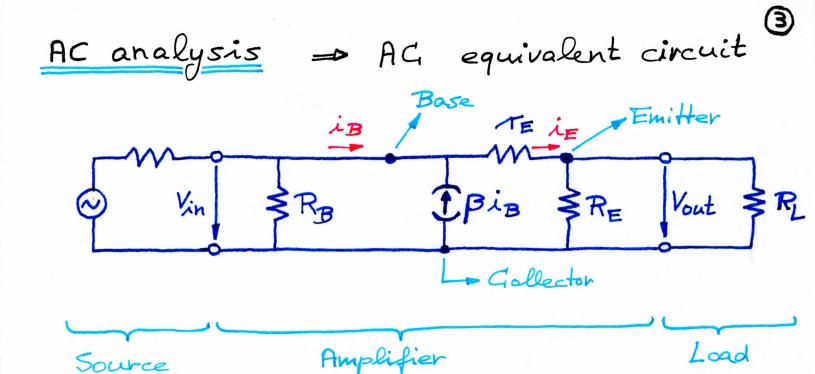
$$\Rightarrow I_{B} = \frac{V_{cc} - 0.7V}{R_{B} + (\beta+1)R_{E}}$$

$$T_{E} = (\beta + 1)T_{B} \qquad T_{C} = \beta T_{B}$$

$$V_{E} = T_{E}R_{E} = (\beta + 1)T_{B}R_{E}$$

$$V_{CE} = V_{CC} - V_{E}$$

=> This completes Q-point calculation



OG Voltage gain Avac

$$A_{Voc} = \frac{V_{out}}{V_{in}} = \frac{i_E R_E}{i_E r_E + i_E R_E} = \frac{R_E}{r_E + R_E}$$

$$\Rightarrow A_{Voc} < 1$$

$$Recall: r_E \ll R_E \Rightarrow A_{Voc} \approx \frac{R_E}{R_E} \approx 1$$

$$\Rightarrow A_{Voc} \approx 1 \Rightarrow V_{out} \approx V_{in}$$

# Input impedance Zin

$$Z_{in} = \frac{V_{in}}{J_{in}} = \frac{V_{in}}{J_{RB} + J_{B}} = \left(\frac{J_{RB} + J_{B}}{J_{in}}\right)^{-1}$$

$$= \left(\frac{J_{RB} + J_{B}}{J_{in}}\right)^{-1} = R_{B} \parallel \frac{V_{in}}{J_{B}}$$

$$= R_{B} \parallel \frac{J_{E} \left(T_{E} + R_{E}\right)}{J_{B}} = R_{B} \parallel (J_{E} + J_{E})$$

$$\approx R_{B} \parallel J_{E} \left(T_{E} + R_{E}\right)$$

$$\approx R_{B} \parallel J_{E} \left(T_{E} + R_{E}\right)$$

Note: Base bias network generally more resistive than RE. Also: NE RE

=> Emitter follower has high input impedance.

## Output impedance Zout

Zpre-Transistor

Pre- Transistor

$$Z_{pre-T} = \frac{V_{pre-T}}{j_E} = \frac{j_B(R_S || R_B)}{j_E} = \frac{j_B(R_S || R_B)}{(\beta+1)j_B}$$
$$= \frac{R_S || R_B}{\beta+1}$$

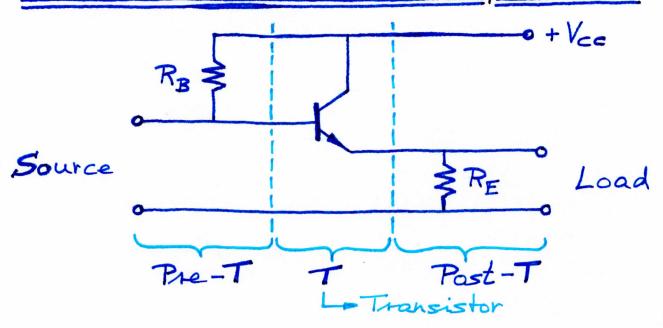
# SG current gain A 150

$$A_{ISG} = \frac{j_{out}}{j_{in}} = \frac{(\beta+1)j_{B}}{j_{B}+j_{RB}} = \frac{\beta+1}{j_{B}}$$

Summary of emitter follower

$$Z_{out} \approx R_E$$
 (low)

# Note on resistance transformation



Note: When applying a signal is to the input side, the current through  $R_E$  is  $(\beta+1)$  is. Accordingly, the voltage drop across  $R_E$  is is  $(\beta+1)R_E$ , so that the resistance appears to be  $(\beta+1)R_E$ 

Note: When applying a signal if to the output side, the current through RB is  $\frac{iE}{B+1}$ . Accordingly, the voltage drop across RB is  $iE = \frac{1}{B+1} RB$ , so that the resistance appears to be RB/(B+1).