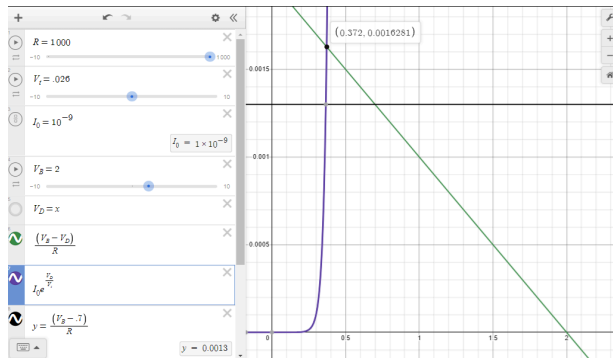
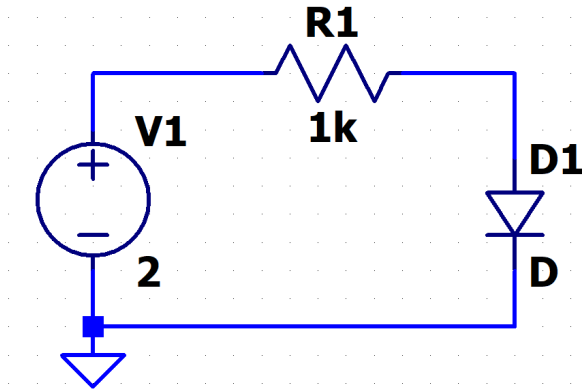


Hayden Fuller
Intro to Electronics HW3

- 1) Consider a series circuit consisting of an ideal DC voltage source (2.0 V), a resistor (1.0 k Ω) and a silicon diode that is biased in the forward direction. A Si diode has a turn-on voltage of 0.7 V and a reverse saturation current of $I_0 = 1$ nA.

- a) Sketch the electrical circuit. Make a quantitative plot of the diode forward characteristic and the load line. Determine the diode forward current from the plot.



$$V_{\text{bat}} - IR = V_{\text{diode}}$$

$$I = (V_{\text{Bat}} - V_{\text{diode}}) / R$$

$$I = (2 - x) / 1000$$

$$I = I_0 (e^{V_d / V_t})$$

$$I = 10^{-9} (e^{V_d / 0.026})$$

$$I = 0.016 \text{ A}$$

- b) Calculate the diode forward current without using the plot. Comment on the agreement / disagreement between graphical solution and analytical solution.

$$V_{\text{diode}} \approx 0.7$$

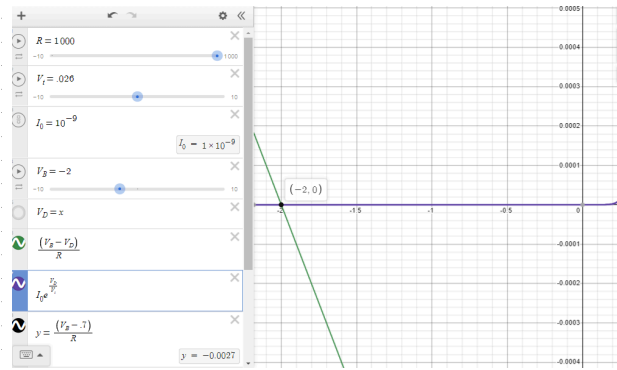
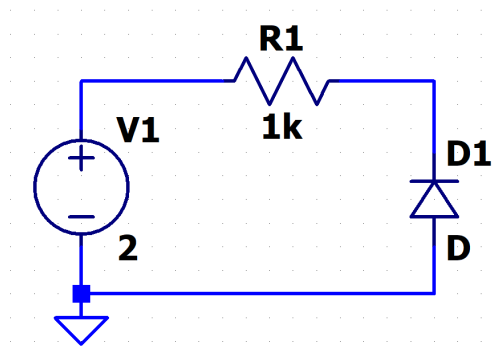
$$I = (V_{\text{Bat}} - V_{\text{diode}}) / R$$

$$I = (2 - 0.7) / 1000$$

$$I = 0.013 \text{ A}$$

It's approximately equal. This works because $V_{\text{diode}} \gg V_t$

- c) Next consider that the polarity of the diode is reversed. Sketch the electrical circuit. Make a quantitative plot of the diode reverse characteristic and the load line. Determine the diode current from the plot.



Essentially 0A

- d) What is the diode reverse current (without using the plot)? Comment on the agreement / disagreement between graphical solution and analytical solution.

$$V_{\text{diode}} \approx 0.7$$

$$I = (V_{\text{Bat}} - V_{\text{diode}}) / R$$

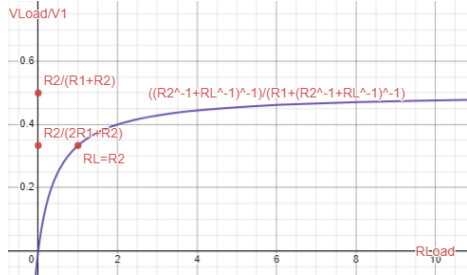
$$I = (-2 - 0.7) / 1000$$

$$I = -0.0027 \text{ A}$$

This is incorrect because $V_{\text{diode}} \gg V_T$

- 2) A voltage divider is intended to provide a stable voltage to a load. A Zener diode circuit is also intended to provide a stable voltage to a load. This problem compares the two methods.

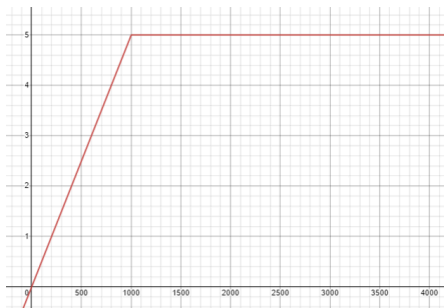
- a) The circuit below is a voltage divider that is connected to a load resistance (R_{Load}). Draw the right-hand-side (RHS) diagram and complete it.



- b) What do we learn from the diagram? Which condition must the load resistance satisfy, so that a voltage divider works as intended?

$$R_L \gg R_2$$

- c) The circuit below is a Zener-diode-voltage-stabilization circuit that a load is connected to. Draw the RHS diagram and complete it.



- d) What do we learn from the diagram? Which condition must the load resistance satisfy, so that a voltage stabilization circuit provides a stable voltage?

$$R_L > V_d \cdot R_1 / (V_1 - V_d)$$

$$R_L > 5 \cdot 1000 / (10 - 5) = 5000 / 5 = 1000$$

- e) Compare the two circuits above. Advantages? Disadvantages?

The Zener diode gives a constant voltage as long as R_L is over a certain threshold, but will dump more current past the load to do so.

- 3) Consider the voltage-stabilization circuit below.

- a) Explain the purpose of the Zener diode.

A smoothing diode to maintain a more stable voltage. If the voltage goes too high, it will act as a smaller resistance and allow it to drop.

- b) What is the major disadvantage of the circuit?

The diode will waste power. It will waste less if it's just to protect from higher voltages, but will waste more if the RMS of V_{source} is greater than the zener diode voltage.

- 4) Consider the following diode circuit containing a silicon pn-junction diode with $V_{AC} = 10$ mV, $R_{AC} = 100 \Omega$, $V_{DC} = 5$ V, $R_{DC} = 100 \Omega$, and $R = 1$ k Ω . Assume that the capacitor C blocks all DC but lets pass all AC.

- a) Determine the steady-state voltage and current (DC values) of the diode (numerical values). The steady-state values may be called quiescent-point (Q-point) values.

$$V=5, R=1100, V_d=.7$$

$$V_R=5-.7=4.3$$

$$I=V_R/R=4.3/1100=3.909\text{mA}$$

- b) Draw an AC small-signal equivalent circuit of the diode circuit. Determine the Si-diode's differential resistance (numerical value).

$$V_{max}=5.01, R=1100, V_d=.7$$

$$V_R=5.01-.7=4.31$$

$$I_{max}=V_R/R=4.31/1100=3.918\text{mA}$$

$$V_{min}=4.99, R=1100, V_d=.7$$

$$V_R=4.99-.7=4.29$$

$$I_{min}=V_R/R=4.29/1100=3.900\text{mA}$$

$$V_{diff}=0.02\text{V}, I_{diff}=0.01818\text{mA}$$

$$R=V_{diff}/I_{diff}=1100.110011$$

$$r_D=R-1100=0.11 \text{ ohms}$$

- c) Determine the AC voltage across and current through the diode (numerical values).

$$I_{AC}=0.00909\text{mA}, V_{AC}=0.03020\text{mV}$$

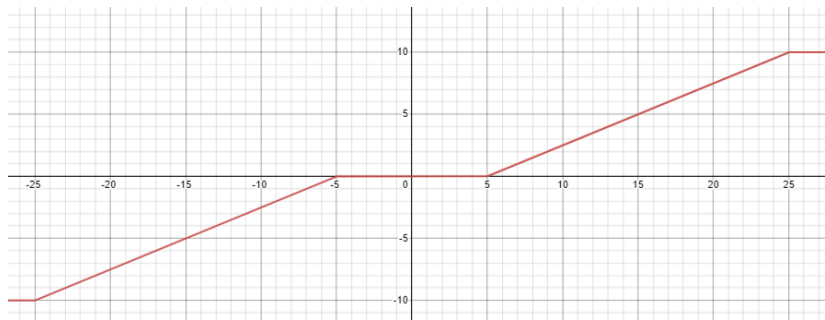
- 5) Consider the following Si Zener-diode circuit. Assume that the Zener voltage of all four Zener diodes is $V_{Zener} = 5\text{ V}$. Assume that $R_1 = R_2 = 1\text{ k}\Omega$.

- a) Define the diode threshold voltage (in your own words) and give its numerical value for the above circuit. Define the Zener voltage (in your own words) and give its numerical value for the above circuit.

The voltage required to make a significant current flow through a diode

The voltage a zener diode will maintain when more than that is applied, 5V.

- b) Draw the output-voltage-versus-input-voltage diagram of the circuit. Use the abscissa (horizontal axis) for the input voltage and the ordinate (vertical axis) for the output voltage. Mark all significant points of the diagram with numerical voltage values.



- c) Explain the diagram that you drew under the previous question.

The first two diodes block out anything less than 5, the resistors divide the remaining voltage in half, and the last two diodes cut off the voltage at 10.

- d) What is the output voltage for $V_{in} = 10\text{ V}$? What is the current value through R_1 ?

$$V(R_1 + R_2) = V_{in} - 5 = 5\text{ V}$$

$$V_{divider} = 5/2 = 2.5\text{ V}$$

$$V_{out} = 2.5\text{ V}$$

$$5\text{ V} / 2\text{ k}\Omega = 2.5\text{ mA}$$

$$V_{out} = 2.5\text{ V}$$

$$I(R_1) = 2.5\text{ mA}$$

- e) What is the output voltage for $V_{in} = 30\text{ V}$? What is the current value through R_1 ?

$$V(R_1 + R_2) = V_{in} - 5 = 25\text{ V}$$

$$V_{divider} = 25/2 = 12.5\text{ V}$$

$$V_{out} = 10\text{ V}$$

$$V(R_1) = 25 - 10 = 15\text{ V}$$

$$I = V/R = 15/1\text{ k} = 15\text{ mA}$$

$$I(R_1) = 15\text{ mA}$$

$$V_{out} = 10\text{ V}$$

$$I(R_1) = 15\text{ mA}$$