

ITE - Homework 04 - Solution

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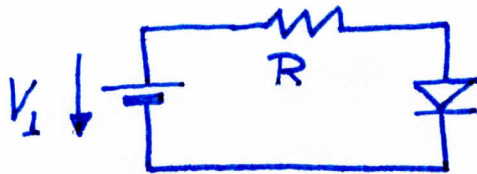
Problem 01

DC voltage source 2.0V

$R = 1\text{ k}\Omega$

Si-diode

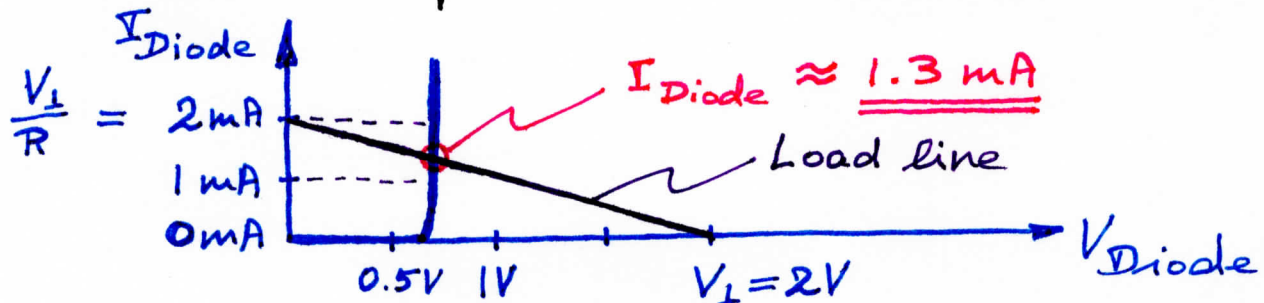
(a)



$$V_{\text{Diode}} = V_1 - RI$$
$$\Rightarrow I = \frac{V_1 - V_{\text{Diode}}}{R}$$

Load line

Quantitative plot



(b) Diode is forward biased $\Rightarrow V_{\text{Diode}} = 0.7\text{V}$

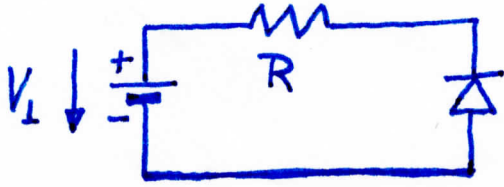
$$\Rightarrow V_1 = IR + 0.7\text{V}$$

\downarrow
 2V

$$\Rightarrow IR = 2.0\text{V} - 0.7\text{V} = 1.3\text{V} \Rightarrow I = \frac{1.3\text{V}}{R} = \frac{1.3\text{V}}{1\text{ k}\Omega}$$
$$= \underline{\underline{1.3\text{ mA}}}$$

Graphical solution ($\sim 1.3\text{ mA}$) and analytic solution are in good agreement.

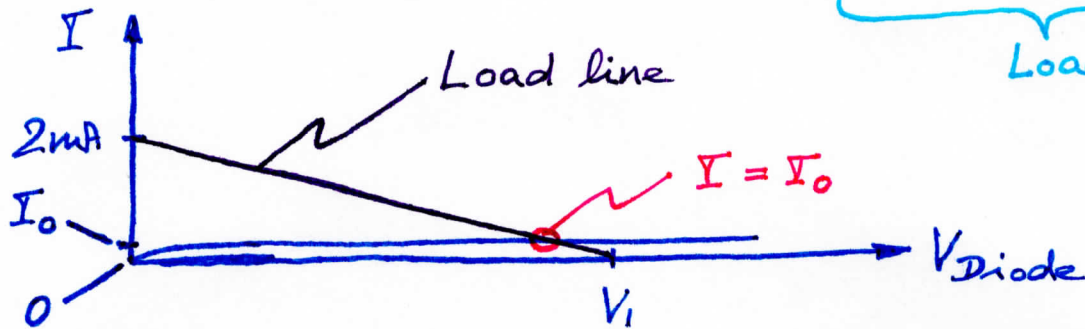
(c) Reverse polarity of diode



$$V_I = IR + V_{\text{Diode}}$$

$$\Rightarrow I = \frac{V_I - V_{\text{Diode}}}{R}$$

Load line



\Rightarrow Diode current is $I_0 = 1.0 \text{ nA}$

(d) Diode current is reverse saturation current $\Rightarrow 1 \text{ nA}$. Voltage drop across resistor: $V_R = IR = 1 \text{ nA} \times 1 \text{ k}\Omega = 1 \mu\text{V}$
 $\Rightarrow V_R$ can be neglected.

A reversely biased diode carries the reverse saturation current $\Rightarrow I_0 = 1 \text{ nA}$

\Rightarrow Good agreement between graphical and analytic solution.

Problem 2: Voltage divider circuit

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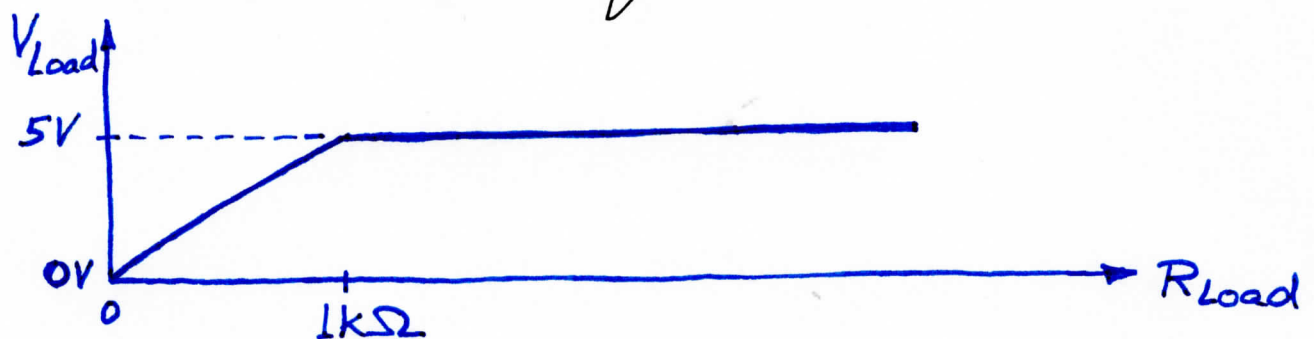
(a) Completion of diagram

$$V_{Load} = V_1 \frac{R_2 \parallel R_{Load}}{R_1 + (R_2 \parallel R_{Load})}$$



(b) We learn that the condition $R_{Load} \gg R_2$ must be satisfied for the voltage divider to work as intended

(c) Completion of diagram



\Rightarrow Zener diode provides better voltage stabilization than voltage divider.

R_{Load} must not be too small. Here R_{Load} must be $> 1k\Omega$.

(e) A Zener diode generally provides better voltage stabilization than a voltage divider.

Problem 3

Power supply

- (a) The Zener diode stabilizes the output voltage to V_{Zener} . Any excess voltage is cut off.
- (b) Major disadvantage: Lots of power will be dissipated by the Zener diode, if the voltage from the rectifier bridge exceeds the Zener voltage.

Problem 4

Diode circuit

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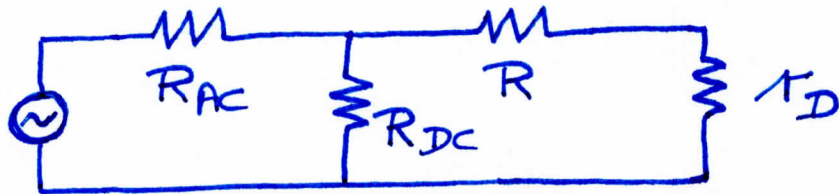
(a) Si-diode voltage = 0.7V

Si-diode current

$$V_{DC} = IR_{DC} + IR + V_{Diode} \quad (KVL)$$

$$\Rightarrow I = \frac{V_{DC} - V_{Diode}}{R_{DC} + R} = \frac{5V - 0.7V}{100\Omega + 1k\Omega} = \underline{\underline{3.91 \text{ mA}}}$$

(b) AC equivalent circuit



$$r_D = \frac{V_t}{I_{Diode}} = \frac{26 \text{ mV}}{3.91 \text{ mA}} = \underline{\underline{6.65 \Omega}}$$

Thermal voltage = 26 mV

(c) Total resistance of circuit

$$R_{total} = \underbrace{R_{AC}}_{100\Omega} + \left(\underbrace{R_{DC}}_{100\Omega} \parallel \underbrace{(R + r_D)}_{1006.65\Omega} \right)$$
$$= 100\Omega + 90.96\Omega = 191\Omega$$

AC current

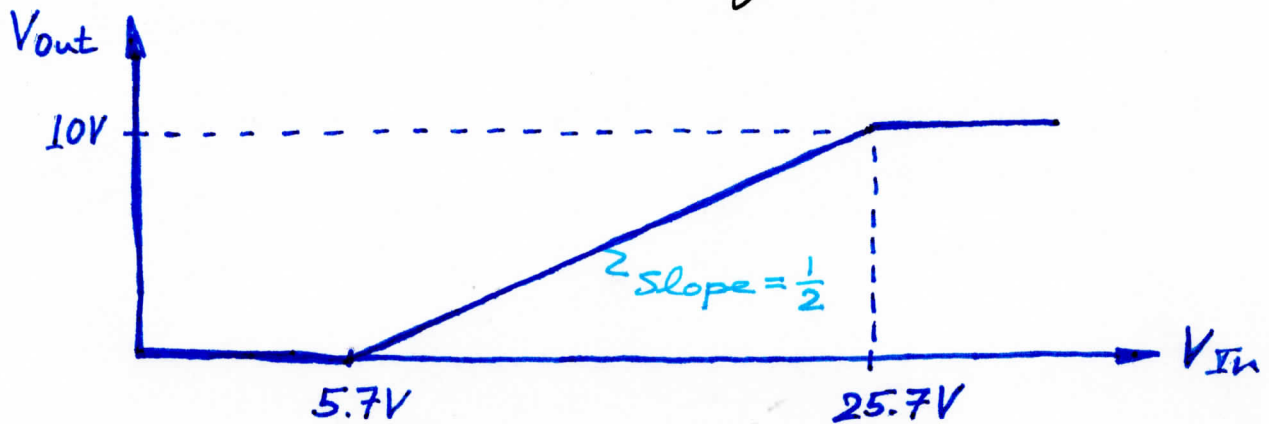
$$I_{R_{AC}} = \frac{V_{AC}}{R_{total}} = \frac{10 \text{ mV}}{191\Omega} = \underline{\underline{52.4 \mu A}}$$

Problem 5 Zener diode circuit

(a) Threshold voltage is voltage at which diode becomes conductive when biased in forward direction $\Rightarrow V_{th} = 0.7V$.

Zener voltage is voltage at which a Zener diode becomes conductive when biased in reverse direction. Example:
 $V_{zener} = 5V$.

(b) V_{out} - versus - V_{in} diagram



(c) For $V_{in} < 5.7V \Rightarrow$ No current $\Rightarrow V_{out} = 0$

For $5.7V < V_{in} < 25.7V \Rightarrow V_{out} = \frac{1}{2}(V_{in} - 5.7V)$
 due to voltage divider circuit

For $V_{in} > 25.7V \Rightarrow V_{out} = 10V$

due to the two Zener diodes

(9)

(d) $V_{In} = 10V$

$$\Rightarrow \text{Voltage across resistors} = 10V - 5V - 0.7V \\ = 4.3V \Rightarrow \underline{V_{out}} = \frac{1}{2} 4.3V = \underline{2.15V}$$

Current through R_L

$$I_{R_L} = \frac{V}{R} = \frac{4.3V}{2k\Omega} = \underline{2.15mA}$$

(e) $V_{In} = 30V$

$$\Rightarrow \underline{V_{out}} = 10V \quad \text{due to Zener diodes}$$

$$\text{Voltage drop across } R_1 = 30V - 5V - 0.7V - 10V \\ = \underline{14.3V}$$

$$\Rightarrow I_{R_L} = \frac{14.3V}{1k\Omega} = \underline{14.3mA}$$