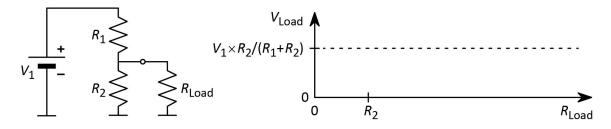
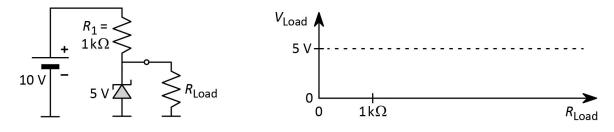
## Homework 04

- 1. Consider a series circuit consulting of an ideal DC voltage source (2.0 V), a resistor (1.0 k $\Omega$ ) and a silicon diode that is biased in the forward direction. A Si diode has a turn-on voltage of 0.7 V and a reverse saturation current of  $I_0$  = 1 nA.
  - (a) Sketch the electrical circuit. Make a quantitative plot of the diode forward characteristic and the load line. Determine the diode forward current from the plot.
  - (b) Calculate the diode forward current without using the plot. Comment on the agreement / disagreement between graphical solution and analytical solution.
  - (c) Next consider that the polarity of the diode is reversed. Sketch the electrical circuit. Make a quantitative plot of the diode reverse characteristic and the load line. Determine the diode current from the plot.
  - (d) What is the diode reverse current (without using the plot)? Comment on the agreement / disagreement between graphical solution and analytical solution.
- 2. A voltage divider is intended to provide a stable voltage to a load. A Zener diode circuit is also intended to provide a stable voltage to a load. This problem compares the two methods.
  - (a) The circuit below is a voltage divider that is connected to a load resistance ( $R_{Load}$ ). Draw the right-hand-side (RHS) diagram and complete it.

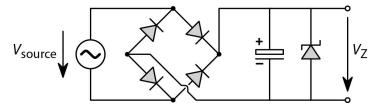


- (b) What do we learn from the diagram? Which condition must the load resistance satisfy, so that a voltage divider works as intended?
- (c) The circuit below is a Zener-diode-voltage-stabilization circuit that a load is connected to. Draw the RHS diagram and complete it.

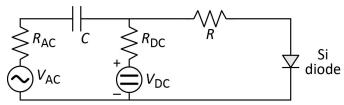


- (d) What do we learn from the diagram? Which condition must the load resistance satisfy, so that a voltage stabilization circuit provides a stable voltage?
- (e) Compare the two circuits above. Advantages? Disadvantages?

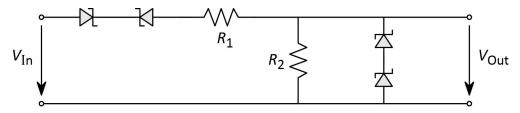
3. Consider the voltage-stabilization circuit below.



- (a) Explain the purpose of the Zener diode.
- (b) What is the major disadvantage of the circuit?
- 4. Consider the following diode circuit containing a silicon pn-junction diode with  $V_{AC}$  = 10 mV,  $R_{AC}$  = 100  $\Omega$ ,  $V_{DC}$  = 5 V,  $R_{DC}$  = 100  $\Omega$ , and R = 1 k $\Omega$ . Assume that the capacitor C blocks all DC but lets pass all AC.



- (a) Determine the steady-state voltage and current (DC values) of the diode (numerical values). The steady-state values may be called quiescent-point (Q-point) values.
- (b) Draw an AC small-signal equivalent circuit of the diode circuit. Determine the Si-diode's differential resistance (numerical value).
- (c) Determine the AC voltage across and current through the diode (numerical values).
- 5. Consider the following Si Zener-diode circuit. Assume that the Zener voltage of all four Zener diodes is  $V_{\text{Zener}} = 5 \text{ V}$ . Assume that  $R_1 = R_2 = 1 \text{ k}\Omega$ .



- (a) Define the diode threshold voltage (in your own words) and give its numerical value for the above circuit. Define the Zener voltage (in your own words) and give its numerical value for the above circuit.
- (b) Draw the output-voltage-versus-input-voltage diagram of the circuit. Use the abscissa (horizontal axis) for the input voltage and the ordinate (vertical axis) for the output voltage. Mark all significant points of the diagram with numerical voltage values.
- (c) Explain the diagram that you drew under the previous question.
- (d) What is the output voltage for  $V_{in} = 10 \text{ V}$ ? What is the current value through  $R_1$ ?
- (e) What is the output voltage for  $V_{\text{in}} = 30 \text{ V}$ ? What is the current value through  $R_1$ ?