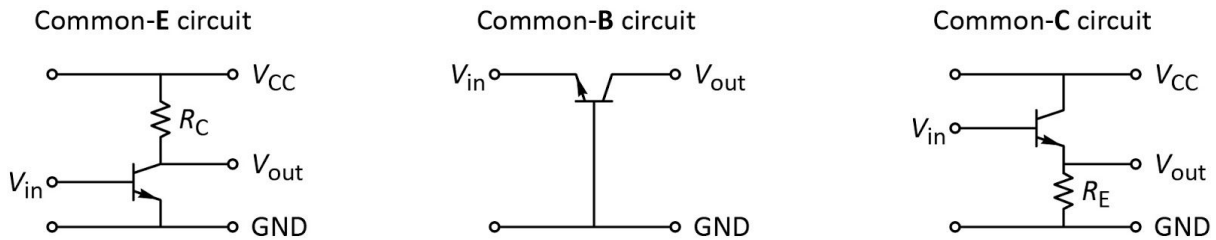
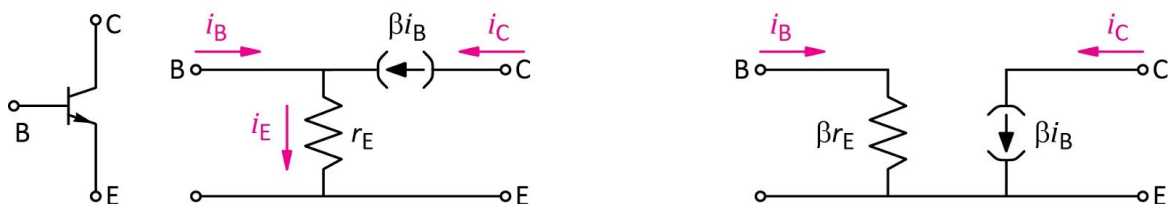


Homework 06

1. The figure below shows the three BJT circuits (bipolar junction transistor circuits). These circuits lack any biasing. For this reason, the circuits may be referred to as the basic BJT circuits. Let us assume that the source voltages include DC components that make the BJTs operate in their normal (forward active) mode.



- Define input impedance (Z_{in}), output impedance (Z_{out}), open-circuit-voltage-gain (A_{VOC}), and short-circuit-current gain (A_{ISC}).
 - For the common-E circuit, draw an AC small-signal equivalent circuit of the BJT circuit. Determine z_{in} , z_{out} , A_{VOC} , and A_{ISC} . Assume that the input signal has a DC component that causes the BJT to operate in the forward active regime (that is, the regular operating regime).
 - Answer the previous question for the common-B circuit.
 - Answer the previous question for the common-C circuit.
 - Which one of the three circuits has the lowest input impedance? Is low input impedance desirable?
 - Which one of the three circuits has the lowest output impedance? Is low output impedance desirable? Note: Two circuits may be similar with respect to output impedance.
 - Which of the three circuits has the lowest A_{VOC} ?
 - Which of the three circuits has the lowest A_{ISC} ?
 - Name a specific application for which each one of the three BJT circuits is particularly well suited for.
2. The figure below shows the small-signal equivalent circuit of a BJT that is configured in the common-emitter configuration. The LHS (left-hand side) circuit was derived in class; it is referred to as a “T” equivalent circuit. The RHS (right-hand side) circuit is identical to the LHS circuit with respect to its behavior at its three terminals B, E, and C; the circuit is referred to as “ π ” equivalent circuit.



- (a) Draw the “**T**” and “ **π** ” equivalent circuits and indicate all relevant quantities. Articulate three conditions (relating to input resistance, output resistance, and short-circuit current amplification A_{ISC}) that must be satisfied by the RHS circuit, if the RHS circuit is identical to the LHS circuit with respect to its behavior at the terminals B, E, and C.
 - (b) Show that the RHS circuit meets the three conditions that you have articulated under the previous question. It is acceptable to assume $\beta + 1 \approx \beta$.
 - (c) Can you articulate an advantage of the LHS circuit? Can you articulate an advantage of the RHS circuit?
3. Are the following statements **true** or **false**? Explain your answer.
- (a) A BJT under normal operating conditions will have a DC BE voltage of 0.7 V and an AC BE voltage $\ll 0.7$ V (BE = Base-Emitter).
 - (b) The large-signal equivalent circuit of a BJT, valid for all operating conditions, must include two pn-junction diodes.
 - (c) A BJT amplifier circuit having the common-**C** configuration can attain high values of both A_{VOC} and A_{ISC} .
 - (d) The common-**B** configuration of the BJT is frequently used, whereas the common-**E** configuration of the BJT is rarely used.