
Acorn Enhanced Expansion Card Specification

(formerly *Acorn expansion card specification*)

Acorn Enhanced Expansion Card Specification

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Acorn Enhanced Expansion Card

the single Eurocard dimension only – double Eurocards will not fit. All expansion cards fitted to the Risc PC should include an EMC gasket where required, fitted inside the rear panel. The EMC gasket should be as detailed in the section entitled *EMC designing*

Acorn Enhanced Expansion Card

Acorn Enhanced Expansion Card

and D. The slowest cycle type is A and the quickest is D
– cycle types B and C fall in-between. Data can be

Acorn Enhanced Expansion Card

drive the _____

Acorn Enhanced Expansion Card

must be installed to improve the ventilation, if not already fitted.

Acorn Enhanced Expansion Card

byte of the ECId. In this way the operating system can quickly find which expansion card is generating the interrupt.

If the expansion card contains paged ROM, these status bits may be located elsewhere in the expansion card address space, in which case the two bits in the ECId low byte should be zeros. The location of these status bits must appear in the ROM space above the extended ECId.

If an expansion card is not capable of generating either a PIRQ

Acorn Enhanced Expansion Card

paged) ROM into main memory, and as such is capable of updating the page register as required. There may be

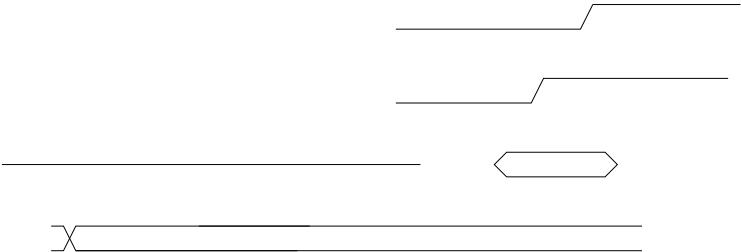
Figure 3: ExtenS86 MeCI

Acorn Enhanced Expansion Card

Figure 7: Stretched expansion card read cycle



Figure 10: Medium cycle read



Acorn Enhanced Expansion Card

Figure 15: Synchronous cycle read

REF8M

$\overline{\text{IORQ}}$ (A)

$\overline{\text{IOGT}}$

$\overline{\text{PS}}$

$\overline{\text{BL}}$

BD[0:15]

LA[2:15]

Figure 16: Synchronous cycle write

REF8M

$\overline{\text{IORQ}}$ (A)

Acorn Enhanced Expansion Card

A MEMC I/O cycle is shown in *Figure 18* below. The cycle starts with

Safety

The current industry wide IT safety standard is IEC950 whose European 'harmonised' version is EN60950 / BS7002, but your particular application may also be within the scope of other additional standards.

The main requirements are that the equipment provide protection against:

- the spread of fire
- hazardous voltages or energy.

The spread of fire

The Underwriters Laboratory (UL) of the USA have

DMA Extended Bus Interface

Description of signals

The following table shows the complete pin-out of the Acorn enhanced expansion bus. The DEBI specific

DMA Extended Bus Interface

DMA access timings

The DMA access timings for Cycle types A, B, C, and D are as follows:

Table 4: DMA access timings

Sym.	Description	Cycles	Min	Typ	Max	Units
t0	CLK16 clock cycle	all		62.5		ns
t1	Clock low to $\overline{\text{DACK}}$ low	all	0		20	ns
t2	Clock low to TC high	all	0		20	ns
t3	$\overline{\text{DACK}}$ strobe width	A	427			ns
		B	302			ns
		C	175			ns
		D	115			ns

Reader's Comment Form

Acorn Enhanced Expansion Card Specification (Issue 5)

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