GPIO Header

| VSYNC | 3 | | 2 | 5V |
| HSYNC | 5 | | 4 | 5V |
| B2 | 7 | | 6 | GND |
| GND | 9 | | 8 | G6 |
| R3 | 11 | | 10 | G7 |
| | 13 | | 12 | R4 |
| CSYNC | 15 | | 14 | GND |
| | 17 | | 16 | GPIO_23 |
| G2 | 19 | | 18 | |
| B7 | 21 | | 20 | GND |
| G3 | 23 | | 22 | |
| GND | 25 | | 24 | B6 |
| | 27 | | 26 | B5 |
| B3 | 29 | | 28 | |
| B4 | 31 | | 30 | GND |
| G5 | 33 | | 32 | G4 |
| R5 | 35 | | 34 | GND |
| | 37 | | 36 | R2 |
| GND | 39 | | 38 | R6 |
| | | | 40 | R7 |

R7 — R1 499
R6 — R4 1k
R5 — R7 2k
R4 — R10 4k
R3 — R13 8k
R2 — R16 16k
R_OUT

G7 — R2 499
G6 — R5 1k
G5 — R8 2k
G4 — R11 4k
G3 — R14 8k
G2 — R17 16k
G_OUT

B7 — R3 499
B6 — R6 1k
B5 — R9 2k
B4 — R12 4k
B3 — R15 8k
B2 — R18 16k
B_OUT

CSYNC — R19 1k — G_OUT

CN3 — MOLEX RGsB + 12V

| GND | 1 | | 6 | B_OUT |
| GND | 2 | | 7 | G_OUT |
| GND | 3 | | 8 | R_OUT |
| GND | 4 | | 9 | GPIO_23 |
| GND | 5 | | 10 | 12V |

| Schematic | Schematic1 | Create at | 2025-11-02 |
| | | Update at | 2025-12-04 |
| Board | Board1 | Page | P1 |
| Drawn | | RPi5_HAT_RGsB_PIO_CSYNC | |
| Reviewed | | | |
| | Version | Size | Page 1 Total 1 |
| | V1.0 | A4 | |