

Anjuman-i-Islam's
M. H. Saboo Siddik College of Engineering

QUESTION BANK FOR I A E 1(MARCH 2024)

SEM: IV Computer

Sub: Microprocessor

Q.1	02 Marks Questions	
	a)	Explain POP Instruction with examples.
	b)	Compare SUB and CMP instruction of 8086.
	c)	Compare AND and TEST instruction of 8086.
	d)	Explain features of 8086.
	e)	Explain fixed port addressing with an example.
	f)	Explain variable port addressing with example.
	g)	Explain the programmer's model of 8086.
	h)	Draw architecture diagram of 8086.
	i)	Explain flag register of 8086.
	j)	Explain Direct memory addressing mode of 8086.
	k)	Explain implied addressing mode of 8086.
	l)	Explain purpose of instruction queue in 8086.
	m)	Draw block diagram of 8255 PPI
	n)	Numerical on I/O Mode or BSR Mode.
	l)	Explain Modes of 8255 I/O Mode and BSR Mode
Q.2	05 Marks Questions	
	a)	Explain addressing modes of 8086.
	b)	Draw and Explain block diagram of 8086 in Minimum Mode.
	c)	Draw and Explain block diagram of 8086 in Maximum Mode.
	d)	Draw and explain architecture of 8086.
	e)	Explain Memory Banking of 8086.
	f)	Explain Memory Segmentation of 8086.
Q.3	05 Marks Questions	
	a)	Write a program in assembly language to sort 10 bytes in ascending/descending order.
	b)	Write a program in assembly language to transfer 20 bytes from data segment location (0000H) to data segment location (7000H).
	c)	Write a program in assembly language to transfer 20 bytes from data segment location to extra segment using STRING instruction.
	d)	Write a program in assembly language to search for a number from 10 bytes of data segment.