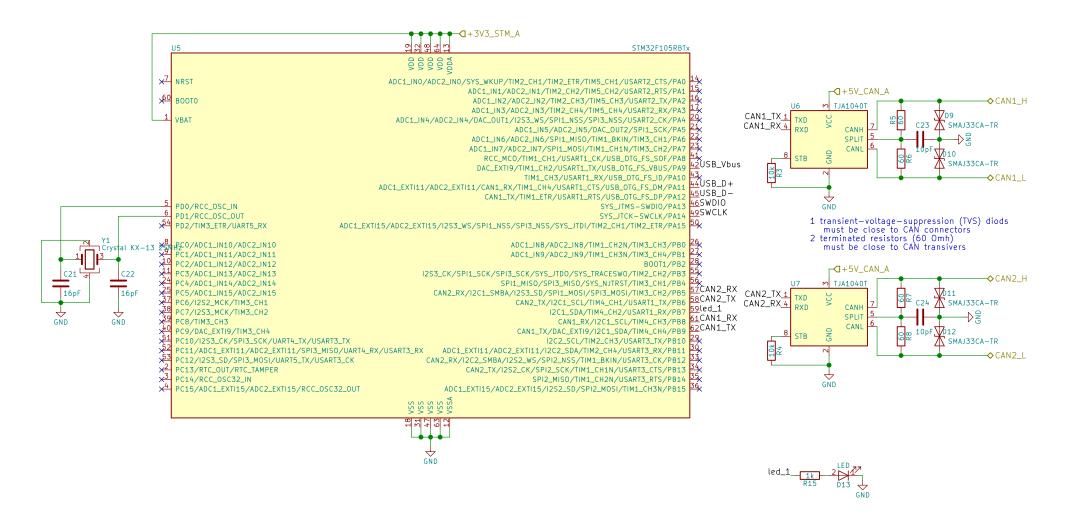
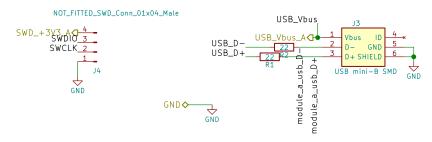
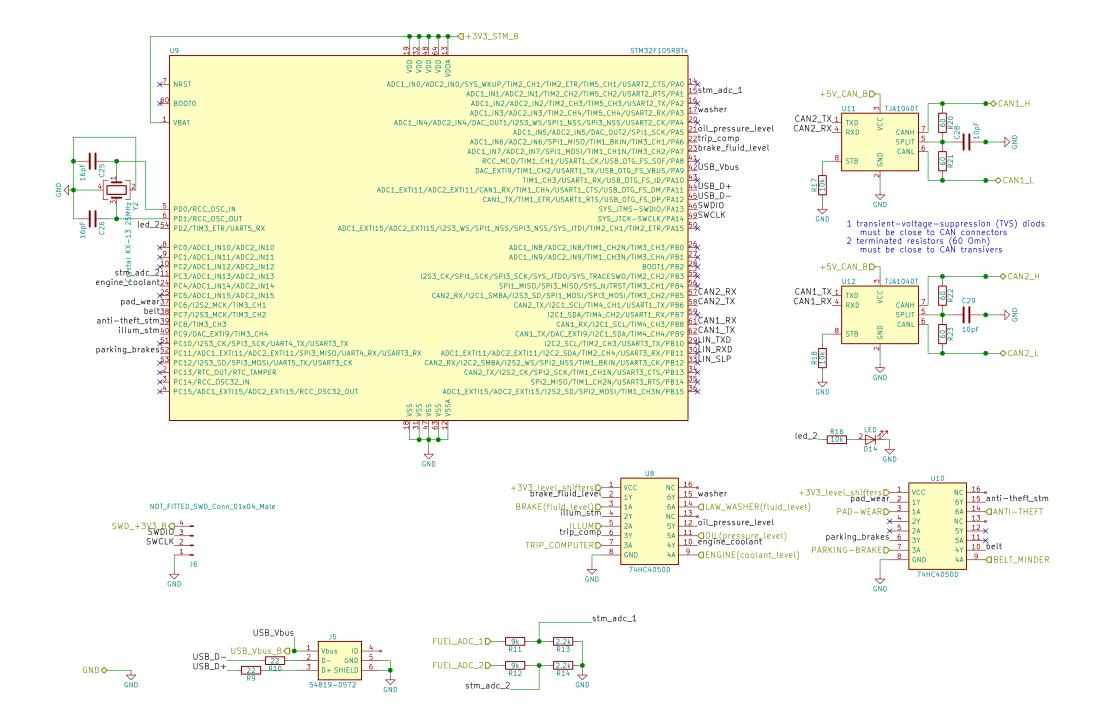


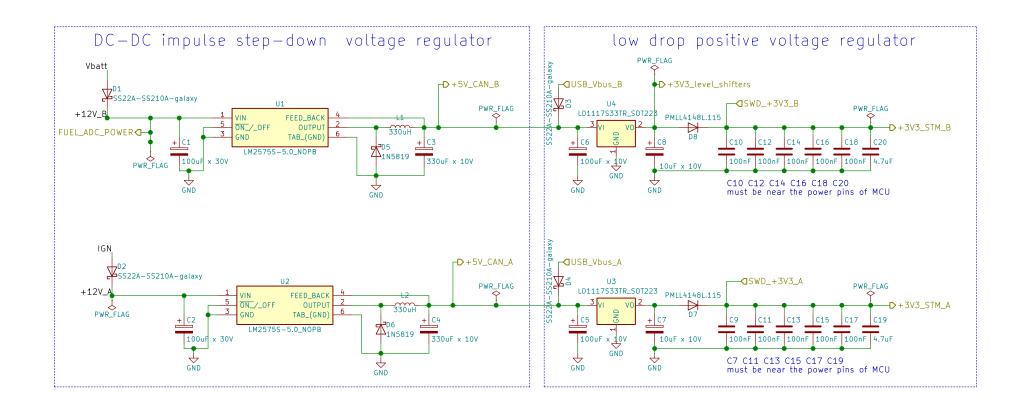
1 HS\_CAN\_H

 $\leftarrow$  GND











two separate module on 1 PCB — module B powered from Vbatt — module A powered from IGN (Vbatt present always)