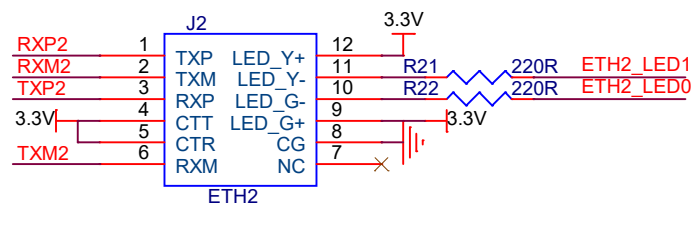
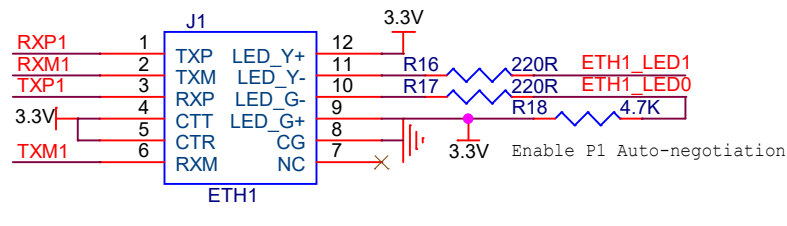
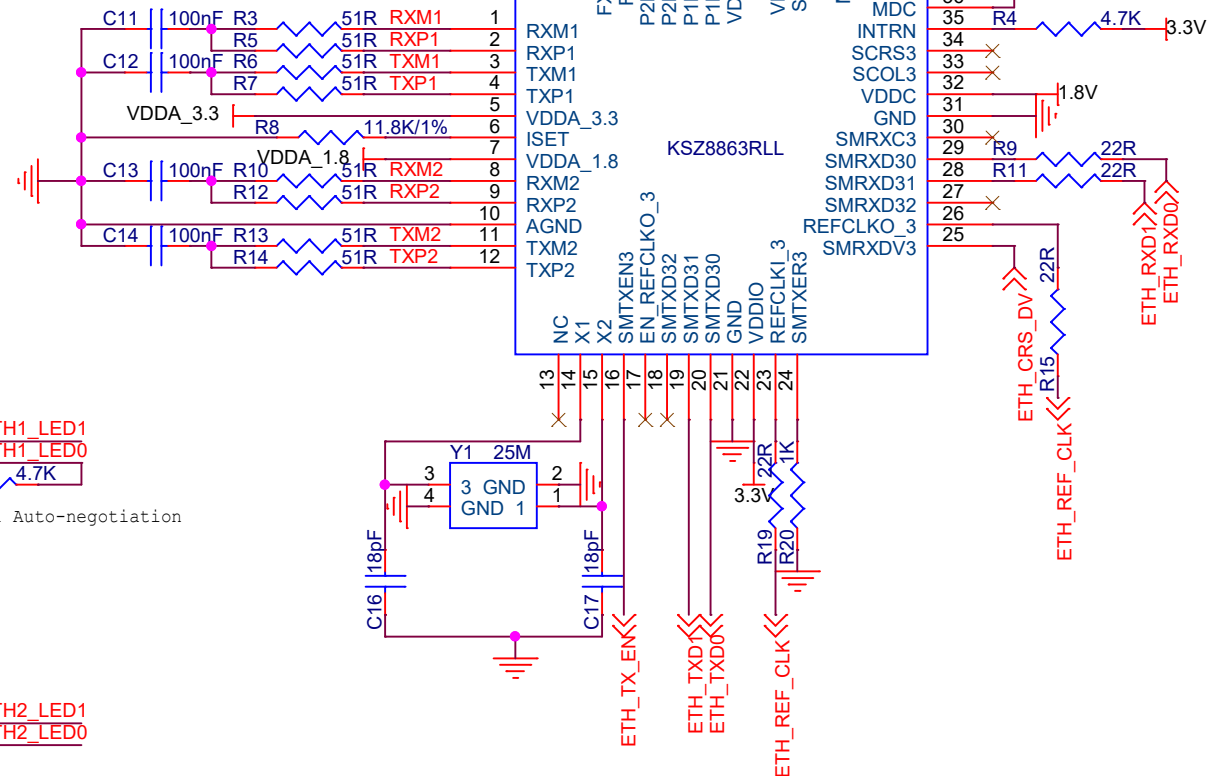
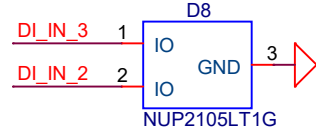
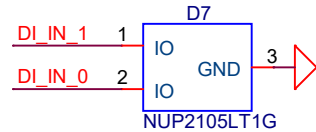
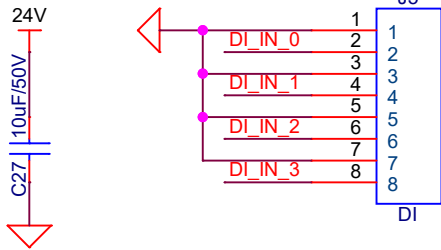
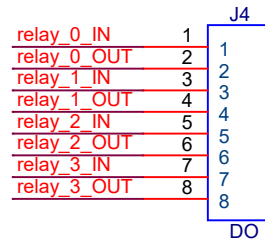
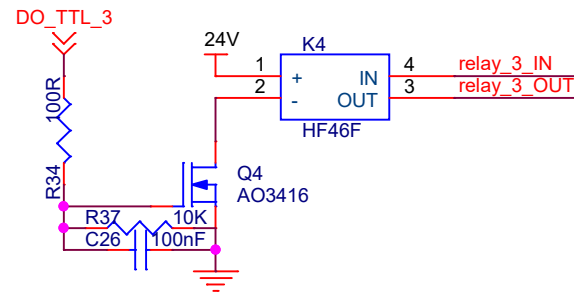
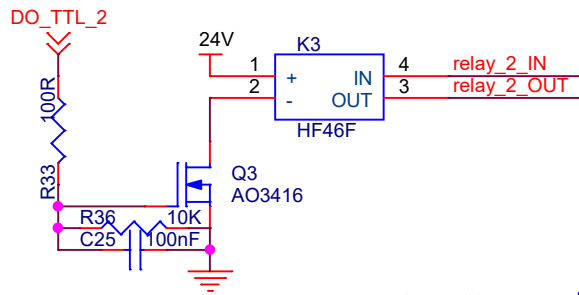
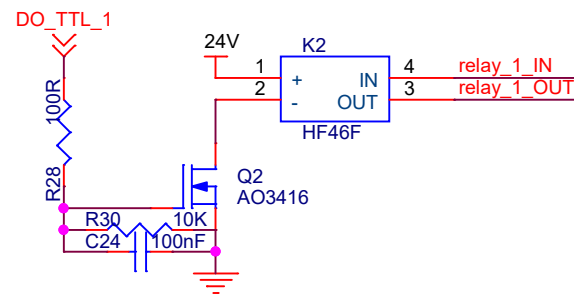
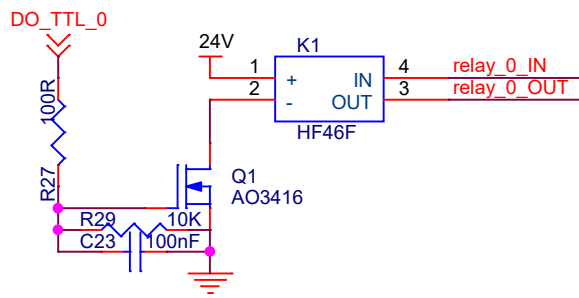
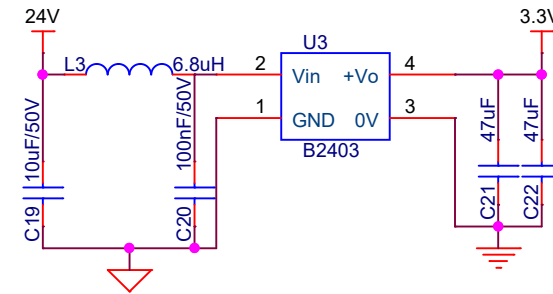
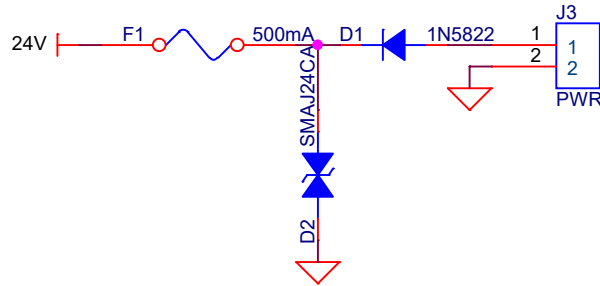
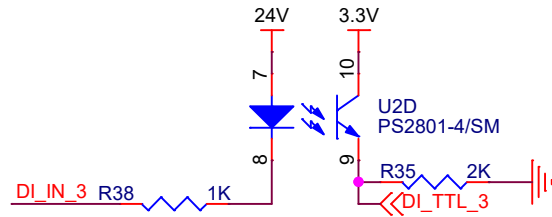
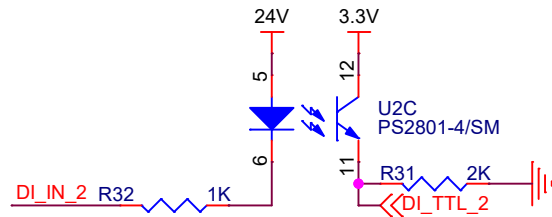
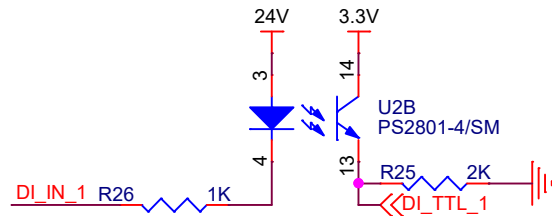
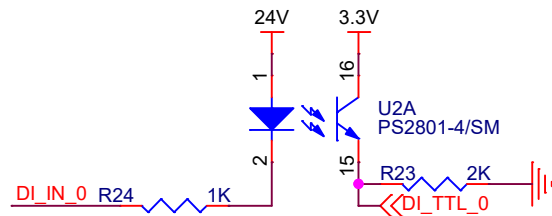


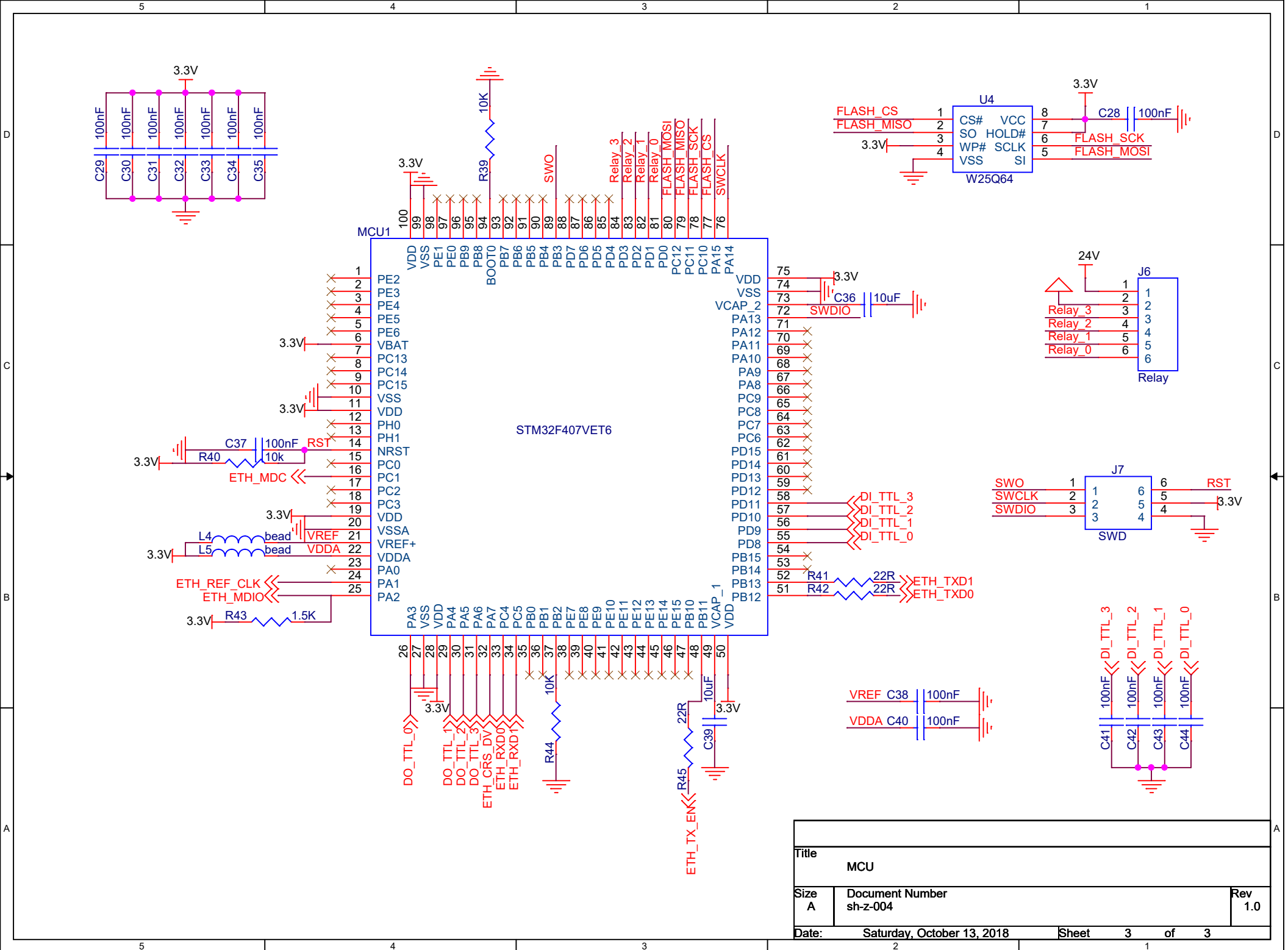
Place components close to KS8873.
Route TX pairs on one side.
Route RX pairs on another side.
Route TX & RX differential pairs close together, 8mil/8mil
parallel' spacing, and keep other signals 20 mil minimum) away



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ETH			
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IO		
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Title		
MCU		
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