

### **EDUCATION**

### Sept 2015-July 2018

UITM Shah Alam Bachelor Engineering (Hons) Electronics Engineering (Microelectronics)

• GPA/ CGPA: 3.79/ 3.78

### June 2012- April 2015

UITM Pulau Pinang Diploma in Electric and Electronics Engineering (Electronics)

GPA/ CGPA: 3.88/ 3.76

#### Jan 2008- Dec 2011

SMK Kulim, Kulim Kedah Sijil Pelajaran Malaysia

Passed

### **AWARDS**

#### 2022

Award: Star Tester for UOBM PLCE Single Source and DMS Integration

#### 2021

Award: Rock Star Performer Awards for Mortgage Repricing Project

### 2019

Award: ANC and First Class Awards in Bachelor Engineering (Hons) Electronics Engineering

### SITI ZURIA SOLEHAH MOHAMAD ZAMRI

BACHELOR ENGINEERING (HONS) ELECTRONIC ENGINEERING

To be successful software tester, and having a working knowledge of software and test design, including capability to run through tests, and the ability to analyze the results.

Permanent Address: 53-1-13, Apartment Pantai Indah, Jalan Pantai Dalam, 59200, KL, Malaysia.

Personal Info: Contacts:

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Gender: Female E-mail: zuriazamri94@gmail.com

Nationality: Malaysian

### **INDUSTRIAL EXPERIENCE**

# QA Tester (Senior Software Enginner) at Cappemini Service (MY) SDN BHD May 2021 - Current

- Working on Web Based tester specialize for Bank client.
- Understand clearly every module specification and CR request and able to write test cases for every module by less review comment.
- Understand thoroughly on requirement and develop test plan, test strategy, test analysis, test execution, and monitoring during product release.
- Design and execute systems to check for issues and Document including maintain test cases
- Perform and document risk analysis and Record test progress including results
- Experience in JIRA environment for report defects. Having good relationship between client and developer team for better discussion to achieve quality assurance for the testing.
- Has being worked with multiple server and environment
- Regularly monitor on testing progress and identify ways to tackle problems to ensure the task completed within allotted time frame.
- Project involved has been awarded with two different awards which are Rock Star Performer and Star Tester.

### QA ENGINEER (Design Engineer) at SONY EMCS (MALAYSIA) SDN BHD (KL) OCT 2019 – May 2021

- Worked as Design Engineer, specialized in Testing Engineer focused on User Interface (UI) with the old/new design Television in User Experience Department for QA purpose.
- Testing to track bug if received market claim.
- Testing new software released to Sony TV.
- Software testing of android devices Nougat, Oreo, Pie, Q and R.
- Troubleshooting hardware and software.
- Automation Developer involved in verification, validation and debug scripting on User Interface (UI) and different UI Translation using Python scripting.
- Executing test cases and report defects JIRA

# Graduate Trainee (GT) at INTEL MICROELECTRONIC (PSG), PG14, PENANG Aug 2018 – Aug 2019

- Worked as Programming Intellectual Protocol Graduate Trainee (GT) in (PIPE) Dept, specialized as Technical Support in IP Logic Design for Altera Rapidio2 and Serial Lite4.
- Involved in design verification, validation and debug with Perl, System Verilog & TCL scripting.
- Involved in several tools for simulator ModelSim and Synopsys. Internal tools Hammer and Perforce.
- Expert on using Quartus tool, to compile and generate IP.
- Involved in IP test and rerunning the test and find the root of causing failure
- Involved in RTL checked LINT and CDC for Altera Rapidio2.
- Has been assigned to create from scratch Spyglass Test for new IP for Lint and CDC test for Serial Lite 4 and make it passing for 19.1, 19.2, and 19.3.

# INTERNSHIP PROGRAM at TNB KULIM DISTRIBUTION SDN BHD July-August 2017

- Duration of internship: 8 weeks.
- •Learned Network and Planning, Studied Operation and Maintenance and Managed Provision and Service.

### TEST TECHNICIAN at CELESTICA SDN BHD (KULIM) MALAYSIA Jun-August 2015

- •Start working from June 2015 till August 2015.
- As Test Technician specialized in debug hardware testing for DC, Functional and Open Short Circuit.
- Expert on finding hardware failure especially in component faults.
- Quit to continue study in Bachelor of Engineering (Hons) Electronic.

### **SOFTWARE SKILLS**

- Python/Perl
- C/ C++
- Shell Script
- Proteus/Multism
- Model Sim
- LT Spice / Ps Spice
- Microsoft Office

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Model Sim.



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### **Personal Skill**

Leadership • • • • • • • •

Communication Skill  $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$ 

Teamwork  $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$ 

Quick Learner

Adaptability & Flexibility lacktriangledown lacktriangledown lacktriangledown lacktriangledown

Strategic Plan  $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$ 

Creativity  $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$   $\bullet$ 

Ability work under pressure

### **LANGUAES**

Malay
Speaking / Writing / Read
English

Speaking / Writing / Read

### Hobbies





### **REFERENCES**

### NISHA VIJAYSHANKAR

Manager

Capgemini Services (MY) Sdn Bhd

15, Gtower, 199, Jln Tun Razak, Kuala Lumpur, 50450 Kuala Lumpur, Wilayah Persekutuan Kuala Lumpur.

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### MR. ARUL PANIANDI

Manager

Intel PG14, Bayan Lepas Industrial Park, 11900 Bayan Lepas, Penang, Malaysia

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### MICHAEL LOPEZ

Manager

Sony EMCS (Malaysia) Sdn Bhd

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Bangi, Selangor

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### MR KUMARANVELU

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