Trasarea execuției programului de test pentru MIPS32

Pas	SW(7:5)	"000"	"001"	"010"	"011"	"100"	"101"	"110"	"111"	De completat numai pentru instrucțiuni de salt	
	Instr (*în asamblare*)	**Instr** (*hexa*)	**PC+4**	**RD1**	**RD2**	**Ext_Imm**	**ALURes**	**MemData**	**WD**	**BranchAddr**	**JumpAddr**
0	lw $2, 0($0)	X"8C020000"	X"00000004"	X"00000000"	-	X"00000000"	X"00000000"	X"00000008"	X"00000008"	-	-
1	lw $4, 4($0)	X"8C040004"	X"00000008"	X"00000000"	-	X"00000004"	X"00000004"	X"0000000A"	X"0000000A"	-	-
2	addi $5, $0, 1	X"20050001"	X"0000000C"	X"00000000"	-	X"00000001"	X"00000001"	-	X"00000001"	-	-
3	addi $6, $0, 1	X"20060001"	X"00000010"	X"00000000"	-	X"00000001"	X"00000001"	-	X"00000001"	-	-
4	add $1, $0, $0	X"00000820"	X"00000014"	X"00000000"	X"00000000"	-	X"00000000"	-	X"00000000"	-	-
5	beq $1, $4, 13	X"1024000D"	X"00000018"	X"00000000"	X"0000000A"	X"0000000D"	X"00000000"	-	-	X"0000004C"	-
6	slti $9, $1, 2	X"28290002"	X"00000001C"	X"00000000"	-	X"00000002"	X"00000001"	-	X"00000001"	-	-
7	beq $9, $0, 3	X"11200003"	X"00000020"	X"00000001"	X"00000000"	X"00000003"	X"00000001"	-	-	-	-
8	addi $7, $0, 1	X"20070001"	X"00000024"	X"00000000"	-	X"00000001"	X"00000001"	-	X"00000001"	-	-
9	j 18	X"08000012"	X"00000028"	-	-	-	-	-	-	-	X"00000048"
10	sll $10, $7, 3	X"000750C0"	X"0000002C"	X"00000001"	X"00000003"	-	X"00000008"	-	X"00000008"	-	-
11	add $8, $1, $0	X"00204020"	X"00000030"	X"00000000"	X"00000000"	-	X"00000000"	-	X"00000000"	-	-
12	sll $8, $8, 2	X"00084100"	X"00000034"	X"00000000"	X"00000002"	-	X"00000000"	-	X"00000000"	-	-
13	add $8, $2, $8	X"00484020"	X"00000038"	X"00000008"	X"00000000"	-	X"00000008"	-	X"00000008"	-	-
14	sw $10, 0($8)	X"AD0A0000"	X"0000003C"	X"00000008"	X"00000008"	X"00000000"	X"00000008"	-	-	-	-
15	addi $1, $1, 1	X"20210001"	X"00000040"	X"00000000"	-	X"00000001"	1	-	X"00000001"	-	-
16	j 5	X"08000005"	X"00000044"	-	-	-	-	-	-	-	X"00000014"
17	Sare inapoi la linia 5 ...										

PC+4 + 4*13 = 76 => 4C IN HEX

offset-ul este 13

se inmulteste cu 4 pentru ca fiecare instructiune are 4 bytes