Semnale de control MIPS32

Instrucțiune	**Opcode** *Instr[31-26]*	**Reg Dst**	**ExtOp**	**ALUSrc**	**Branch**	**Br_?** (opțional)	**Jump**	**JmpR** (opțional)	**Mem Write**	**Memto Reg**	**Reg Write**	**ALUOp[1:0]**	**function** *Instr[5-0]*	**ALUCtrl[2:0]**
add	000000	1	x	0	0		0		0	0	1	10	100000	000 (+)
sub	000000	1	x	0	0		0		0	0	1	10	100010	100 (-)
sll	000000	1	x	0	0		0		0	0	1	10	000000	010 (<</)
srl	000000	1	x	0	0		0		0	0	1	10	000010	011 (>>/)
or	000000	1	x	0	0		0		0	0	1	10	100101	010 (|)
and	000000	1	x	0	0		0		0	0	1	10	100100	001 (&)
xor	000000	1	x	0	0		0		0	0	1	10	100110	101 (^)
ori	001101	0	0	1	0		0		0	0	1	11	-	010 (|)
addi	001000	0	1	1	0		0		0	0	1	00	-	000 (+)
slti	001010	0	1	1	0		0		0	0	1	11	-	111 (<)
lw	100011	0	1	1	0		0		0	1	1	00	-	000 (+)
sw	101011	x	1	1	0		0		1	x	0	00	-	000 (+)
beq	000100	x	1	0	1		0		0	x	0	01	-	100 (-)
bgez	000001	x	1	0	1		0		0	x	0	01	-	011 (>>/)
bne	000101	x	1	0	1		0		0	x	0	01	-	100 (-)
j	000010	x	x	x	X		1		0	x	0	xx	-	xxx