

# Wensheng Zheng

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## Work Interest

Particularly interested in data science, machine learning, and LLM, with a focus on machine learning and model optimization. Seeking to deepen my knowledge of data analysis, model training through academic and industry-driven research.

## Education

### Yonsei University

Seoul, South Korea

B.S. in Computer Science

2021.03 - 2025.03

- Core Courses: Data Structures, Algorithm Analysis, Computer Vision, Compiler Design, Automata and Formal Languages, Discrete Mathematics, Logic Circuit Design, Computer Architectures, Database Management Systems, Operating System, Computer Networks, Machine Learning, Information Security
- IELTS: 7.0

## Research Experience

### AI Acceleration with Deep Learning Compiler

2024.09 - 2024.12

Graduation Project 2; Supervisor: Prof. YongJun Park

- Developed simple AI model accelerations on Nvidia Jetson Xavier. Writing benchmarks of models' performance on raw CPUs, raw GPUs, and GPUs with acceleration frameworks.
- Code in Python, C, and bash scripting.

### Synthetic Data Generation: Synthesizer and Evaluation

2024.03 - 2024.05

Graduation Project 1; Supervisor: Prof. Won-Suk Lee

- Developed synthetic data generator based on Marginal distributions and data analyzer with result visualization tools.
- Code in Python, with mathematical libs.

## Selected Course Projects

### Computer System

2024.09 - 2024.12

- CSAPP labs (Bomb lab, Buffer overflow attacks, Hardware acceleration), and a naive implementation of Linux find() using unix I/O.

### Operating System

2024.03 - 2024.06

- CPU scheduling (FIFO, RR, MLFQ), mmap/munmap System call, Multithread support.

### Compiler Design

2023.09 - 2023.12

- Frontend of a naive C compiler, with lexical analysis, syntax analysis, semantic analysis, and code generation to Java bytecode.

### Computer Networks

2023.09 - 2023.12

- A simple proxy HTTP server, implementing URL filtering and image filtering.

### Computer Architectures

2023.03 - 2023.06

- 5-staged pipelined CPU, which breaks down the execution of a MIPS instruction into instruction fetch (IF), instruction decode (ID), execute (EX), data memory access (MEM) and writeback (WB).

## Skills

- Python, Bash, Linux, Shell command.