

MOSFET Amplifier Characteristics

AIM:

To verify the frequency response analysis of single stage MOSFET using LTspice software

APPARATUS REQUIRED:

LTspice software

PROCEDURE:

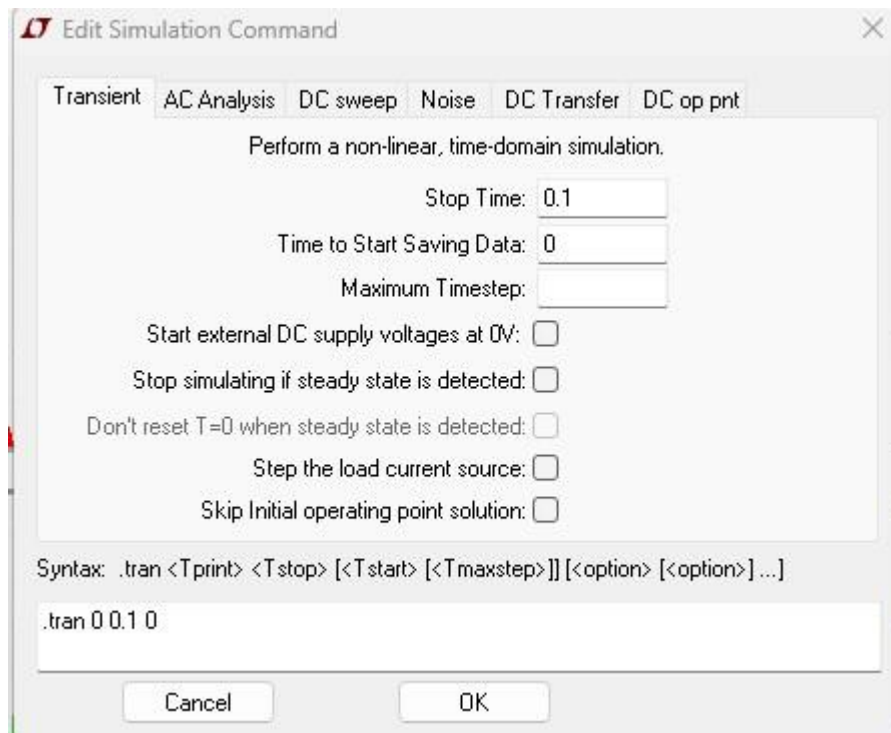
Using the components available in LTspice a circuit is build that is used to verify three types of frequency response analysis:

- i) AC analysis
- ii) Transient analysis

Procedure:

- Design the circuit with necessary components as per the circuit diagram.
- Assign the NMOS transistor Q1 as Si7336ADP.
- Assign the values to the capacitors as C1 and C2 as 0.12u.
- Assign the values to the resistors as R1 = 470, R2 = 150, R3 = 200k and R4 = 100k.
- Assign the V1 Voltage values in SINE function as DC Offset [V] = 0 Amplitude [V] = 0.05m Frequency [Hz] = 100.
- Assign the V2 voltage as 15.
- Select DC op point and run the file.
- Then, in AC Analysis give, Type of Sweep as Decade No. of Points as 10 Start Frequency as 10m Stop Frequency as 1Meg.
- Then, in Transient Analysis give stop time as 10m, Time to start saving data as 0. Save the waveforms of the given circuit in all the analysis given above and calculate the bandwidth of the amplifier.

Command for Transient operating point:



The image shows a dialog box titled "Edit Simulation Command" with a close button (X) in the top right corner. It contains several tabs: "Transient", "AC Analysis", "DC sweep", "Noise", "DC Transfer", and "DC op pnt". The "Transient" tab is selected. Below the tabs, the text "Perform a non-linear, time-domain simulation." is displayed. There are five input fields: "Stop Time:" with the value "0.1", "Time to Start Saving Data:" with the value "0", "Maximum Timestep:" (empty), "Start external DC supply voltages at 0V:" with an unchecked checkbox, "Stop simulating if steady state is detected:" with an unchecked checkbox, and "Don't reset T=0 when steady state is detected:" with an unchecked checkbox. Below these are two more checkboxes: "Step the load current source:" (unchecked) and "Skip Initial operating point solution:" (unchecked). At the bottom, the syntax ".tran <Tprint> <Tstop> [<Tstart> [<Tmaxstep>]] [<option> [<option>] ...]" is shown. Below the syntax is a text box containing ".tran 0 0.1 0". At the very bottom are "Cancel" and "OK" buttons.

Transient AC Analysis DC sweep Noise DC Transfer DC op pnt

Perform a non-linear, time-domain simulation.

Stop Time: 0.1

Time to Start Saving Data: 0

Maximum Timestep:

Start external DC supply voltages at 0V: ☐

Stop simulating if steady state is detected: ☐

Don't reset T=0 when steady state is detected: ☐

Step the load current source: ☐

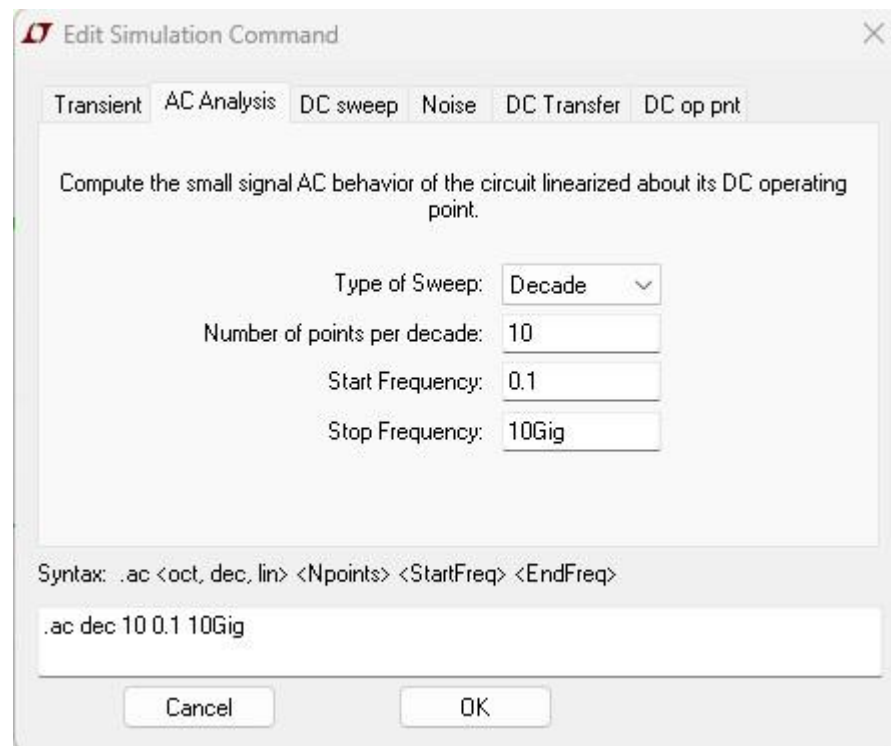
Skip Initial operating point solution: ☐

Syntax: .tran <Tprint> <Tstop> [<Tstart> [<Tmaxstep>]] [<option> [<option>] ...]

.tran 0 0.1 0

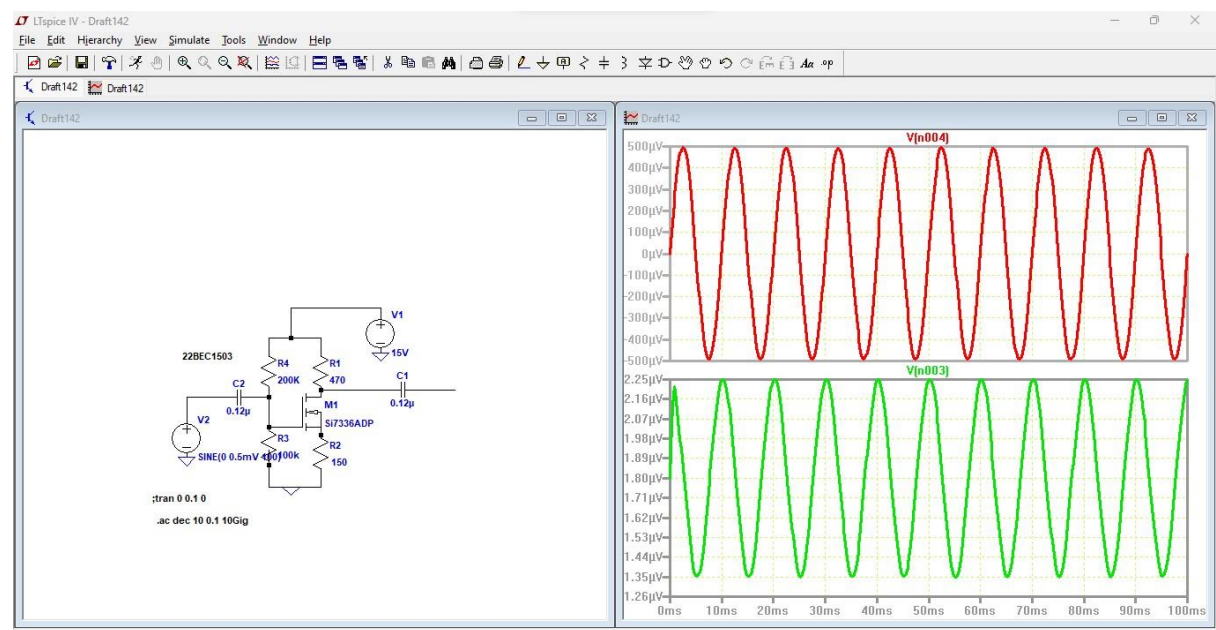
Cancel OK

Command for AC analysis:



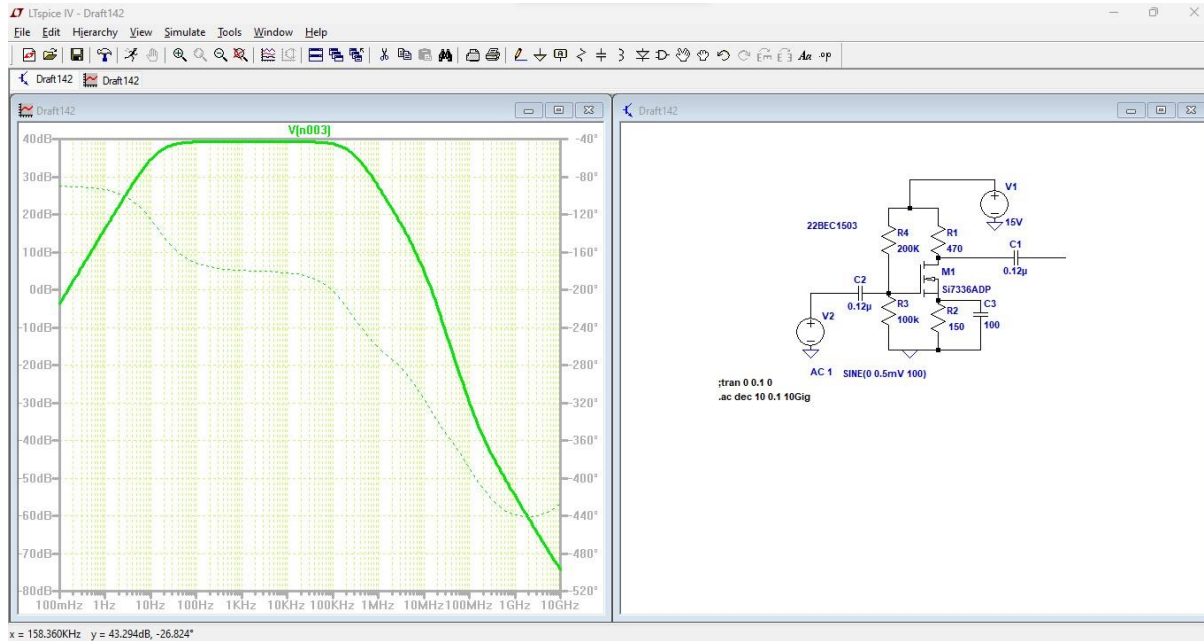
Circuit and output:

Transient response:

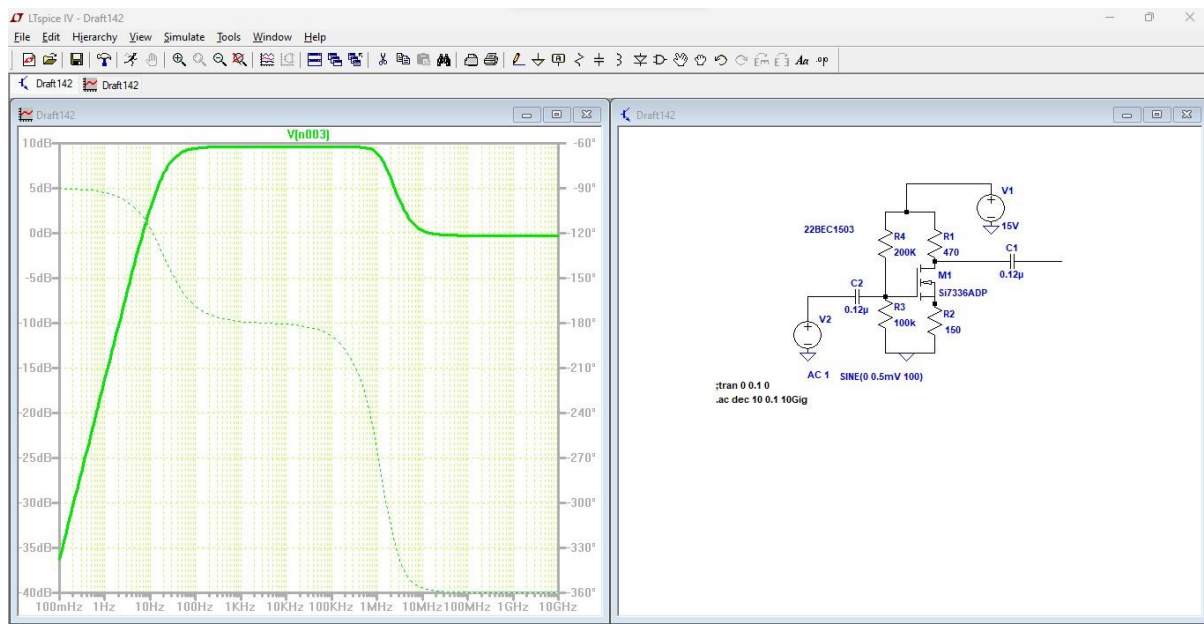


AC response:

i) With capacitor:



ii) Without capacitor:



Result:

The frequency response of single stage MOSFET has been verified using LTSPICE software. The phase shift in transient analysis is 180 . The bandwidth of the amplifier circuit in ac analysis is 1.82 MHz.