HARISH PUVVADA
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EDUCATION

New York University - Tandon School of Engineering, New York, NY — May 2018
Master of Science in Computer Engineering, GPA: 3.67/4

Amrita University, Bangalore, India — May 2015
Bachelor of Technology in Electronics and Instrumentation, GPA: 8.18/10

COURSE WORK

Internet Architecture and Protocols	Data Center and Cloud Computing	Computer Architecture
Design and Analysis of Algorithms	Real Time Embedded Systems	Introduction to Operating Systems

SKILLS

Tools and Technologies: Wireshark, Arduino, MATLAB (Robotics Toolbox), Keil, AVR studio, Verilog, Qualtrics
Programming Languages: C, C++, Java, JavaScript, PHP, Python, R
Platforms: Windows (XP, win7, win8, win10), Linux
Databases: PL/SQL, MySQL
Web & Application Servers: Apache, JBoss, Tomcat

EXPERIENCE

NYU Technology Management and Innovation Department, Brooklyn, NY — September 2016 - April 2017
Graduate Assistant

- Design and develop a Web application using PHP and MySQL for Human Computer Interaction research.

Tata Consultancy Services Ltd, Mumbai, India — June 2015 - July 2016
Associate Systems Engineer

- Involved in database administration using combination of Java and MySQL and for more than 150 clients.
- Resolved application issues and optimized code in collaboration with the development team.

Amrita Robotics Research Center, Bangalore, India — November 2014 - May 2015
Research Assistant

- Designed and constructed anthropomorphic arms from scratch.
- Developed a coordination algorithm for the arms using MATLAB and Arduino to perform a task in a common workspace.

Indo Global Services, Hyderabad, India — December 2013 - January 2014
Intern

- Developed efficient and cost effective Home Automation Systems.
- Assisted in conducting embedded systems workshops for freshmen at several colleges.

PROJECTS

Team Up — January 2017 - March 2017

- Designing and developing a crowdsourcing platform using Django, Python and JS
- Professors and Students of a university can post about their ideas or projects and collaborate for working on the projects.

MIPS Processor — September 2016 - December 2016

- Implemented an instruction-level simulator for Single cycle MIPS processor using C++.
- Simulator can support a subset of the MIPS instruction set and model the execution of each instruction.

Network Traffic Analysis using SDN — January 2017 - March 2017

- Programmed RYU controller and Open vSwitch to install flows/rules for a fat tree topology.
- Created topologies using mininet to direct traffic between hosts at desired bandwidths.
- Set different policies through controller to block/allow traffic between hosts in the network.

Internet Architecture and Protocols — September 2016 - December 2016

- Analysis of TCP, UDP packets using Wireshark.
- Configured Bridges and Routers using Cisco IOS
- Analyzed dynamic implementation of Apache server, Network Time Protocol and Network Address Translation.