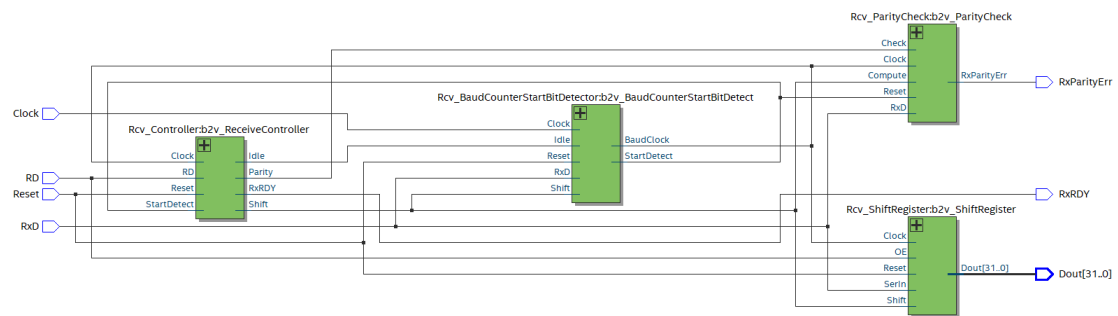
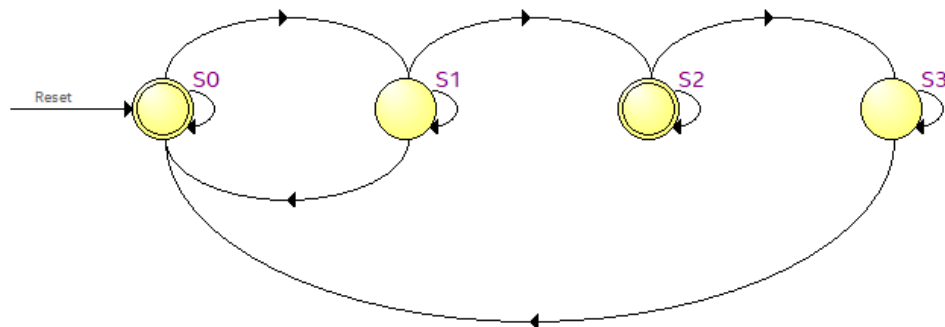


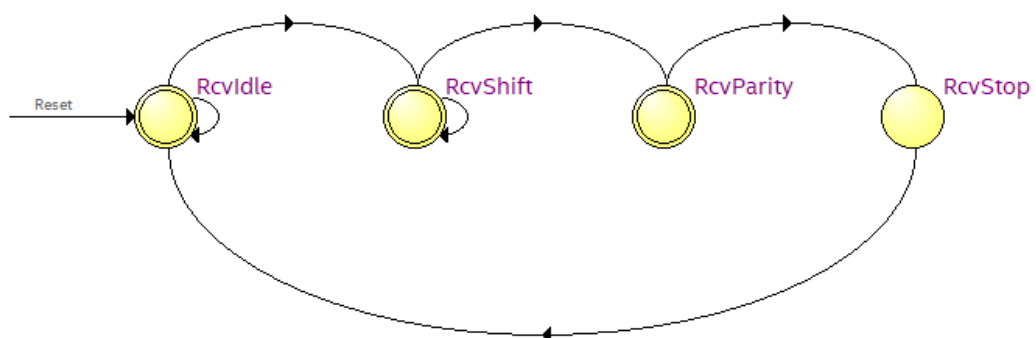
Synthesized Schematic:



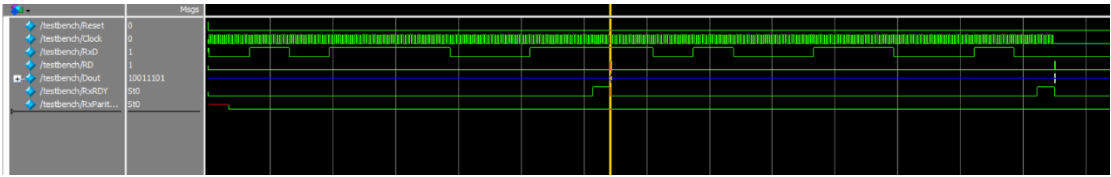
Start bit detector's FSM



Controller's FSM



Testbench Result for 10011101:



Testbench Result for 10011001:

