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Drafted: Matthew Guo

Paisley Microsystems

Sheet: /

File: proto.kicad_sch

Title: Root Layout

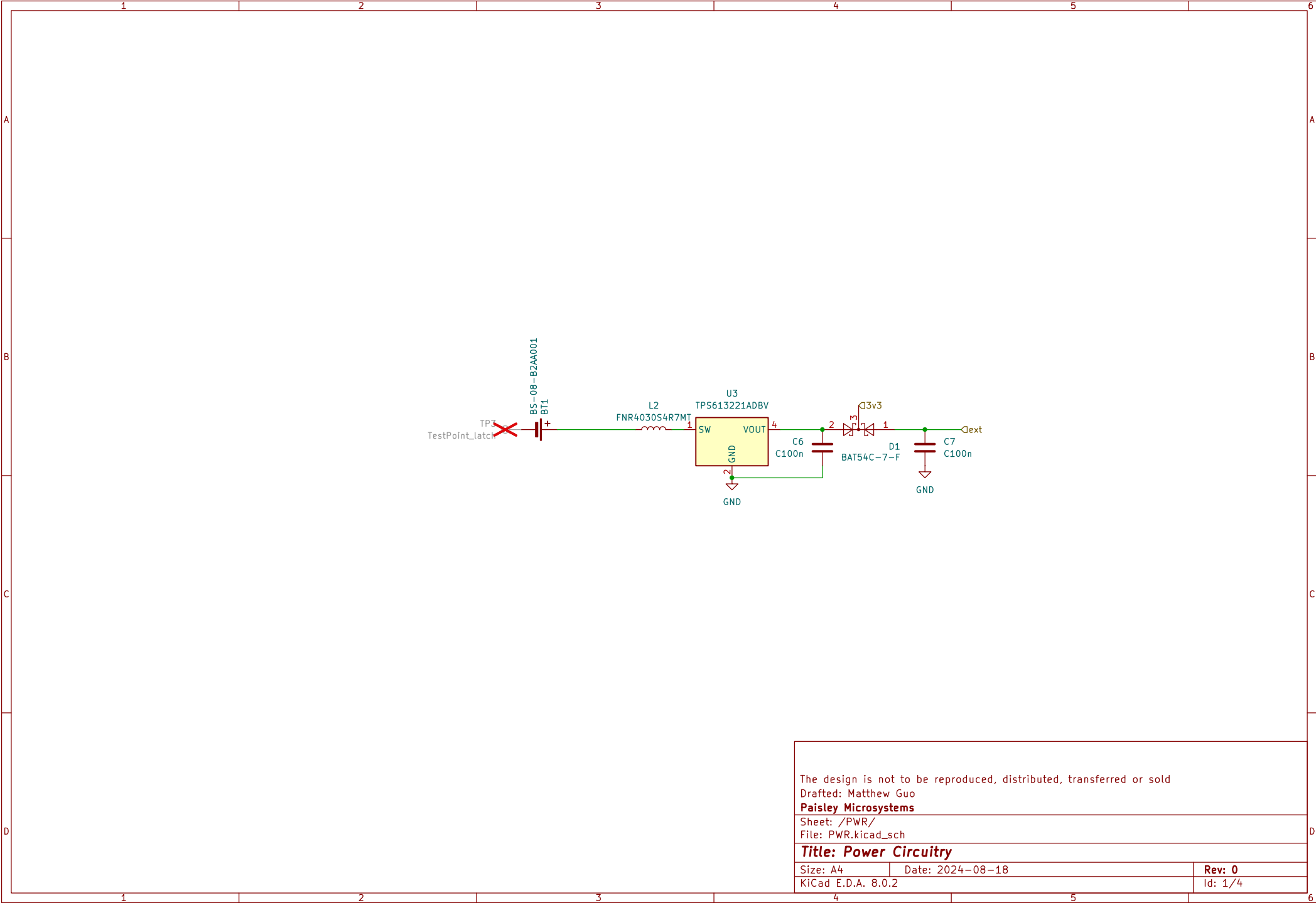
Size: A4

Date: 2024-08-18

Rev: 0

KiCad E.D.A. 8.0.2

Id: 1/4



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Sheet: /PWR/

File: PWR.kicad_sch

Title: Power Circuitry

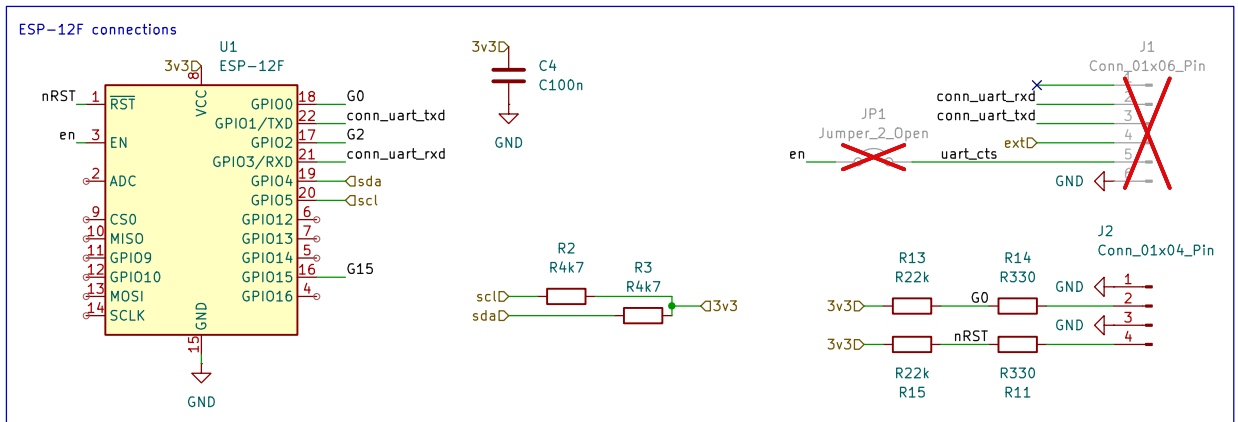
Size: A4

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Id: 1/4



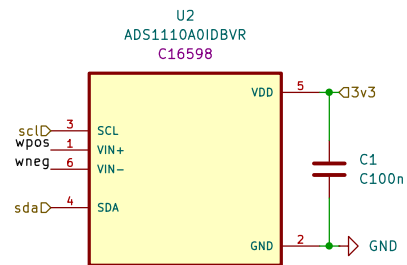
Id: 1/4

Diff. amplifier

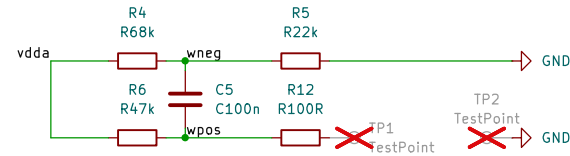
FSD = 2.048 @ PGA = 1

FSR = 2.048V, $-0.2V < V_{CM} < 3.5V$

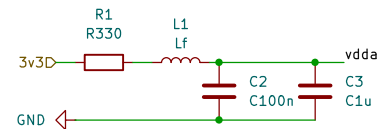
CMV = $-0.2 \text{ --- } 3.5V$



RP1P2 = 0 \rightarrow VDIFF = $-1.334V$
RP1P2 = $\infty \rightarrow$ VDIFF = $1.951V$



VDDA Filter



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Sheet: /DIFFAMP/

File: DIFFAMP.kicad_sch

Title: DIFFAMP

Size: A4 Date: 2024-08-18

KiCad E.D.A. 8.0.2

Rev: 0

Id: 1/4