Pre-Lab 6

Part I

2) Synchronous reset, active low. In simulation, need to set SW[0] to 0 early.

3)

|  |  |  |
| --- | --- | --- |
| **Current state** | **Input** | **Next state** |
| A | 0 | A |
| A | 1 | B |
| B | 0 | A |
| B | 1 | C |
| C | 0 | E |
| C | 1 | D |
| D | 0 | E |
| D | 1 | F |
| E | 0 | A |
| E | 1 | G |
| F | 0 | E |
| F | 1 | F |
| G | 0 | A |
| G | 1 | C |