PRODUCT: TFT TOUCH MODULE

MODULE NO.: CT080XGA004-WCT

DATE: Nov 27, 2019

SPECIFICATION

Revision: 0.0

CT080XGA004-WCT

This module uses ROHS material

This specification may change without prior notice in order to improve performance or quality. Please contact us for updated specification and product status before design for this product or release of this order.

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REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
0.0	2019-11-27	First release	Preliminary

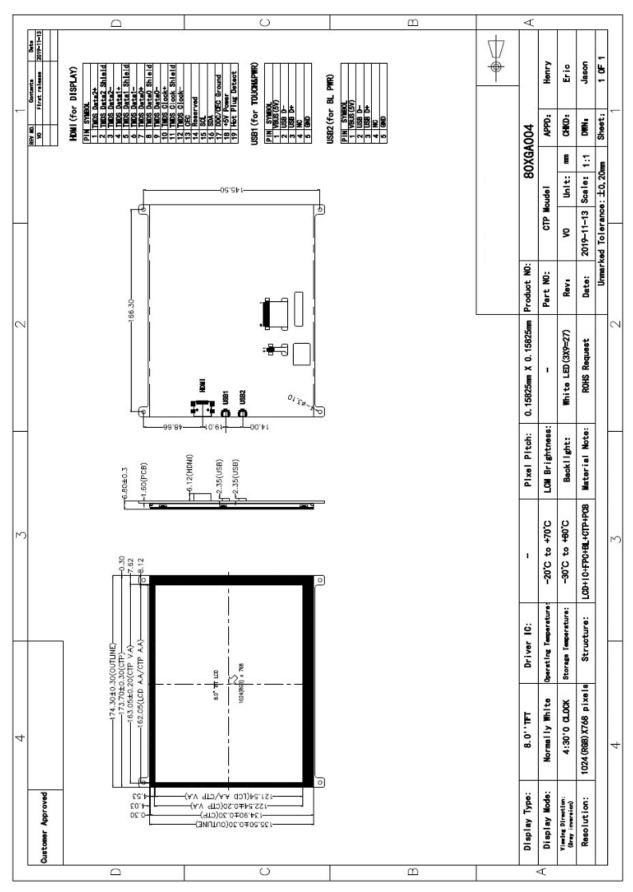
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1. GENERAL INFORMATION

Item of general information		Unit		
LCD Display Size (Diagonal)		inch		
Module Structure	LCD Displ	LCD Display + CTP Touch + PCB		
LCD Display Type		TFT	-	
LCD Display Mode	Λ	Jormally White	-	
Recommended Viewing Direction	4:30 o'clo	ck(Gray level inversion)		
Module size $(W \times H \times T)$	174.	30×135.50×6.80	mm	
Active area (W×H)	1	62.05×121.54	mm	
Number of pixels (Resolution)	1	pixel		
Pixel pitch (W×H)	0	15825×0.15825	mm	
Color Pixel Arrangement		RGB Stripe	-	
	LCD Display	HDMI interface	-	
Module Interface Type	CTP Touch	USB interface	-	
	Win7/Wind	8/Win10(Plug and play)	-	
System Support	Android/Linux (need to be configured first)		-	
Power Supply	USB (5.0V)		-	
Module Power consumption	USB1(300mA)+USB2(450mA)		mA	
Color Numbers		-		
Backlight Type		White LED	-	

2, EXTERNAL DIMENSIONS



3. ABSOLUTE MAXIMUM RATINGS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
Operating temperature	Тор	-20	70	${\mathscr C}$
Storage temperature	Tst	-30	80	${}^{\!$
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings means the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

4, ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

Parameter of DC characteristics	Symbol	Min.	Тур.	Max.	Unit
PCB operating voltage	VUSB	-	5.0	-	V
LCD I/O operating voltage	VDD	3.0	3.3	3.6	V
Input voltage 'H' level	VIH	0.7*VDD	-	VDD	V
Input voltage 'L' level	VIL	VSS	-	0.3*VDD	V
Output voltage 'H' level	VOH	VDD-0.4	-	VDD	V
Output voltage 'L' level	VOL	VSS	-	VSS+0.4	V

5, CTP CHARACTERISTICS

Item of CTP characteristics	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	1024 × 768	pixel	-
Surface Hardness	<i>≥6H</i>	-	-
Transparency	>82%	-	-
Driver IC	-	-	-
Interface Type	USB	-	-
Support Points	5	-	-
Sampling Rate	100	Hz	-
Supply voltage	3.3	V	-

6, ELECTRO-OPTICAL CHARACTERISTICS

Item o electro-op character	otical	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	Note
Response	time	Tr+Tf	0.0	-	20	40	ms	FIG 1.	4
Contrast I	Ratio	CR	$\theta=0$ $\varnothing=0$	-	400	-	-	FIG 2.	1
Luminance un	Luminance uniformity		<i>Ta=25</i> °C	-	80	-	%	<i>FIG 2</i> .	3
Surface Lum	inance	Lv	10. 20	-	200	-	cd/m2	<i>FIG 2</i> .	2
CIE (x, y)		White x	$\theta = 0$	1	0.302	-			
chromaticity	White	White y	Ø=0 Ta=25°C	1	0.338	-	1	FIG 2.	5
	Ø=90(1	2 o'clock)		1	70	-	deg		
	Ø=270((6 o'clock)	CD > 10	-	70	_	deg	EIC 2	
	Ø=0(3	o'clock)	<i>CR</i> ≥ 10	-	80	_	deg	FIG 3.	6
	Ø=180(9 o'clock)		-	80	-	deg		
NTSC ratio		-	-	-	-	_	%	-	-

Note 1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

 $Contrast\ Ratio(CR) = \frac{Average\ Surface\ Luminance\ with\ all\ white\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}{Average\ Surface\ Luminance\ with\ all\ black\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv=Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5,P6,P7,P8,P9)

Note 3. The uniformity in surface luminance (SWHITE) is determined by measuring luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

 $\delta \text{WHITE} = \frac{Minimum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5, P6, P7, P8, P9)}{Maximum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5, P6, P7, P8, P9)}$

Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1.

Note 5. CIE (x, y) chromaticity, The x,y value is determined by screen active area position 5. For more information see FIG 2.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

Note 8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

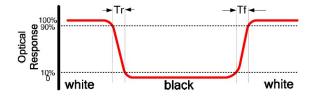


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE(x, y) chromaticity

A:H/6;

B: V/6;

H,V: Active Area(AA) size

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

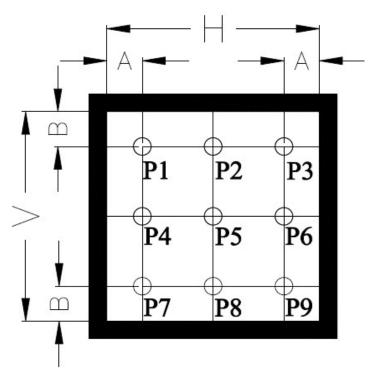
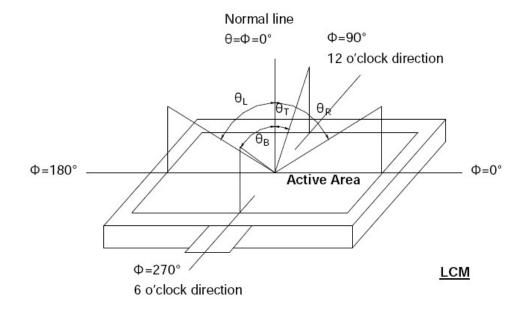


FIG.3. The definition of viewing angle



7. INTERFACE DESCRIPTION

A. HDMI Interface Description

NO.	Symbol	DESCRIPTION
1	TMDS Data2+	Positive side of channel 2 TMDS low-voltage signal differential input pair
2	TMDS Data2 Shield	Ground
3	TMDS Data2-	Negative side of channel 2 TMDS low-voltage signal differential input pair
4	TMDS Data1+	Positive side of channel 1 TMDS low-voltage signal differential input pair
5	TMDS Data1 Shield	Ground
6	TMDS Data1-	Negative side of channel 1 TMDS low-voltage signal differential input pair
7	TMDS Data0+	Positive side of channel 0 TMDS low-voltage signal differential input pair
8	TMDS Data0 Shield	Ground
9	TMDS Data0-	Negative side of channel 0 TMDS low-voltage signal differential input pair
10	TMDS Clock+	Positive side of reference clock. TMDS low-voltage signal differential input pair
11	TMDS Clock Shield	Ground
12	TMDS Clock-	Negative side of reference clock. TMDS low-voltage signal differential input pair
13	CEC	No Connection
14	Reserved(N.C.)	No Connection
15	SCL	DDC SCL
16	SDA	DDC SDA
17	DDC/CEC Ground	Ground
18	+5V Power	+5V Power
19	Hot Plug Detect	Hot Plug Detect

B . USB1 Interface Description

NO.	Symbol	DESCRIPTION
1	VUSB	USB Power
2	D-	USB Data-
3	D+	USB Data+
4	NC	No connection
5	GND	Power Ground

C. USB2 Interface Description

NO.	Symbol	DESCRIPTION
1	VUSB	USB Power
2	NC	No connection
3	NC	No connection
4	NC	No connection
5	GND	Power Ground

Application Note: Please connect the USB first, and then connect the HDMI interface.

8, LCD TIMING

Danam atau	Corob of		I 724		
Parameter	Symbol	Min.	Тур.	Max.	Unit
DCLK frequency	DCLK	52	65	71	MHz
Horizontal display area	thd	1024			DCLK
1 Horizontal Line	th	1114	1344	1400	DCLK
HSYNC Blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd	768			Н
VSYNC period time	tv	778	806	845	Н
VSYNC Blanking	tvb+tvfp	10	38	77	Н

9、RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	80°C/120 hours
2	Low Temperature Storage	-30°C/120 hours
3	High Temperature Operating	70°C/120 hours
4	Low Temperature Operating	-20°C/120 hours
5	Temperature Cycle Storage	-20°C(30min.)~25(5min.)~70°C(30min.)×10cycles

A. Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- ➤ Air bubble in the LCD;
- > Sealleak;
- ➤ Non-display;
- Missing segments;
- ➤ Glass crack;
- Current is twice higher than initial value.

B, Remark:

- The test samples should be applied to only one test item.
- Sample size for each test item is $5\sim10$ pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

10, INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 3.5 inch.

10.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

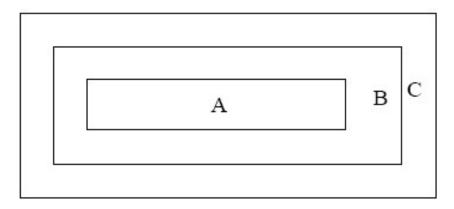
Minor defect: AQL 1.5

10.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of $20\sim40W$ light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature $20\sim25$ ° Cand normal humidity 60 $\pm15\%$ RH)

10.3 Definition of Inspection Item.

A. Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

B. Definition of some visual defect

	Because of losing all or part function, bad pixel dots appear bright and the					
Bright dot	size is more than 50% of one dot in which LCD panel is displaying under					
	black pattern.					
Dark dot	Dots appear dark and unchanged in size in which LCD panel is displaying					
	under pure red, green, blue picture, or pure whiter picture.					

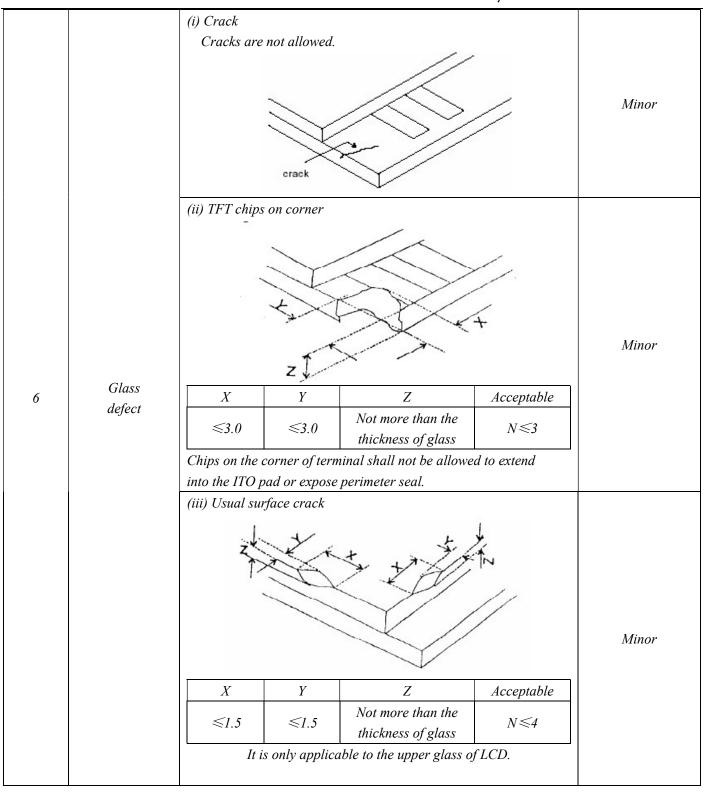
10.4 Major Defect

Item No.	Items to be inspected	Inspection standard	Classification of defects
1	Functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6)Backlight no lighting, flickering and abnormal lighting	major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	

10.5 Minor Defect

Item No.	Items to be	Inspection standard						Classification of defects
1	Bright dot /dark dot defect	Dark pixel dots 2bright dots adjace 2dark dots adjace Total bright and dots Note: Minimum distace Pixel dots' function	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$				Acceptable 5mm;	Minor
2	Dot defect	Zone $Size(mm)$ $\Phi \leq 0.2$ $0.2 < \Phi \leq 0.5$ $\Phi > 0.5$ Note: 1. Minimum distance	3.5"~7" Acceptable 4 0	Acceptable Qty $A+B$ $7" 7\sim 10.1" >10.1" C$		○ Acceptable	Minor	
3	Linear defect	Zone Acceptable Qty Size (mm) $A+B$ Length Width $3.5"\sim7"$ $7\sim10.1"$ $>10.1"$ C Ignore $W\leq0.05$ Acceptable Acceptable Acceptable Acceptable $L \leq 5.0$ $0.05 <$ W ≤ 0.1 4 5 6 be $L > 5.0$ $W > 0.1$ 0 0 0				Minor		

		5.4.1 Pola (i) Shiftin dimension (ii) Incor allowed. 5.4.2 Dirt Dirt which						
		Zone		Acceptable Qty A+B				
		Size(mm))	3.5"~7" 7~10.1" >10.1"				
		Ф	≤0.2	Acceptable	Acceptable	Acceptable	Acc	
		0.2 <	<i>Φ</i> ≤0.5	4	5	6	Acceptable	
4	Polarizer defect	Φ	>0.5	0	0	0	ıble	Minor
		(i) If the polarizer scratch can be seen after cover assembling or in the operating condition, judge by the linear defect of 5.3. (ii) If the polarizer scratch can be seen only in non-operating condition or some special angle, judge by the following:						
		Length	Width	3.5"~7"	7~10.1"	>10.1"	С	
		Ignore	<i>W</i> ≤0.05	Acceptable	Acceptable	Acceptable	_	
		1.0 <l ≤5.0</l 	0.05 < W≤0.20	4	5	6	Acceptab	
		L>5.0	W>0.2	0	0	0	able	
5	MURA	Using						
	White/Black dot (MURA)	V. 0.13	Minor					



10.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects
1	Difference in Spec.	Not allowable	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3		No soldering missing	Major
	Soldering defects	No soldering bridge	Major
		No cold soldering	Minor
4	Resist flaw on PCB	Visible copper foil (Φ 0.5 mm or more) on substrate pattern is not allowed	Minor
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed Φ 0.2mm)	Minor
9	Stain	No stain to spoil cosmetic badly	Minor
10	Plate discoloring	No plate fading, rusting and discoloring	Minor
	I. I and moute	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor
	1. Lead parts	b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor
	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder.	Minor
11	3. Chips	Minor	
	4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \ge 0.13$ mm. The diameter of solder ball $d \le 0.15$ mm.	Minor
		b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm2.	Minor
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major