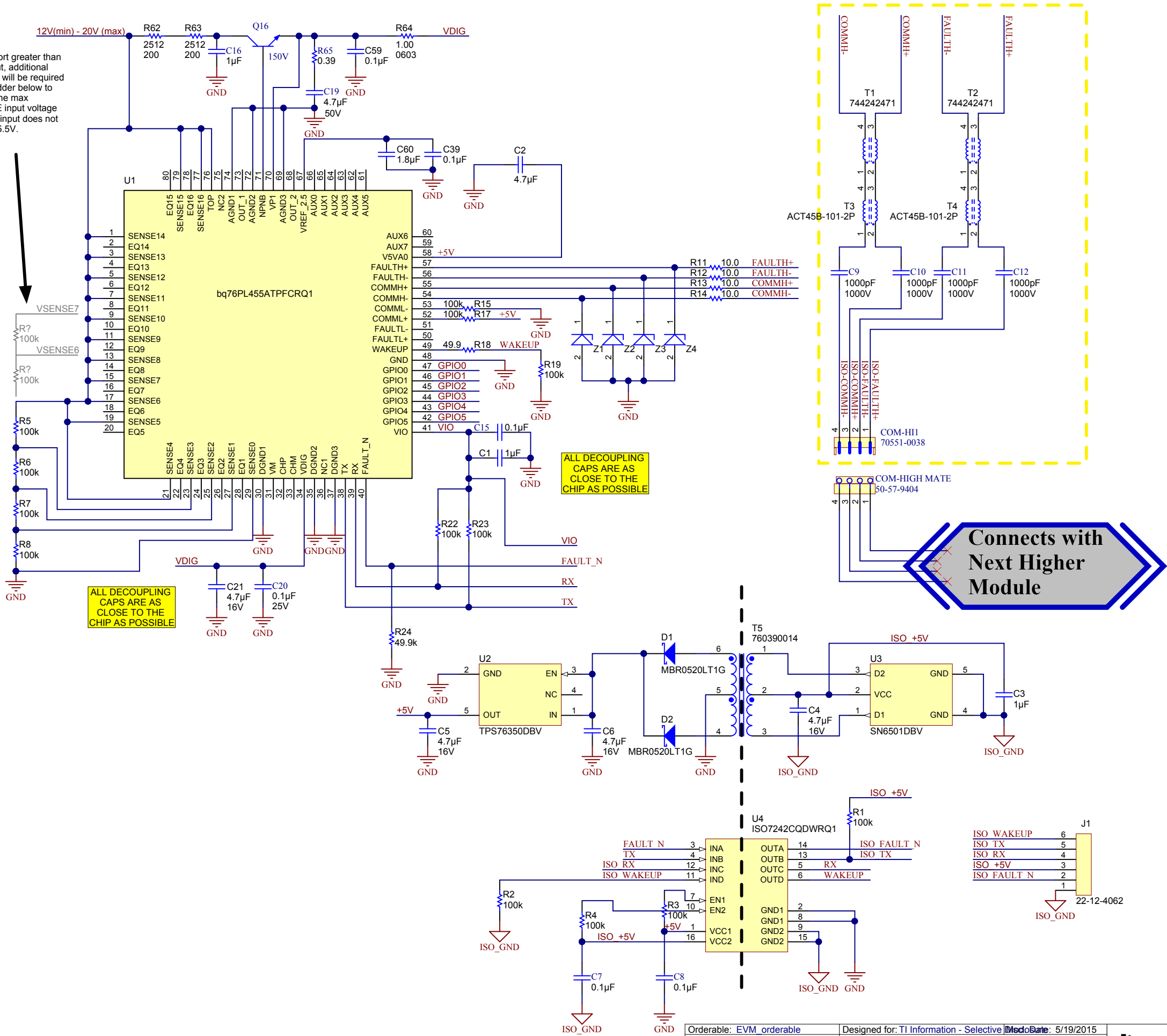


Revision History	
Revision	Notes
05/05/2015	First release

To support greater than 20V input, additional resistors will be required in the ladder below to ensure the max VSENSE input voltage on each input does not exceed 5.5V.



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.