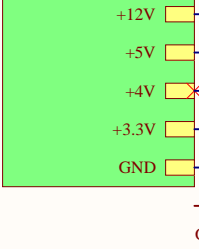
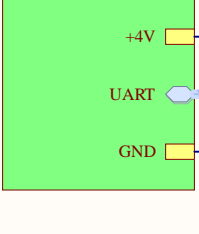


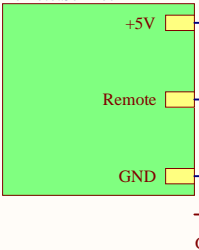
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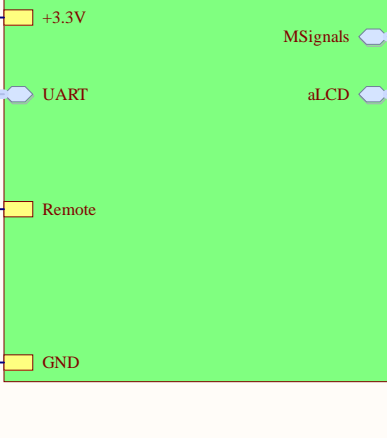
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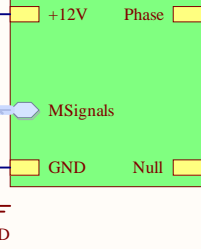
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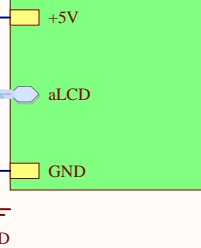
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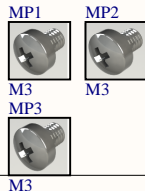
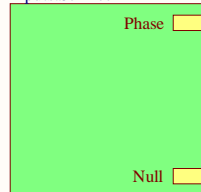
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Outputs.SchDoc



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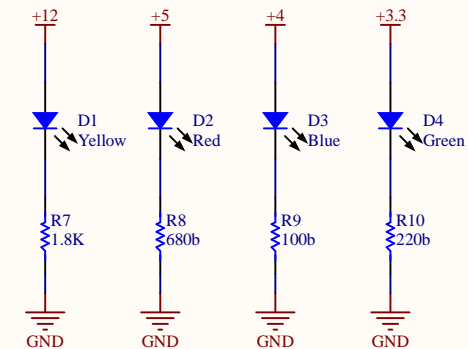
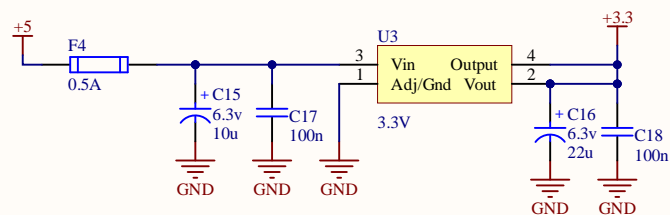
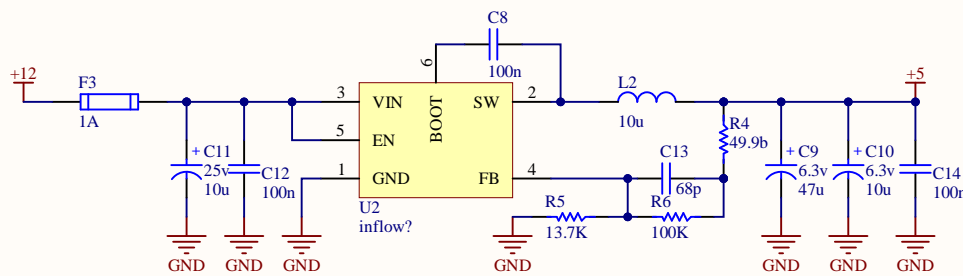
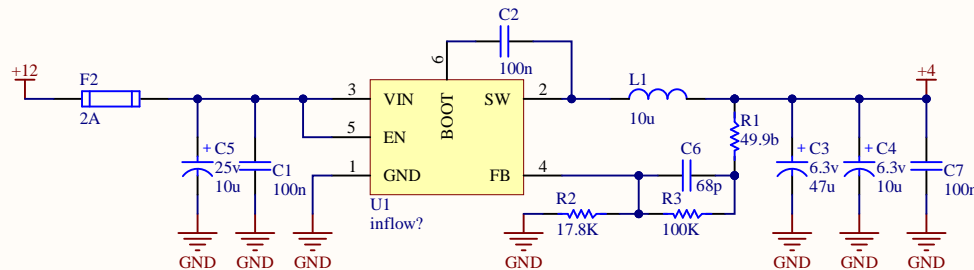
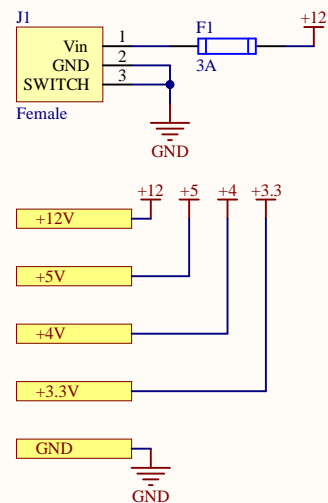


U_Inputs
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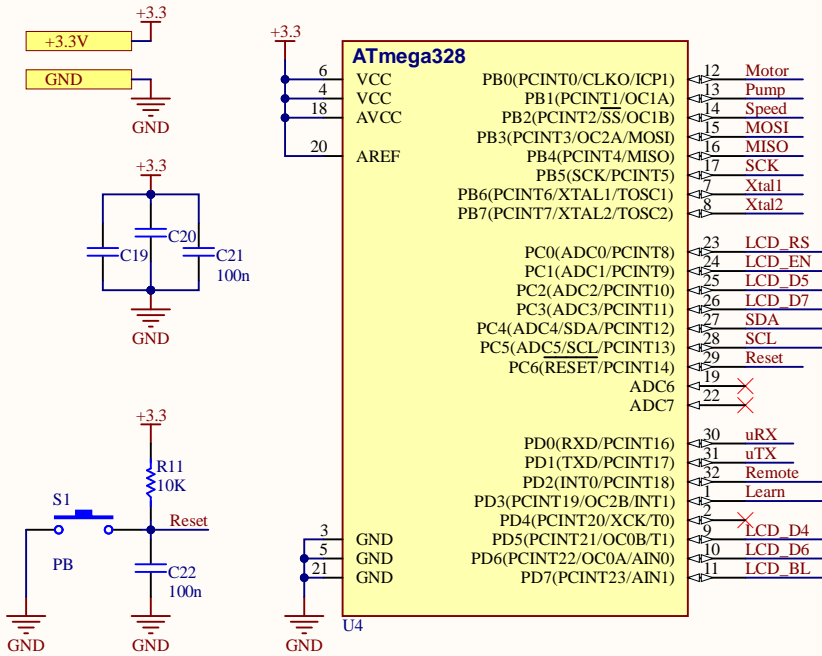


Title: Main	Drawn by: Hossein Bagheri
Project: AVR RCS	Revision: 1.0
Company: github.com/aKaReZa75	Sheet 1 of 8

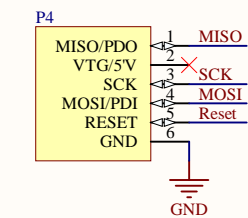




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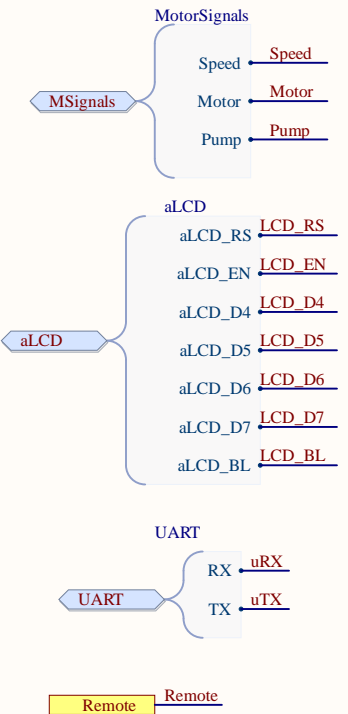
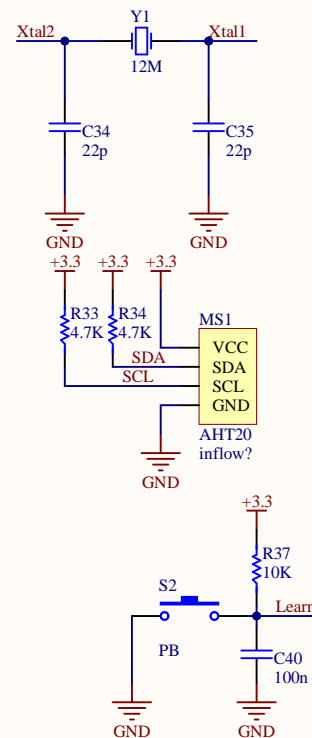


B



C

D



A

B

C

D

