

Design Rules Verification Report

Filename : C:\Users\Hossein-KUT\Documents\Camtasia\CAN Analyzer\PCB.PcbDoc

Warnings 2
Rule Violations 0
Waived Violations 8

Warnings	
Unplated multi-layer pad(s) detected	2
Total	2

Rule Violations	
Clearance Constraint (Gap=0.25mm) (All),(InPolygon)	0
Clearance Constraint (Gap=0.2mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint (All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.2mm) (Max=10mm) (Preferred=0.3mm) (All)	0
Routing Layers(All)	0
Routing Via (MinHoleWidth=0.4mm) (MaxHoleWidth=3mm) (PreferredHoleWidth=0.4mm) (MinWidth=0.7mm)	0
Differential Pairs Uncoupled Length using the Gap Constraints (Min=0.254mm) (Max=0.254mm) (Preferred=0.254mm)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Minimum Annular Ring (Minimum=0.15mm) (All)	0
Acute Angle Constraint [Tracks Only] (Minimum=60.000) (All)	0
Hole Size Constraint (Min=0.4mm) (Max=10mm) (All)	0
Hole To Hole Clearance (Gap=0.3mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.01mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.01mm) (IsPad),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Board Clearance Constraint (Gap=0mm) (All)	0
Component Clearance Constraint (Horizontal Gap = 0.254mm, Vertical Gap = 0.254mm) (All),(All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All)	0
Total	0

Waived Violations	
Un-Routed Net Constraint (All)	2
Minimum Annular Ring (Minimum=0.15mm) (All)	2
Acute Angle Constraint [Tracks Only] (Minimum=60.000) (All)	1
Silk To Solder Mask (Clearance=0.01mm) (IsPad),(All)	3
Total	8

Unplated multi-layer pad(s) detected	
Pad J1-(6.301mm,7.036mm) on Multi-Layer on Net NetF3_1	
Pad J1-(6.301mm,12.816mm) on Multi-Layer on Net NetF3_1	

Un-Routed Net Constraint (All)	
Un-Routed Net Constraint: Unplated Pad J1-(6.301mm,12.816mm) on Multi-Layer Waived by at 10/26/2025 4:22:52 PM	
Un-Routed Net Constraint: Unplated Pad J1-(6.301mm,7.036mm) on Multi-Layer Waived by at 10/26/2025 4:22:52 PM	

Minimum Annular Ring (Minimum=0.15mm) (All)	
Minimum Annular Ring: (No Ring) Pad J1-(6.301mm,12.816mm) on Multi-Layer (Annular Ring missing on Top Layer)Waived by at 10/26/2025 4:22:52 PM	
Minimum Annular Ring: (No Ring) Pad J1-(6.301mm,7.036mm) on Multi-Layer (Annular Ring missing on Top Layer)Waived by at 10/26/2025 4:22:52 PM	

Acute Angle Constraint [Tracks Only] (Minimum=60.000) (All)	
Acute Angle Constraint: (45.000 < 60.000) Between Track (15.936mm,15.745mm)(18.315mm,15.745mm) on Top Layer And Track	

Silk To Solder Mask (Clearance=0.01mm) (IsPad),(All)	
Silk To Solder Mask Clearance Constraint: (Collision < 0.01mm) Between Pad D2-1(40.767mm,5.969mm) on Multi-Layer And Text "RX-TX"	
Silk To Solder Mask Clearance Constraint: (Collision < 0.01mm) Between Pad R16-1(31.067mm,2.921mm) on Top Layer And Text "WORK"	
Silk To Solder Mask Clearance Constraint: (Collision < 0.01mm) Between Pad R16-2(33.167mm,2.921mm) on Top Layer And Text "WORK"	