

Design Rules Verification Report

Filename : E:\aKaReZa\#gitHub\Board_STLINK_Interface\Interface\PCB.PcbDoc

Warnings 2
Rule Violations 4

Warnings	
Unplated multi-layer pad(s) detected	2
Total	2

Rule Violations	
Clearance Constraint (Gap=7.874mil) (All),(All)	0
Clearance Constraint (Gap=9.842mil) (All),(InPolygon)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint (All)	2
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=7.874mil) (Max=393.701mil) (Preferred=15.748mil) (All)	0
Routing Layers(All)	0
Routing Via (MinHoleWidth=15.748mil) (MaxHoleWidth=118.11mil) (PreferredHoleWidth=15.748mil)	0
Differential Pairs Uncoupled Length using the Gap Constraints (Min=10mil) (Max=10mil) (Preferred=10mil) and Width	0
Minimum Annular Ring (Minimum=5.906mil) (All)	2
Acute Angle Constraint [Tracks Only] (Minimum=60.000) (All)	0
Hole Size Constraint (Min=15.748mil) (Max=393.701mil) (All)	0
Hole To Hole Clearance (Gap=11.811mil) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.394mil) (All),(All)	0
Silk To Solder Mask (Clearance=0.394mil) (IsPad),(All)	0
Net Antennae (Tolerance=0mil) (All)	0
Board Clearance Constraint (Gap=0mil) (All)	0
Component Clearance Constraint (Horizontal Gap = 10mil, Vertical Gap = 10mil) (All),(All)	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All)	0
Total	4

Unplated multi-layer pad(s) detected	
Pad J1-(674.22mil,249.906mil) on Multi-Layer on Net NetF1_1	
Pad J1-(901.78mil,249.906mil) on Multi-Layer on Net NetF1_1	

Un-Routed Net Constraint (All)	
Un-Routed Net Constraint: Unplated Pad J1-(674.22mil,249.906mil) on Multi-Layer	
Un-Routed Net Constraint: Net NetF1_1 Between Pad J1-(901.78mil,249.906mil) on Multi-Layer [Unplated] And Via (905mil,185mil) from Top Layer to	

Minimum Annular Ring (Minimum=5.906mil) (All)	
Minimum Annular Ring: (No Ring) Pad J1-(674.22mil,249.906mil) on Multi-Layer (Annular Ring missing on Top Layer)	
Minimum Annular Ring: (No Ring) Pad J1-(901.78mil,249.906mil) on Multi-Layer (Annular Ring missing on Top Layer)	