

Problem	Max Total Setup Delay	Max Total Hold Delay	Max Total Delay	Post Synthesis Utilization	Post Implementation Utilization																																								
1) Combinational 6-bit Multiplier	15.927 ns	2.163 ns	15.927 ns	Utilization <div>Post-Synthesis Post-Implementation</div> <div>Graph Table</div> <table><tr><th>Resource</th><th>Estimation</th><th>Available</th><th>Utilization %</th></tr><tr><td>LUT</td><td>44</td><td>63400</td><td>0.07</td></tr><tr><td>IO</td><td>24</td><td>210</td><td>11.43</td></tr></table>	Resource	Estimation	Available	Utilization %	LUT	44	63400	0.07	IO	24	210	11.43	Utilization <div>Post-Synthesis Post-Implementation</div> <div>Graph Table</div> <table><tr><th>Resource</th><th>Utilization</th><th>Available</th><th>Utilization %</th></tr><tr><td>LUT</td><td>44</td><td>63400</td><td>0.07</td></tr><tr><td>IO</td><td>24</td><td>210</td><td>11.43</td></tr></table>	Resource	Utilization	Available	Utilization %	LUT	44	63400	0.07	IO	24	210	11.43																
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2) Seq. 6-bit Unsigned Multiplier (LS)	6.524 ns	0.244 ns	6.524 ns	Utilization <div>Post-Synthesis Post-Implementation</div> <div>Graph Table</div> <table><tr><th>Resource</th><th>Estimation</th><th>Available</th><th>Utilization %</th></tr><tr><td>LUT</td><td>29</td><td>63400</td><td>0.05</td></tr><tr><td>FF</td><td>33</td><td>126800</td><td>0.03</td></tr><tr><td>IO</td><td>27</td><td>210</td><td>12.86</td></tr><tr><td>BUFG</td><td>1</td><td>32</td><td>3.13</td></tr></table>	Resource	Estimation	Available	Utilization %	LUT	29	63400	0.05	FF	33	126800	0.03	IO	27	210	12.86	BUFG	1	32	3.13	Utilization <div>Post-Synthesis Post-Implementation</div> <div>Graph Table</div> <table><tr><th>Resource</th><th>Utilization</th><th>Available</th><th>Utilization %</th></tr><tr><td>LUT</td><td>29</td><td>63400</td><td>0.05</td></tr><tr><td>FF</td><td>33</td><td>126800</td><td>0.03</td></tr><tr><td>IO</td><td>27</td><td>210</td><td>12.86</td></tr><tr><td>BUFG</td><td>1</td><td>32</td><td>3.13</td></tr></table>	Resource	Utilization	Available	Utilization %	LUT	29	63400	0.05	FF	33	126800	0.03	IO	27	210	12.86	BUFG	1	32	3.13
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3) Seq. 6-bit Unsigned Multiplier (LS)	5.056 ns	0.295 ns	5.056 ns	Utilization <div>Post-Synthesis Post-Implementation</div> <div>Graph Table</div> <table><tr><th>Resource</th><th>Estimation</th><th>Available</th><th>Utilization %</th></tr><tr><td>LUT</td><td>28</td><td>63400</td><td>0.04</td></tr><tr><td>FF</td><td>32</td><td>126800</td><td>0.03</td></tr><tr><td>IO</td><td>27</td><td>210</td><td>12.86</td></tr><tr><td>BUFG</td><td>1</td><td>32</td><td>3.13</td></tr></table>	Resource	Estimation	Available	Utilization %	LUT	28	63400	0.04	FF	32	126800	0.03	IO	27	210	12.86	BUFG	1	32	3.13	Utilization <div>Post-Synthesis Post-Implementation</div> <div>Graph Table</div> <table><tr><th>Resource</th><th>Utilization</th><th>Available</th><th>Utilization %</th></tr><tr><td>LUT</td><td>28</td><td>63400</td><td>0.04</td></tr><tr><td>FF</td><td>32</td><td>126800</td><td>0.03</td></tr><tr><td>IO</td><td>27</td><td>210</td><td>12.86</td></tr><tr><td>BUFG</td><td>1</td><td>32</td><td>3.13</td></tr></table>	Resource	Utilization	Available	Utilization %	LUT	28	63400	0.04	FF	32	126800	0.03	IO	27	210	12.86	BUFG	1	32	3.13
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