Problem	Max Total Setup Delay	Max Total Hold Delay	Max Total Delay	Post Synthesis Utilization Utilization Post-Synthesis Post-Implementation				Post Implementation Utilization			
1)	15.927	2.163	15.927					Utilization Post-Synthesis Post-Implementation			
Combinational	ns	ns	ns	Graph Table							Graph Table
6-bit				Resource	Estimation	Available U	tilization %	Resource	Utilization	Available	Utilization %
Multiplier				LUT	44	63400	0.07	LUT	44	63400	0.07
				10	24	210	11.43	Ю	24	210	11.43
2) Seq. 6-bit Unsigned Multiplier (LS)	6.524 ns	0.244 ns	6.524 ns	Utilization Post-Synthesis Post-Implementation Graph Table Graph Table							
				Resource	Estimation	Available	Utilization %	Resource LUT	Otilization 29	63400	0.05
				LUT	33			FF	33	126800	
				IO	2			Ю	27	210	12.86
				BUFG				BUFG	1	32	3.13
3) Seq. 6-bit	5.056	0.295	5.056	Utilization Post-Synthesis Post-Implementation				Utilization	Post	-Synthesis Pos	t-Implementation
Unsigned	ns	ns	ns	Graph Table				Graph Table			
Multiplier (LS)				Resource	Estimation	Available	Utilization %	Resource	Utilization	Available	Utilization %
				LUT	2	3 6340	0.04	LUT	28	63400	0.04
				FF	3	2 12680	0.03	FF	32	126800	0.03
	1	1	1			-		IO	27	210	12.86
				IO	2	7 21	12.86	BUFG	1	32	3.13