Comparison of the 3 adder designs

Design	Critical Path Delay	Hardware Requirements
Ripple Carry Binary Adder	=Max(delays of all paths) = 7.118 ns	Utilization Post-Synthesis | Post-Implementation Graph | Table Resource / Utilization / Available / Utilization % LUT 8 63400 0.01 IO 26 210 12.38
Hybrid Binary Adder	=Max(delays of all paths) = 7.118 ns	Utilization Post-Synthesis | Post-Implementation Graph | Table Resource / Utilization / Available / Utilization % LUT 8 63400 0.01 IO 26 210 12.38
Bit-serial Binary Adder	=Max(delays of all paths) = 4.090 ns	Utilization Post-Synthesis | Post-Implementation Graph | Table Resource / Utilization / Available / Utilization... LUT 69 63400 0.11 FF 54 126800 0.04 IO 27 210 12.86 BUFG 1 32 3.13