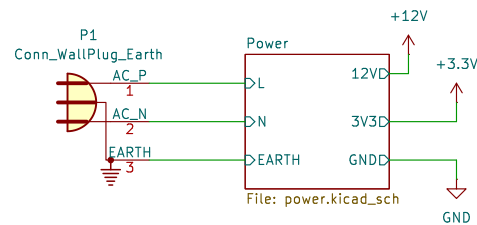
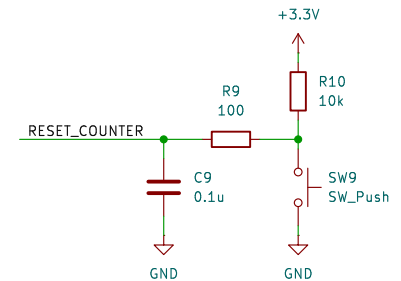
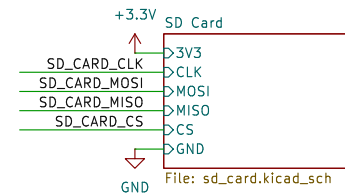
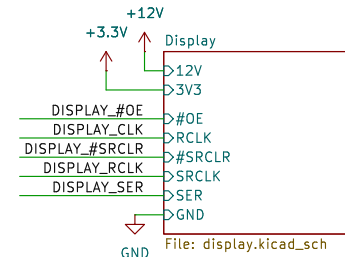
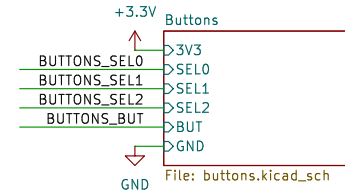
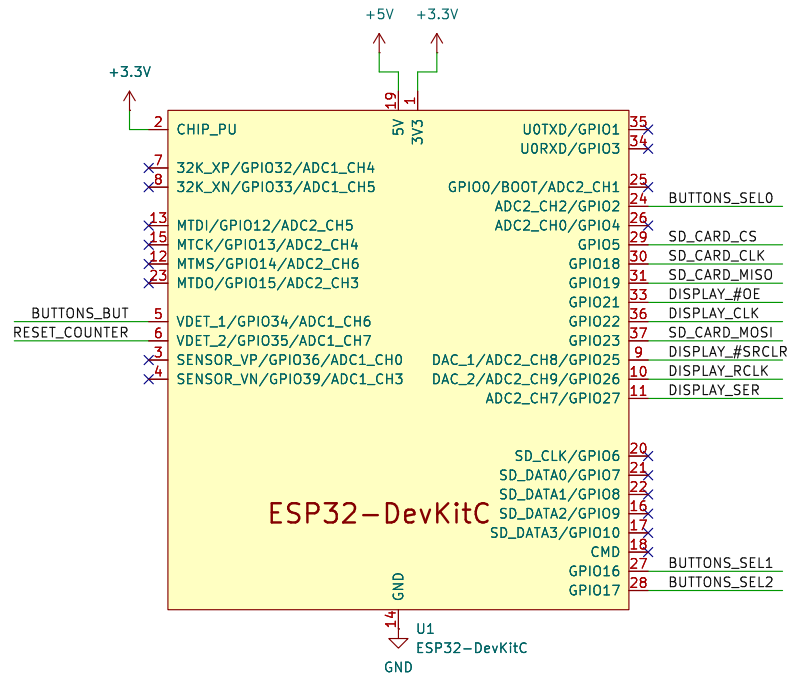


The devkit being used is a different one, and has less pins than the one showed here!!!
(I should create a symbol for the new one, but right now works)

SPI0: GPIO6–11 are usually connected to the SPI flash integrated on the module and therefore should not be used for other purposes.

JTAG: GPIO12–15 are usually used for inline debug.



Associació Cultural TGC

Sheet: /

File: compta-birres.kicad_sch

Title: Root

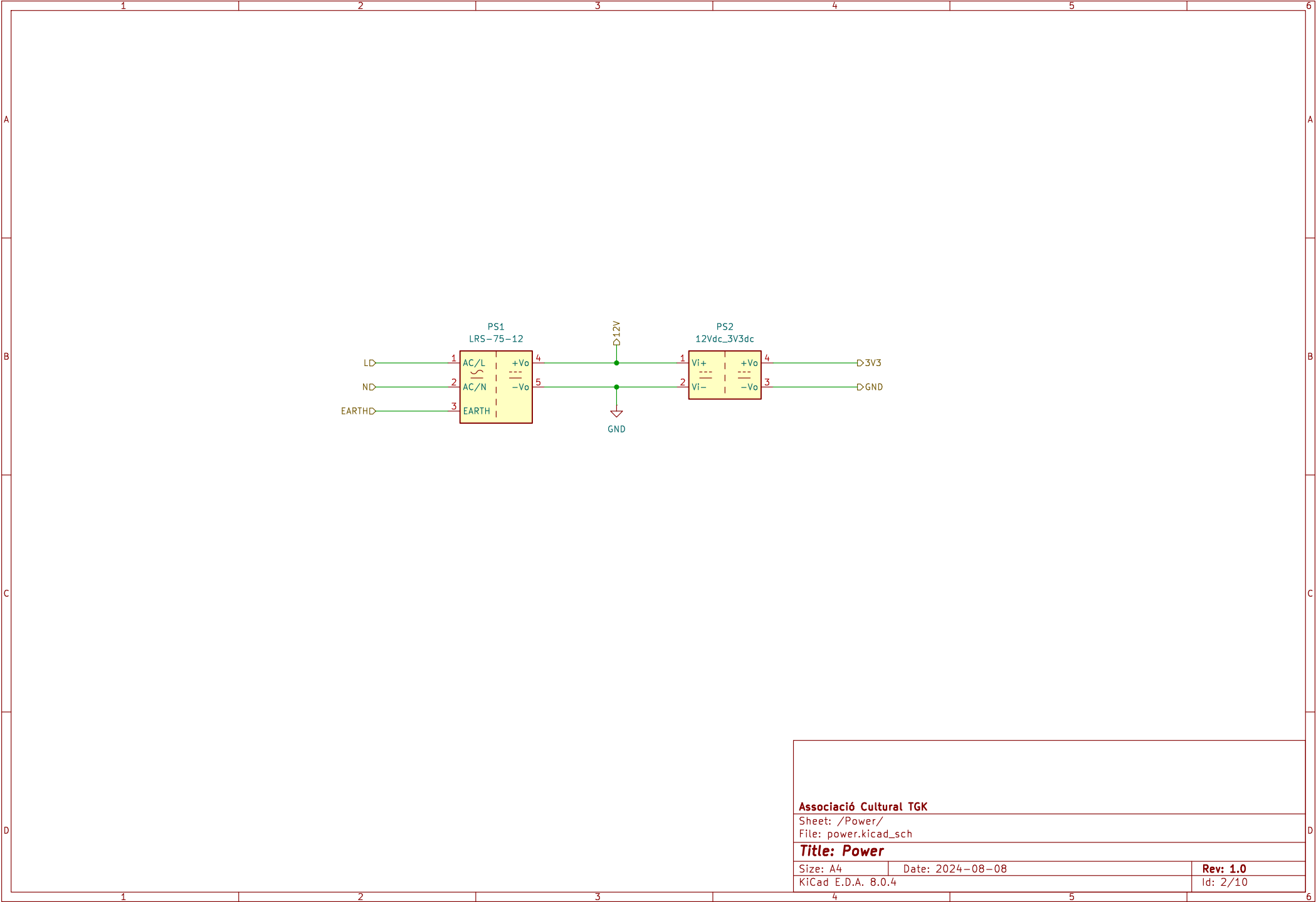
Size: A4

Date: 2024-08-08

Rev: 1.3

KiCad E.D.A. 8.0.4

Id: 1/10

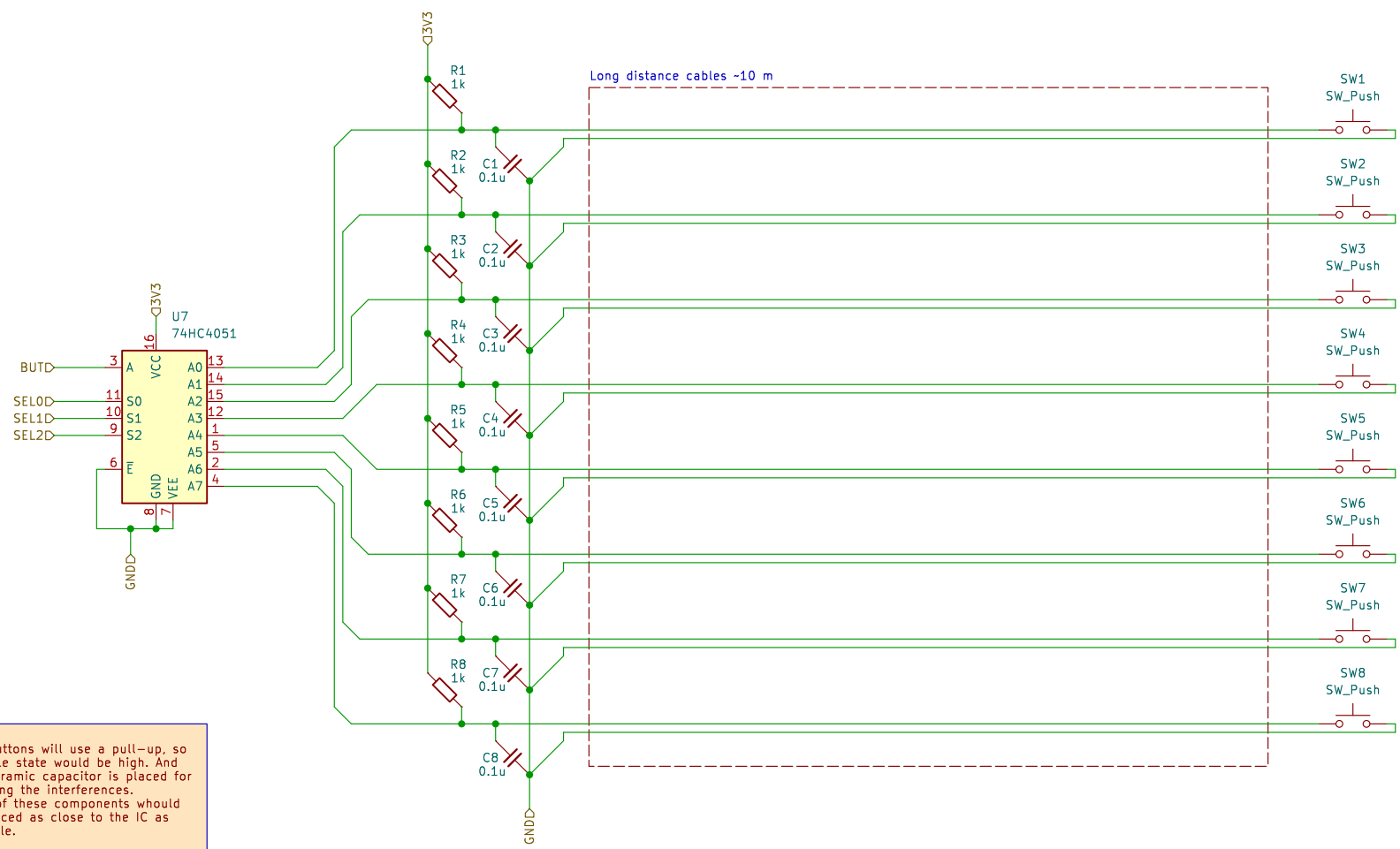


Associació Cultural TGK

Sheet: /Power/
File: power.kicad_sch

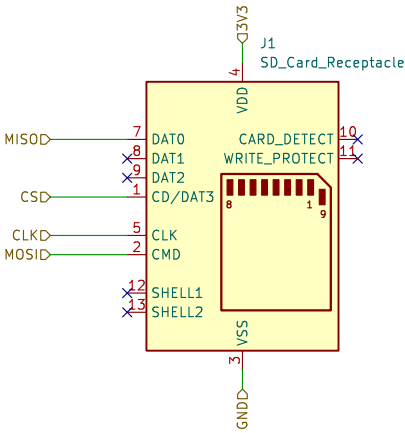
Title: **Power**

Size: A4	Date: 2024-08-08	Rev: 1.0
KiCad E.D.A. 8.0.4		Id: 2/10



NOTE:
The buttons will use a pull-up, so the idle state would be high. And the ceramic capacitor is placed for reducing the interferences. Both of these components should be placed as close to the IC as possible.

Associació Cultural TGK		
Sheet: /Buttons/ File: buttons.kicad_sch		
Title: Buttons		
Size: A4	Date: 2024-08-04	Rev: 1.1
KiCad E.D.A. 8.0.4		Id: 3/10



Associació Cultural TGK

Sheet: /SD Card/
File: sd_card.kicad_sch

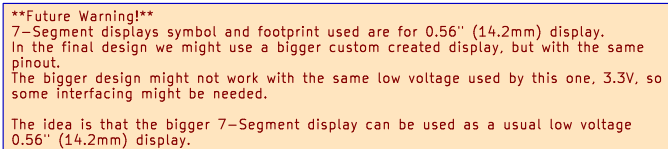
Title: SD Card

Size: A4 Date: 2024-08-08

KiCad E.D.A. 8.0.4

Rev: 1.1

Id: 4/10



Rev: 1.1
Id: 5/10

