

Résumé

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Education

DSc(2008)/Msc(2001) in Technology, Helsinki University of Technology.
BSc in Electrical Eng. (1999), Vilnius Gediminas Technical University.

Employment

- 2013–2014: Faculty of Informatics at the University of Lugano,
Via Giuseppe Buffi 13, CH-6904 Lugano, Switzerland.
- 2011–2012: DEMO, École Nationale Supérieure des Mines de Saint-Étienne,
158 cours Fauriel, 42023 Saint-Étienne, CEDEX2, France.
- 2008–2009: Department of Neurobiology at the University of California,
Los Angeles, 73-235 CHS, CA 90095-1763.
- 2000–2008: Faculty of Information and Natural Sciences at
Helsinki University of Technology,
P.O.Box 5400 (Konemiehentie 2), FI-02015, Finland.

Selected Projects

- [esp32-vpn](#) 2022 Connected the ESP32 globally without 3rd party services.
IoT, VPN, P2P, MQTT, SSH, Wi-Fi, 24/7, MicroPython, Go.
- [twinpeekz2](#) 2021 Implemented real-time multi-pass 3D rendering pipeline.
GLFW, GLTF, OpenGL/GLSL, RenderDoc, 3D ray marching, Nim.
- [adast](#) 2020 Wrote a microcontroller code to repair a factory machine.
Adast Maxima MS80, paper guillotine, avr-gcc, ATmega16.
- [hal-00723427](#) 2012 Sped up Monte Carlo integration in a complex eng. project.
Fluid dynamics, Bayesian optimization, ProActive PACA Grid.

Awards

- 2008: Doctoral thesis award: “Pass with Distinction”.
- 2008: Invited talk (Prof. Mads Nielsen, the University of Copenhagen)
- 2002–2006: ComMIT grant (postgraduate studies).
- 1999–2000: Nokia scholarship (Natalia Kantola, Robertas Tamulevičius).