Embedded Security Showcase using PSoC64 Bug Report

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Title: Exporting Makefile project for PSoC64 MCU (CY8CPROTO-064S1) to Eclipse IDE throws

an error: CY8CPROTO-064S1 is not a supported MCU.

Screenshot:

```
PS C:\Users\Aadarshxp\Desktop\mbed_testing\blinky> <mark>mbed</mark> export -i make_gcc_arm -m CY8CPROTO-064SB
[mbed] Working path "C:\Users\Aadarshxp\Desktop\mbed_testing\blinky" (program)
argument -m/--mcu: CY8CPROTO-064SB is not a supported MCU. Supported MCUs are:
ARCH_BLE,
                                                     ARCH_BLE_OTA,
                          ARCH_BLE_BOOT,
ARCH_GPRŚ,
                          ARCH_LINK,
                                                     ARCH_LINK_BOOT,
                                                                                  Mbed Export option
                                                     ARCH_PRO,
ARM_IOTSS_BEID,
ARM_MPS2_M3,
ARCH_LINK_OTA
                          ARCH_MAX,
ARDUINO_NANO33BLE,
ARM_MPS2_M0,
                          ARM_CM3DS_MPS2,
                                                                                   gives error MCU not
                          ARM_MPS2_MOP,
ARM_MPS2_M7,
                                                                                  supported
ARM_MPS2_M4
                                                     ARM_MUSCA_A1_NS
ARM_MUSCA_A1_S
                          B96B_F446VE
                                                     BLUEPILL_F103C8,
                          CY8CKIT_062S2_43012,
CC3220SF_LAUNCHXL
                                                     CY8CKIT 062 BLE
                          CY8CKIT_064S2_4343W,
CY8CKIT_062_WIFI_BT,
                                                     CY8CPROTO_062S3_4343W,
                                                                                     The same MCU is
CY8CPROTO 062 4343W.
                          CY8CPROTO_063_BLE,
                                                    CY8CPROTO_064_SB.
                                                                                     present in
                          CYW9P62S1_43012EVB_01,
CYW943012P6EVB_01,
                                                     CYW9P62S1_43438EVB_01.
DELTA_DFBM_NQ620,
                          DELTA_DFCM_NNN40,
                                                     DELTA_DFCM_NNN40_BOOT,
                                                                                     Supported MCU list
DELTA_DFCM_NNN40_OTA,
                          DELTA_DFCM_NNN50,
                                                     DELTA_DFCM_NNN50_BOOT,
DELTA_DFCM_NNN50_OTA,
                          DISCO_F051R8,
                                                     DISCO_F100RB,
                          DISCO_F334C8,
DISCO_F303VC,
                                                     DISCO_F401VC,
DISCO_F407VG,
                          DISCO_F413ZH,
                                                     DISCO_F429ZI,
DISCO_F469NI,
                          DISCO_F746NG,
                                                     DISCO_F769NI,
```

Description and temporary fix:

1- The result of the verbose output is attached below, the error is thrown from the script project.py. The file is present in the path <root>\mbed-os\tools.

2- On debugging the project.py script found the script throws error while trying to export at this point, In screenshot at 385 line, we see that exporter object is created and contains all the exporter toolchain and MCU list. Further debugging (step into operation) found it returns some error values from exporter.py python script. (<root>\mbed-os\tools\export\exporter.py)

```
test.py × 👛 exporters.py ×
                                          targets\_init_.py ×
                            project.py ×
                                                             📠 makefile\__init__.py ×
\mathbb{Q}^{\text{w}} not supported by
                               ☐ Match Case ☐ Regex ☐ Words
                if (options.program is None) and (not options.source_dir):
379
                   args_error(parser, "one of -p, -n, or --source is required")
                if options.clean:
382
                   clean(options.source_dir)
                ide = resolve_exporter_alias(options.ide)
                                                                                            While exporting
384
385
                exporter, toolchain_name = get_exporter_toolchain(ide)
                                                                                            error is
                profile = extract_profile(parser, options, toolchain_name, fallback="debug")
                                                                                            returned from
387
                mcu = extract_mcus(parser, options)[0]
                if not exporter.is_target_supported(mcu):
                                                                                            here(exporter
                args_error(parser, "%s not supported by %s" % (mcu, ide))
                                                                                            object)
                try:
```

3- On debugging the exporter.py found that in the API apply_supported_whitelist the toolchain was supported, and the attribute for the MCU (CY8CPROTO-064SB) was present but in the last API binary hook returned False, on changing this value to true, i was successfully able to import the makefile project for CY8CPROTO-064SB board in Eclipse IDE.

```
def apply_supported_whitelist(compiler, whitelist, target):

"""Generate a list of supported targets for a given compiler and post-binary hook
white-list."""

if compiler not in target.supported_toolchains:
    return False
    if not hasattr(target, "post binary hook"):
        return True
    if target.post_binary_hook['function'] in whitelist:
        return True
    else:
        return True
```

4- The expected return and Actual return value for the post binary hook API is attached below:

```
Variables
    🗮 TARGET MAP = (dict) ("LPC11C24"; Target(name="LPC11C24", ison data=f"Target"; OrderedDict(f"core", None), ("trustzone", False), ("default toolchain", "ARM"), ("supported toolchains",
 TARGET_MAP("CY8CPROTO_064_SB") -{Target} Target(name='CY8CPROTO_064_SB', json_data={'Target': OrderedDict([('core', None), ('trustzone', False), ('default_toolchain', 'ARM'), (... View
        89 0 = {str} 'CY8CPROTO_064_SB'
                                                  📐 CY8CPROTO-064SB MCU is present in Target Map
    > = 1 = {dict} {'Target': OrderedDict([('core', None), ('trustzone', False), ('default_toolchain', 'ARM'), ('supported_toolchains', None), ('extra_labels', []), ('components', ['PSA_SRV_IMPL', ... View
    > 1 3 = {list} < class 'list'>: ['CY8CPROTO_064_SB', 'MCU_PSOC6_M4', 'MCU_PSOC6', 'Target']
    > = 4 = {OrderedDict} OrderedDict}((('version', '1'), ('public', False), ('_from_file', 'C:\\Users\\Aadarshxp\\Desktop\\mbed_testing\\blinky\\mbed-os\\tools\\targets\\...\\..\\targets\\targets
       Interpretation
Interpretation
    > 📒 _Target__extra_target_json_files = {list} <class 'list'>: []
       M _Target_targets_json_location = {NoneType} None
       Target targets ison location default = {str} 'C:\\Users\\Aadarshxp\\Desktop\\mbed testing\\blinkv\\mbed-os\\tools\\targets\\...\..\\targets\\targets\\targets\\...on'
    > = _field_defaults = {dict}{}
    > 🕌 _fields = {tuple} <class 'tuple'>: ('name', 'json_data', 'resolution_order', 'resolution_order_names', 'build_tools_metadata')
    _fields_defaults = {dict}{}
    > 🗮 build_tools_metadata = {OrderedDict} OrderedDict([('version', '1'), ('public', False), ('_from_file', 'C:\\Users\\Aadarshxp\\Desktop\\mbed_testing\\blinky\\mbed-os\\tools\\targets\... Viev
       Sore = {str} 'Cortex-M4F'
       M core_without_NS = {str} 'Cortex-M4F'
       is_PSA_non_secure_target = {bool} False
       is_PSA_secure_target = {bool} False
                                                                                                    Post binary hook value of
       is_PSA_target = {bool} False
                                                                                                    CY8CPROTO-064SB MCU
       is_TrustZone_non_secure_target = {bool} False
       [M] is_TrustZone_secure_target = \{bool\} False
       III is_TrustZone_target = {bool} False
       📕 json_data = {dict} {'Target': OrderedDict([('core', None), ('trustzone', False), ('default_toolch in', 'ARM'), ('supported_toolchains', None), ('extra_labels', []), ('components', ['PSA_SR\... Viev
       🕌 labels = {list} <class 'list'>: ['CY8CPROTO_064_SB', 'MCU_PSOC6_M4', 'MCU_PSOC6', 'M4' CORTEX_M', 'RTOS_M4_M7', 'LIKE_CORTEX_M4', 'CORTEX,', 'Cypress', 'PSOC6', 'MXCRYP'... Viev
        In name = {str} 'CY8CPROTO 064 SB'
        post_binary_hook = {OrderedDict} OrderedDict([('function', 'PSOC6Code.sign_image')])
           (72176048) = {str} 'PSOC6Code.sign image'
```

Expected Post binary hook value in whitelist:

```
is_PSA_secure_target = {bool} False
       III is PSA target = {bool} False
       Is_TrustZone_non_secure_target = {bool} False
       is_TrustZone_secure_target = {bool} False
       III is_TrustZone_target = {bool} False
   > 🗮 json_data = (dict) {'Target': OrderedDict([('core', None), ('trustzone', False), ('default_toolchain', 'ARM'), ('supported_toolchains', None), ('extra_labels', []), ('components', ['PSA_SR\... '
   > 掲 labels = {list} <class 'list'>: ['CY8CPROTO_064_SB', 'MCU_PSOC6_M4', 'MCU_PSOC6', 'M4', 'CORTEX_M', 'RTOS_M4_M7', 'LIKE_CORTEX_M4', 'CORTEX', 'Cypress', 'PSOC6', 'MXCRYP'... '
       Ill name = {str} 'CY8CPROTO 064 SB'
   post_binary_hook = {OrderedDict} OrderedDict([('function', 'PSOC6Code.sign_image')])
          Ill 'function' (72176048) = {str} 'PSOC6Code.sign_image'
          图 __len__ = {int} 1
       Program_cycle_s = {float} 1.5
   > 🔚 resolution_order = {list} <class 'list'>: [('CY8CPROTO_064_SB', 0), ('MCU_PSOC6_M4', 1), ('MCU_PSOC6', 2), ('Target', 3)]
   > \frac{1}{2} resolution_order_names = \{\text{list} < \class '\text{list'}: ['CY8CPROTO_064_SB', 'MCU_PSOC6_M4', 'MCU_PSOC6', 'Target']}</p>
   > 🗏 supported_toolchains = {list} <class 'list'>: ['ARM', 'GCC_ARM', 'IAR']
  whitelist = (set) {'LPCTargetCode.lpc_patch', 'LPC4088Code.binary_hook', 'TEENSY3_1Code.binary_hook', 'MCU_NRF51Code.binary_hook', 'PSOC6Code.complete'}
   In compiler = {str} 'GCC_ARM'
   target = {Target} Target(name='CY8CPROTO_064_SB', json_data={'Target': OrderedDict([('core', None), ('trustzone', False), ('default_toolchain', 'ARM'), ('supported_toolchains', None),
whitelist = (set) {'LPCTargetCode.lpc_patch', 'LPC4088Code.binary_hook', 'TEENSY3_1Code.binary_hook', 'MCU_NRF51Code.binary_hook', 'PSOC6Code.complete'}
       3 78322288 = {str} 'LPCTargetCode.lpc_patch'
       39 78322336 = {str} 'LPC4088Code.binary_hook'
                                                                              Expected Whitelist value for post
       8 78352640 = {str} 'TEENSY3_1Code.binary_hook'
                                                                              binary hook
       89 78352584 = {str} 'MCU_NRF51Code.binary_hook'
       | 78322384 = {str| 'PSOC6Code.complete'
       M __len__ = {int} 5
```