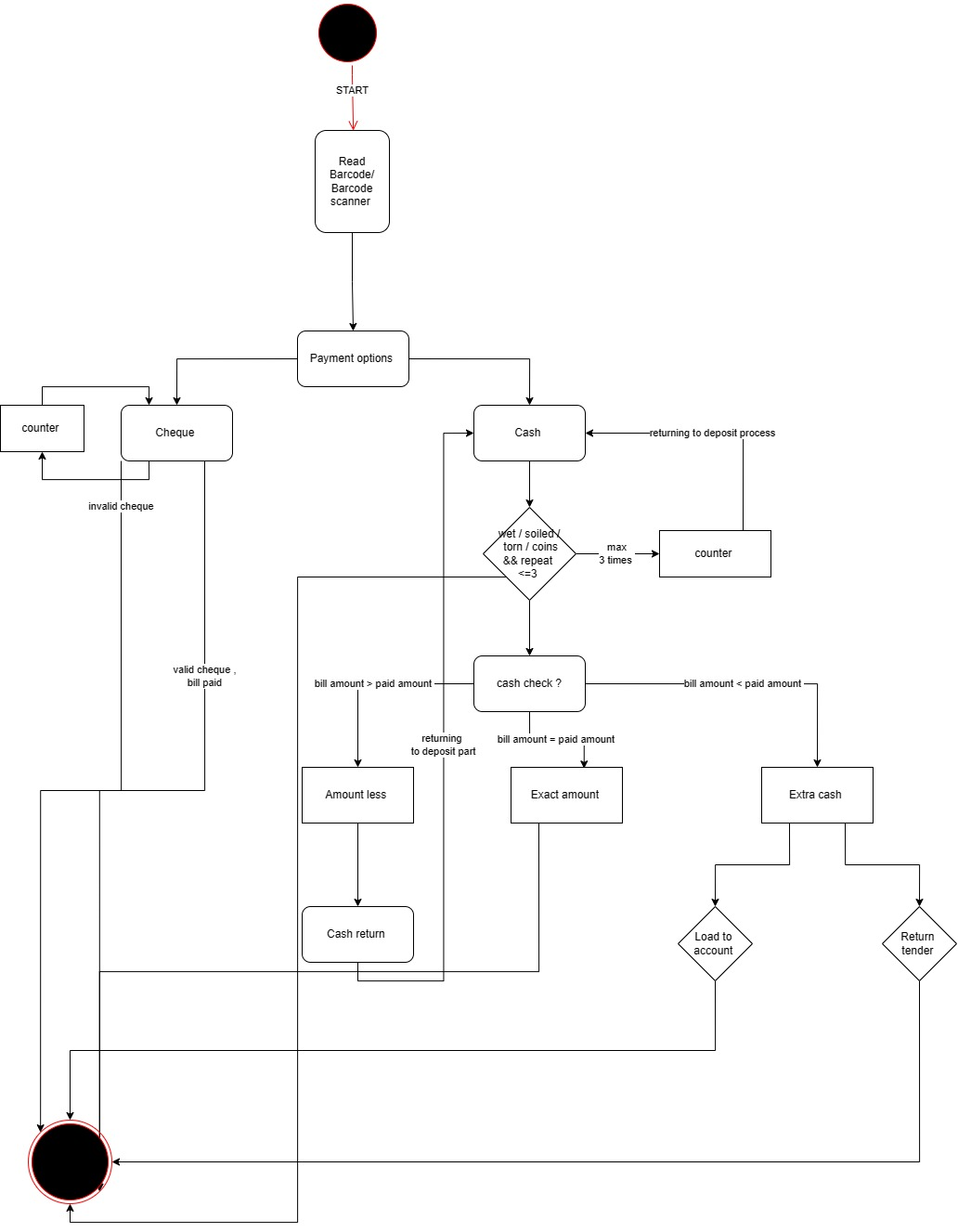
MODELS

*Hi! In this Word file, I have included the models I created to complete this project. The models are the block diagram, flow diagram, and the state machine diagram, which was created using the Mealy model.*

**MODEL BLOCK DIAGRAM:**

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**FLOW DIAGRAM :**



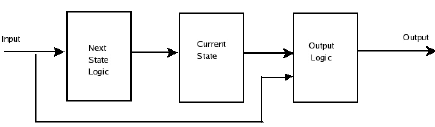
**FSM (Finite State Machine) :**

In a Finite State Machine the circuit’s output is defined in a different set of states i.e. each output is a state. A State Register to hold the state of the machine and a next state logic to decode the next state. An output register defines the output of the machine. In FSM based machines the hardware gets reduced as in this the whole algorithm can be explained in one process.

Two types of State machines are:

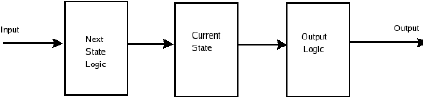
MEALY Machine:

In this machine model, the output depends on the present state as well as on the input. The MEALY machine model is shown in this figure .



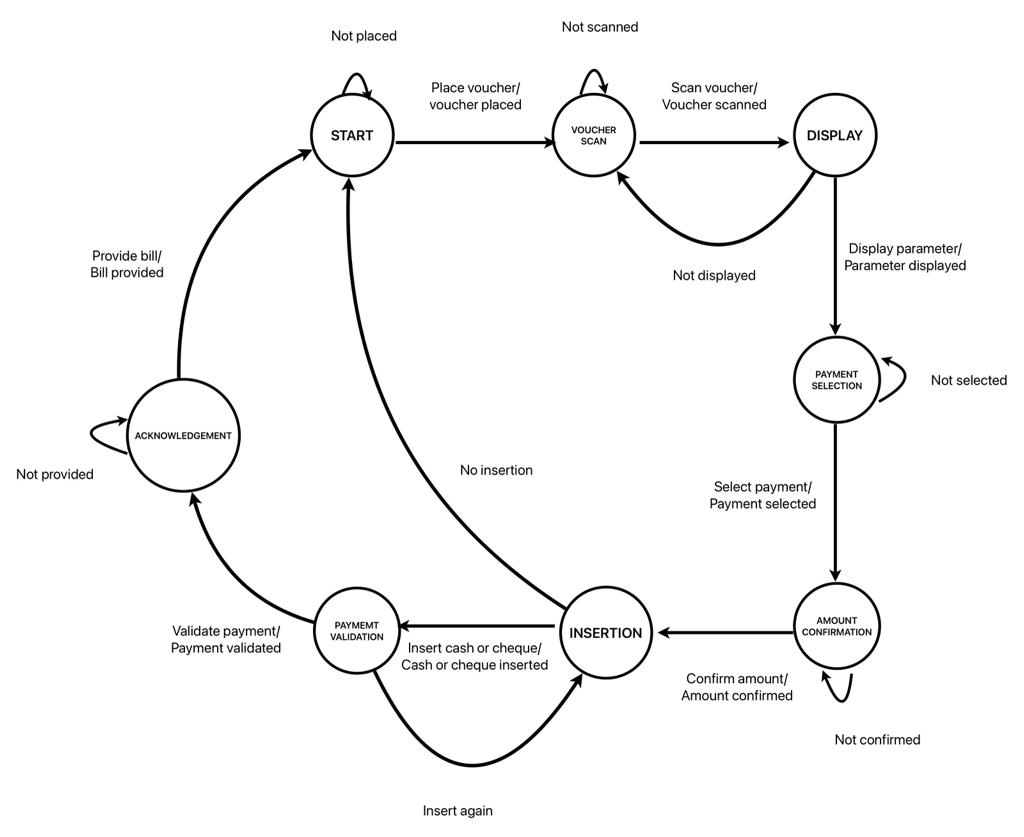
MOORE Machine:

In Moore machine model the output only depends on the present state. The MOORE machine model is shown in this figure



**STATE DIAGRAM:**

(BASED ON MEALY MACHINE)

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The state diagram is a graphical representation of the different states and transitions within the ATP system.

The diagram labels all state transitions and provides a visual representation of the flow of operations during a payment transaction.

It illustrates the sequence of actions performed by the ATP in response to user inputs and system events.