

## Overview of Language Processing System

Source Program



Preprocessor

Compiler



Assembler



Linker/Loader



Target Program

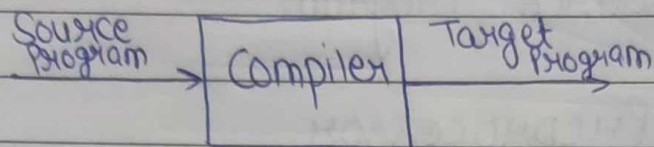
**Preprocessor:-** A preprocessor produce input to compiler they may perform the following functions.

1. **Macro Processing:-** A preprocessor may allow a user to define macros that are shorthands for longer constructs.
2. **File Inclusions:-** A preprocessor may include header files into the program text.
3. **Rational Preprocessor:-** These processor add older languages with more modern flow of control and data structure facilities.
4. **Language Extensions:-** These preprocessor attempts to add capabilities to the language by certain amount to build macro.

**Compiler-** A compiler is a translator program that translates a program written in high



level language and translate it into the equivalent program in machine level language as a target program.



A compiler display ~~error~~ <sup>error</sup> while processing the program.

**Assembler**- Whenever the programmer find it difficult to write or read program in machine language they began to use mnemonics (symbols) for each machine language instructions that translate into machine language and such a mnemonics machine language is called an assembly language.

**Interpreter**- They convert high level language into the machine level language line by line such as BASICS, COBOL, FORTRAN.

**Advantages:-** 1. The modification of user program can be easily made and implemented as execution proceed.

2. The type of object that denotes at various level may change dynamically.

3. Debugging of program and finding errors is simplified task for a program used to interpretation.

4. Interpreter of the language makes it machine independent.

**Disadvantages-** 1. Slow execution of the program.

2. The memory consumption is high.



Linker / Loader:- All the files combined in the form of either linker or loader.

Without the help of Linker / Loader we can't execute our program.

### # List of Compilers

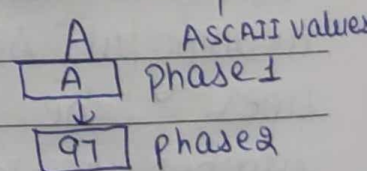
- |                      |                          |
|----------------------|--------------------------|
| 1. .ALGOL Compiler   | 2. .BASIC Compiler       |
| 3. .C# Compiler      | 4. .C Compiler           |
| 5. .C++ Compiler     | 6. .COBOL Compiler       |
| 7. .D Compiler       | 8. .COMMON LISP Compiler |
| 9. .FORTRAN Compiler | 10. .JAVA Compiler       |
| 11. .PASCAL Compiler | 12. .PL/1 Compiler       |
| 13. .PYTHON Compiler | 14. .SCHEME Compiler     |
| 15. .FELIX Compiler  | 16. .EFFIL Compiler      |
| 17. .ADA Compiler    | 18. .Small Talk Compiler |

### # Structure of the Compiler Design

A phase is a logically inter-related operation that takes source program in one representation and produce output in another representation.

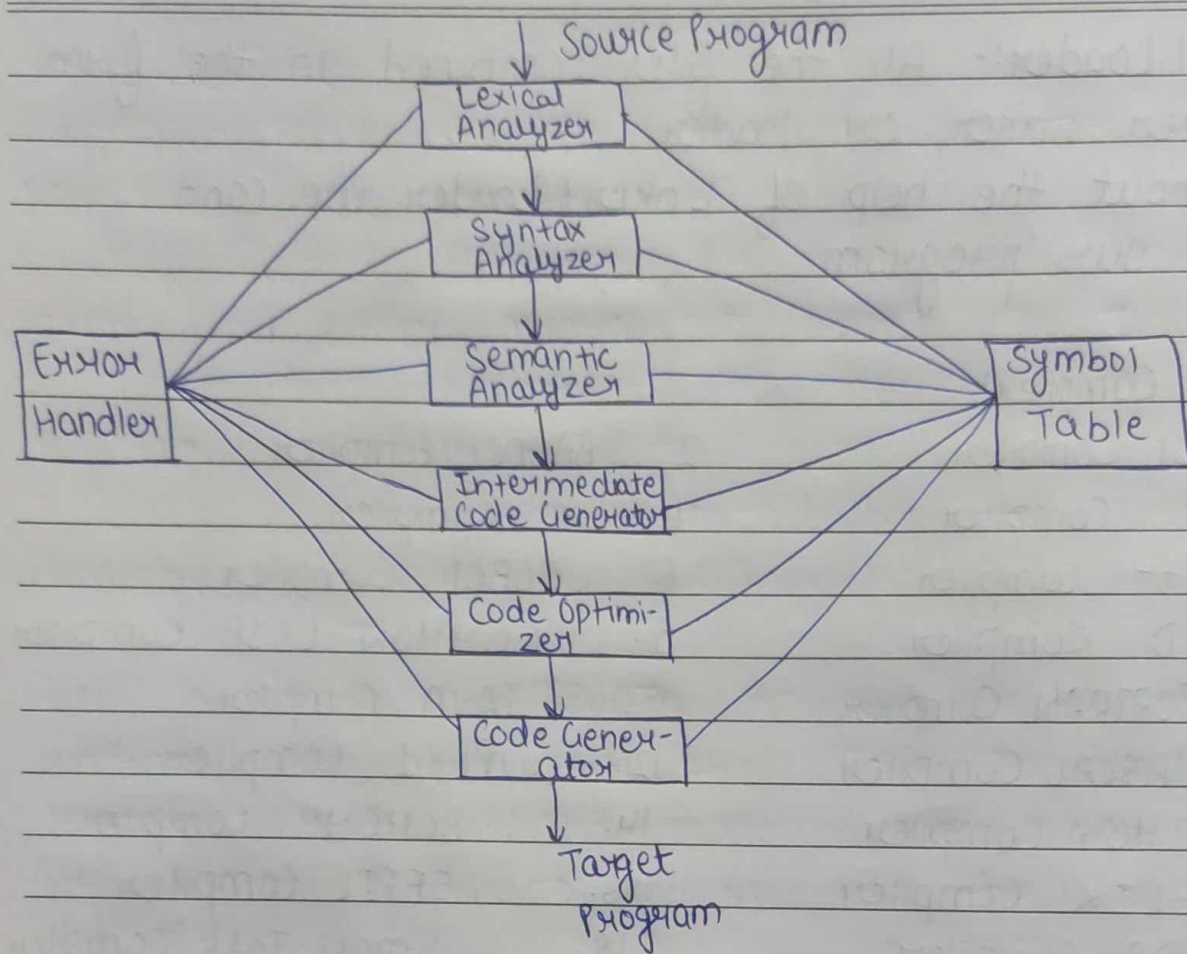
There are normally two phases in the compiler

1. Analysis
2. Synthesis



There are 6 phases of the compiler

- |                      |                                |
|----------------------|--------------------------------|
| 1. Lexical Analyzer  | 2. Syntax Analyzer             |
| 3. Semantic Analyzer | 4. Intermediate Code Generator |
| 5. Code Optimizer    | 6. Code Generator              |



**Lexical Analysis**- Lexical Analysis is the first phase when compiler scans the source code. This process can be left to right, character by character and group these characters into tokens. The character stream from the source program is grouped in meaningful sequences by identifying the tokens. Sometimes the entry of corresponding tokens into the symbol table and passes that token into the next phases of the compiler.

The primary function of these phase are

1. Identify the Lexical Unit in a source code.
  2. Classify Lexical unit into classes like constant, reserve word or different type of sections we have.
- It will ignore comment section of the source program.



3. Identify token which is not a part of the language.

For Eg  $X = Y + 10$

Token	Description
X	Identifier
=	Assignment Operator
Y	Identifier
+	Additional Operator
10	Number

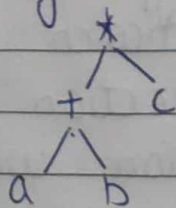
Syntax Analysis - Syntax Analysis is all about discovering structure in a code. It determines whether or not or text follow the aspected format. The main aim of these phase is to make sure that source code was written by the programmer is correct or not.

It is based on the rules regarding specific programming language by constructing the Parse Tree with the help of tokens.

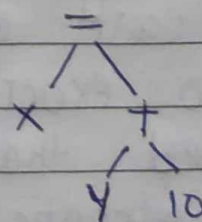
The list of tasks performed in this phase.

1. Obtain tokens from Lexical Analyzer.
2. Check if the expression is syntactically correct or not.
3. Report all syntax error.
4. Construct or Hierarchical structure which is known as Parse Tree.

For Eg.  $(a+b)*c$



$X = Y + 10$





## In Parse Tree

1. Interior Node - Record with an operator field and two fields for children.
2. Leaf - Record with two or more fields one for token and other information about the token.
3. Ensure that components of the program fit together meaningfully.
4. Check operands are permitted by the source language.

**Semantic Analysis** - Semantic Analysis checks the semantic consistency of the code. It uses the syntax tree of the previous phase along with the symbol table to verify that the given source code is semantically consistent. It also checks whether the code is meaningful and appropriate.

Semantic Analyzer will check type mismatch, incompatible operands, a function called with improper arguments, undeclared variable etc.

There are various functions used in Semantic Analyzer.

1. It helps you to store type information gathered and save it in the system symbol table or syntax tree.
2. It allows you to perform type checking.
3. In the case of type mismatch if there are no exact type of correction rule which satisfies the desired operation will generate the semantic error occur error handler.
4. Collects type information and check for type



compatibility.

5. It also check if the source language permits the operand or not.

Eg- Float  $x = 20.2;$   
 $y = x * 30;$

In this code semantic analyzer type caste to the integer 30 to float 30.0 before multiplication.

Intermediate code generation- Once the semantic Analysis is over the compiler generates intermediate code for the target machine.

It generates a program for some abstract machine.

Intermediate code is between the high level language and machine level language.

The intermediate code needs to be generated in such a manner that makes it easy to translate it into the target machine code.

$y = x * 30;$

Variable = Variable 2 \* Constant

The following function can be done on intermediate code generator.

1. It should be generated from the source program.
2. It holds the value computed during the process of translation.
3. It helps you to translate the intermediate code into target language.
4. It allow you to maintain precedence ordering of the source language.
5. It holds the correct no. of operands of



the instruction.

For Eg  $\text{Total} = \text{Count} + \text{Rate} * 5$

The following task will perform by the intermediate code with the help of the following address code method.

$t1 := \text{int\_to\_float}(5)$

$t2 := \text{Rate} * t1;$

$t3 = \text{Count} + t2;$

$\text{total} = t3;$

**Code Optimization-** In this phase we removes unnecessary code lines and arranges the sequence of statement to speed up the execution of the program without wasting resources. The main goal of this phase is to improve on the intermediate code to generate a code that runs faster and occupies less space. The primary function of this phase are -

1. It helps you to establish trade-off between execution and compilation speed.
2. It improves the running time of the target program.
3. Removing unreachable code and eliminate unused variables.
4. Removing statements which are not altered from the loop.

$a = \text{int\_to\_float}(10);$

$b = c * a;$

$d = f + b;$

$k = d;$

$b = c * 10.0;$

$k = f + b;$



		/	/

**Code Generation** It is the last and final phase of the compiler. It gets input from code optimization phase and produce the object code or page code as a result. And the objective of this phase is to allocate storage and generate re-locatable machine code. All the memory locations and registers are also selected and allotted respectively.

for example

$a = b + 60.0;$

MOVF b, R1;

MOVF #600, R2;

ADDF R1, R2;

Example-  $position := initial + rate * 60$

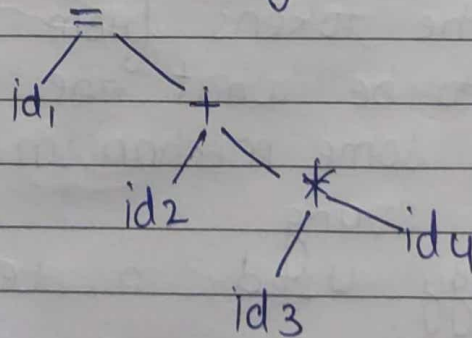


Lexical Analyzer

$id_1 := id_2 + id_3 * id_4$



Syntax Analyzer



Semantic Analyzer



$x - 60$



int to real

intermediate code generator



temp1 := int to Heal (60);

temp2 = id3 \* temp1;

temp3 = id2 + temp2;

id1 := temp3



code optimizer



temp1 = id3 \* 60.0

id1 = id2 + temp1



code generator



MOVF id3, r1

MOVF #60, r2

MULF r2, r1

MOVF id2, r2

ADDF r2, r1

MOVF r1, id1

1. To identify the tokens from an input string.
2. It is decided to be what the tokens are on there is a need of some mechanism to recognize token from the input string.

3. The Terminology Used in Lexical Analysis-

(i) Token

(ii) Lexeme

(iii) Pattern

Token:- The Token is a sequence of characters that can be treated as single logic entity.

For Eg - Identifiers, Keywords, Operators, Special Symbol



constant etc.

Pattern :- It is a set of strings in the input

OR

The set of strings is described by a rule called pattern associated with the tokens.

Lexeme:- It is a sequence of the characters in the source program that is matched by the patterns for a token.

Token	Lexeme	Pattern
Const	Const	Constant
If	If	If
Relation	<, <=, ≠	Less than, less than equals to, Not equal to
I	Pi	Any numeric constant
Literal	" "	

Lexical Errors -

Lexical errors is the errors that can be generated by the lexer and after that we are not able to continue next process.

No way to recognize lexeme as a valid token.

Regular Expressions:-

• + \*

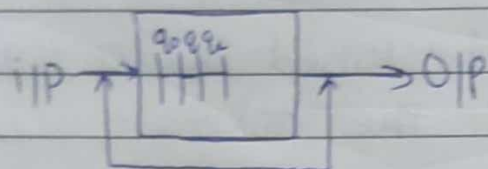
$R^* = \wedge, R, RR, RRR, \dots$

Even no. of zero's RE =  $00.(00)^*$



identifier = letter. (Digit + Letter)\*

Automata



NFA -  $\emptyset, \Sigma, \delta, q_0, f$

$\delta$  - Transition Table

$\emptyset$  - No. of states

" Diagram  
" function

$\Sigma$  - input

$\delta$  - Transition function

$q_0$  - Initial state

DFA -  $\emptyset \times \Sigma \rightarrow \emptyset$

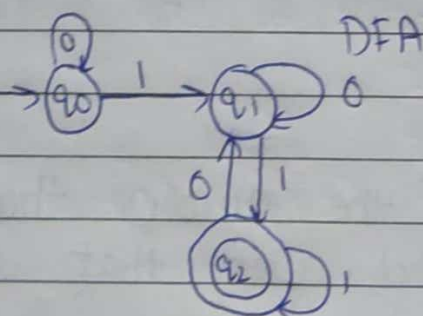
$f$  - final state

NFA -  $\emptyset \times \Sigma \rightarrow 2^{\emptyset}$

$$\delta(q_0, 0) = q_0$$

$$\delta(q_0, 1) = q_1$$

	0	1
$q_0$	$q_0$	$q_1$
$q_1$	$q_1$	$q_2$
$q_2$	$q_1$	$q_2$



Lex specification - Lex program consists of three parts

1. Declaration %x
2. Translation Rule %.
3. Auxiliary Procedure %.

Declaration - In the declaration section includes the declaration of variable and manifest

constant and regular definitions.

Translation Rule- Translation Rule of a lex program are statements of the form.

$P_1 \rightarrow \text{action 1}$

$P_2 \rightarrow \text{action 2}$

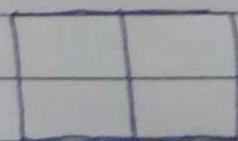
Where  $P$  is a Regular Expression and each action is a program fragment describing what action the Lexical Analyzer should take when a pattern  $p$  match a lexeme.

Auxiliary Procedure- The Third section holds whatever the Auxiliary procedures are needed by the action.

Input Buffering :- The Lexical Analyzer scans the character of the source program one at a time to discover tokens. Because of large amount of time can be consume scanning character specialized buffering techniques had been developed to reduce the amount of overhead required to process input characters.

3. Buffering Techniques are -

1. Buffer Pair
2. Sentinels Sentinels



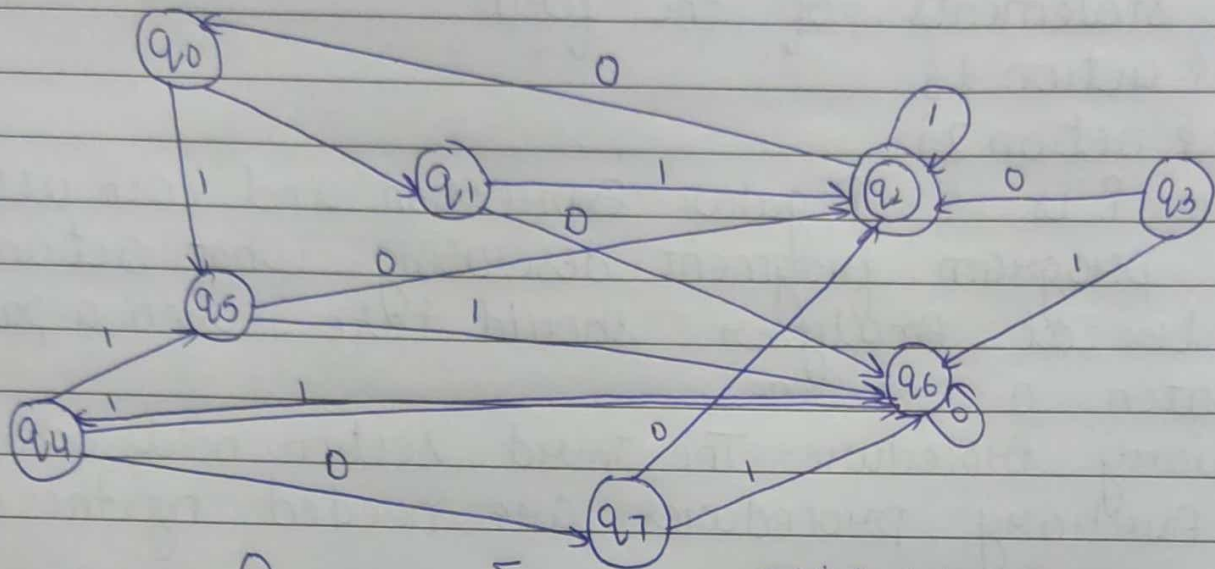
Token  
Beginning

Lookahead  
pointer

In this figure we take buffers divided into two halves suppose 100 each one pointer marks at the beginning of the token being discovered a lookahead pointer scans ahead at the beginning point until the token is discovered.



# Minimization of FA



$$M = \{ (q_0 \dots q_7), \Sigma, \delta, q_0, (q_2, q_6) \}$$

Q                      Σ                      Initial state      Final state

$$\pi_0 = \{ Q_1^0, Q_2^0 \}$$

$$= \{ (q_2), (q_0, q_1, q_3, q_4, q_5, q_6, q_7) \}$$

Q \ Σ	0	1
q <sub>0</sub>	q <sub>1</sub>	q <sub>5</sub>
q <sub>1</sub>	q <sub>6</sub>	q <sub>2</sub>
q <sub>2</sub>	q <sub>0</sub>	q <sub>2</sub>
q <sub>3</sub>	q <sub>2</sub>	q <sub>6</sub>
q <sub>4</sub>	q <sub>7</sub>	q <sub>5</sub>
q <sub>5</sub>	q <sub>2</sub>	q <sub>6</sub>
q <sub>6</sub>	q <sub>6</sub>	q <sub>4</sub>
q <sub>7</sub>	q <sub>6</sub>	q <sub>2</sub>

$$\pi_1 = \{ (Q_1^1, Q_2^1) \}$$

$$= \{ (q_2), (q_0, q_4, q_6), (q_1, q_7), (q_3, q_5) \}$$

$$\pi_2 = \{ (Q_1^2, Q_2^2) \}$$

$$= \{ (q_2), (q_0, q_4), (q_1, q_7), (q_3, q_5), (q_6) \}$$

$$\pi_3 = \{ Q_1^3, Q_2^3 \}$$

$$= \{ (q_2), (q_0, q_4), (q_1, q_7), (q_3, q_5), (q_6) \}$$

A                      B                      C                      D                      E

$$\pi_{n+1} = \pi_n$$

$$M' = \{ (A, B, C, D, E), \Sigma, \delta', q_0', F' \}$$

		/	/

Q \ $\Sigma$	0	1
Init Halt state B	C	D
C	E	A
D	A	E
E	E	B
A	B	A

↑  
 final  
 state

