

Multiple piezo-patch energy harvesters integrated to a thin plate with AC-DC conversion: Analytical modeling and numerical validation

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ABSTRACT

Plate-like components are widely used in numerous automotive, marine, and aerospace applications where they can be employed as host structures for vibration based energy harvesting. Piezoelectric patch harvesters can be easily attached to these structures to convert the vibrational energy to the electrical energy. Power output investigations of these harvesters require accurate models for energy harvesting performance evaluation and optimization. Equivalent circuit modeling of the cantilever-based vibration energy harvesters for estimation of electrical response has been proposed in recent years. However, equivalent circuit formulation and analytical modeling of multiple piezo-patch energy harvesters integrated to thin plates including nonlinear circuits has not been studied. In this study, equivalent circuit model of multiple parallel piezoelectric patch harvesters together with a resistive load is built in electronic circuit simulation software SPICE and voltage frequency response functions (FRFs) are validated using the analytical distributed-parameter model. Analytical formulation of the piezoelectric patches in parallel configuration for the DC voltage output is derived while the patches are connected to a standard AC-DC circuit. The analytic model is based on the equivalent load impedance approach for piezoelectric capacitance and AC-DC circuit elements. The analytic results are validated numerically via SPICE simulations. Finally, DC power outputs of the harvesters are computed and compared with the peak power amplitudes in the AC output case.

Keywords: Piezoelectricity, energy harvesting, equivalent circuits

1. INTRODUCTION

Vibrational energy harvesting has received significant attention over the past decade with the ultimate goal of powering small electronic components, replacing the conventional batteries and thereby enabling self-powered systems^{1,2}. Various vibration-to-electricity conversion methods such as electrostatic³, electromagnetic⁴, magnetostrictive⁵, and piezoelectric transducers⁶, as well as the electroactive and electrostrictive polymers^{7,8} have been used, among which piezoelectric transduction is the most favored one due to the high power density of piezoelectric materials and ease of implementation at different geometric scales⁹.

In the two decades of piezoelectric energy harvesting literature, cantilever beam harvesters have been explored^{10,11} in many studies. Different theoretical models^{12,13} together with numerical^{14,15} and/or experimental verifications¹⁶ have been presented by different researchers. Among the many harvesting studies in the literature, few studies have focused on energy harvesting from two-dimensional plate-like structures. Piezo-patch energy harvesters attached to a thin plate have the merits of removing extra mass loading and volumetric occupancy as well as exhibiting relatively large number of vibrational modes compared to the conventional cantilever designs. In addition, frequent use of thin plates/shells in aerospace, automotive and marine applications, makes this implementation convenient to employ and provide broadband energy harvesting. Recently, analytical closed-form expressions and experimental validations for AC electrical outputs of multiple piezoelectric patch energy harvesters structurally integrated to a thin plate is presented by Aridogan et al.¹⁷. The current work extends that model to the AC input – DC output case, where an AC-DC converter circuit is connected to the parallel configurations of the multiple piezo-patch harvesters.

In the modeling of both cantilever beam and plate-like harvesters, usually an equivalent resistive load is used to predict the AC power output. However, for transferring the electrical harvested power to the practical end devices (e.g. batteries, storage capacitor, etc.), a stable DC signal is required. A standard interface circuit that provides a stable DC output at the resistive load includes a full-wave bridge rectifier followed by a smoothing capacitor. Equivalent circuit model of piezo-patch energy harvesters with nonlinear circuit elements can be built into an electronic circuit simulator (e.g., SPICE) to predict the electrical outputs. The analytical^{18, 19} and numerical^{15, 18} methods for obtaining the equivalent circuit parameters of beam harvesters have been presented in the literature. More recently, Bayik et al.²⁰ have developed the multi-modal analytical and numerical approach for identifying the equivalent circuit parameters of a single piezo-patch harvester attached on a thin plate. On the other hand, several studies have focused on analytical modeling for estimating the DC voltage. Lien and Shu²¹ used the energy balance analysis together with the equivalent impedance implementation, which represents the harvesting circuit by an equivalent electrical impedance, to estimate the steady-state DC voltage output of the single-degree-of-freedom (SDOF) harvester. They utilized the equivalent impedance method for an array of cantilever beam harvesters connected in parallel.

This paper presents the analytical formulation with numerical validations of DC electrical outputs for the parallel connection of multiple piezo-patch energy harvesters attached on a thin plate. Analytical model for the steady-state DC voltage output is derived based on the equivalent impedance method. Equivalent impedance of the electromechanical parts by accounting for multiple vibration-modes together with the interface circuit are built in the electronic circuit simulator (e.g., SPICE), and simulations are performed for the cases of AC input – AC output and AC input – DC output.

2. ANALYTICAL DISTRIBUTED-PARAMETER MODELLING

Figure 1 presents the multiple piezo-patch energy harvesters connected in parallel and attached on a thin plate. The host plate with the length and width of a and b is excited by a transverse point force $f(t)$ acting at the position coordinates (x_0, y_0) . The plate thickness (h_s) is assumed to be much smaller than other dimensions so that the effects of transverse shear deformation are neglected based on the Kirchhoff plate theory. In the following, the subscripts \bar{s} and \bar{p} stand for the parameters related to the host structure and piezo-patches, respectively. The number of piezo-patch harvesters is $n_{\bar{p}}$, and each piezoceramic patch covers a rectangular area with two corners at $(x_{k,1}, y_{k,1})$ and $(x_{k,2}, y_{k,2})$ where $k = 1, 2, \dots, n_{\bar{p}}$. The thickness of the k -th patch is $(h_{\bar{p}})_k$ and the polarization is in thickness direction.

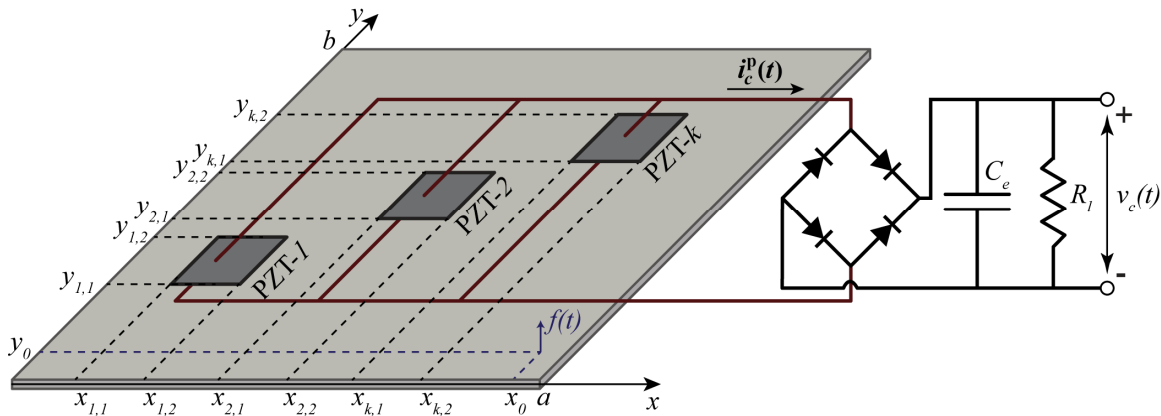


Figure 1. Parallel configuration of Multiple piezo-patch energy harvesters attached on a thin plate with a practical interface circuit

By conducting the modal analysis procedure for the two-dimensional structure¹⁷, electromechanically coupled ordinary differential equations of the plate in modal coordinates can be written as

$$\frac{d^2 \eta_{mn}^p(t)}{dt^2} + 2\zeta_{mn}\omega_{mn} \frac{d\eta_{mn}^p(t)}{dt} + \omega_{mn}^2 \eta_{mn}^p(t) - v^p(t) \sum_{k=1}^{n_p} (\tilde{\theta}_{mn})_k = f_{mn}(t) \quad (1)$$

$$\sum_{k=1}^{n_p} \sum_{n=1}^{\infty} \sum_{m=1}^{\infty} (\tilde{\theta}_{mn})_k \frac{d\eta_{mn}^p(t)}{dt} + \frac{dv^p(t)}{dt} \sum_{k=1}^{n_p} (C_{\bar{p}})_k + i_c^p(t) = 0 \quad (2)$$

where $\eta_{mn}^p(t)$, ω_{mn} and ζ_{mn} are the corresponding modal coordinate, undamped natural frequency and modal damping ratio of the plate for the mn -th vibration mode. The equivalent capacitance for the k -th piezo-patch is defined as $(C_{\bar{p}})_k = (\bar{\epsilon}_{33}^S)_k \frac{(l_{\bar{p}})_k (w_{\bar{p}})_k}{(h_{\bar{p}})_k}$. The piezo-patch dimension terms $(l_{\bar{p}})_k$, $(w_{\bar{p}})_k$ and $(h_{\bar{p}})_k$ are the length, width and thickness of the k th patch harvester, respectively, and $(\bar{\epsilon}_{33}^S)_k$ is the permittivity of the k th patch at constant strain. The electromechanical coupling term $(\tilde{\theta}_{mn})_k$ and modal forcing input $f_{mn}(t)$ can be given by

$$(\tilde{\theta}_{mn})_k = \theta_k \int_{y_{k,1}}^{y_{k,2}} \int_{x_{k,1}}^{x_{k,2}} \left[\frac{\partial^2 \phi_{mn}(x, y)}{\partial x^2} + \frac{\partial^2 \phi_{mn}(x, y)}{\partial y^2} \right] dx dy \quad (3)$$

$$f_{mn}(t) = f(t) \phi_{mn}(x_0, y_0) \quad (4)$$

Here, $\phi_{mn}(x, y)$ is the mass-normalized eigenfunction of the plate for the mn -th vibration mode at (x, y) location. The electromechanical term θ_k for the k th patch harvester is defined as $\theta_k = (h_{pc})_k (\bar{\epsilon}_{31})_k$, which is the multiplication of effective piezoelectric constant $(\bar{\epsilon}_{31})_k$ and reference distance $(h_{pc})_k$ of the center layer of k th piezo-patch from the reference surface (i.e., the neutral surface level of the plate in the piezo region) at the location of the k -th patch. Note that the voltage across each piezo electrode is equal and given by $v^p = v_k^p(t)$, and the current flowing to the interface circuit is identified by $i_c^p(t)$.

3. EQUIVALENT IMPEDANCE MODELING

3.1 Equivalent Impedance of the mechanical domain

The analytical distributed-parameter model of the plate with multiple piezo-patch energy harvesters is presented for the AC input - AC output case, where the interface circuit is a simple resistive load¹⁷. However, in practical applications where the storage systems usually require a stable DC signal, a standard full-wave rectifier circuit is employed for AC-DC conversion. With presence of nonlinear circuit elements, such as diodes, one approach to estimate the DC power output is to build the equivalent circuit model of the electromechanical system in the electronic software simulation (e.g., SPICE) and perform transient simulation. The presented model in this study, accounts for two-way electromechanical coupling and multi-vibration mode of the structure.

Figure 2 demonstrates the equivalent circuit modeling for the multiple piezo-patch harvesters connected in parallel. Each vibrational mode of the harvester is represented as a second-order circuit connected to each piezo-patch capacitance $((C_{\bar{p}})_k)$. By applying Kirchhoff's voltage law to the multi-vibration mode circuit and by analogy with equation (1), system parameters of the equivalent circuit model can be determined. Table 1 gives a summary of the analogy between the analytical distributed-parameter expression and the equivalent circuit model.

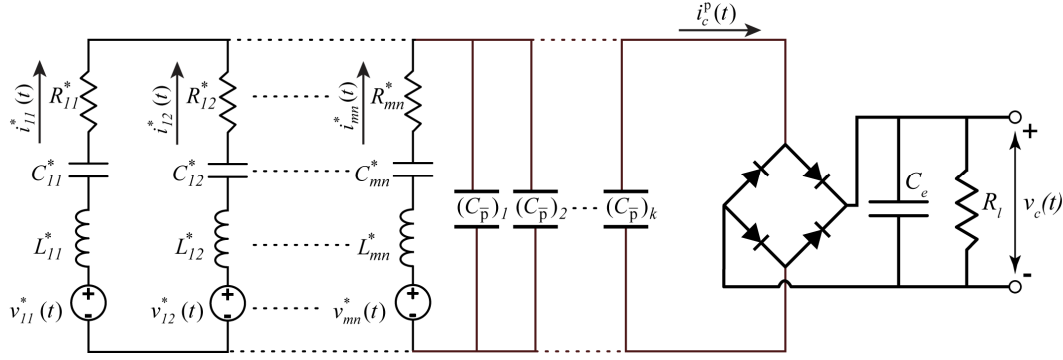


Figure 2. Equivalent circuit representation of the multiple piezo-patch harvesters (parallel configuration) connected to an AC-DC converter by accounting for multiple vibration modes (a total of $m \times n$ modes).

Table 1. Analogy between electrical and mechanical domains of multiple piezo-patch energy harvesters integrated to a thin plate

Equivalent circuit parameters	Mechanical counterparts
Voltage source: $v_{mn}^*(t)$	$-\left(\sum_{k=1}^{n_{\bar{p}}}(\tilde{\theta}_{mn})_k\right)^{-1} f_{mn}(t)$
Electrical current: $i_{mn}^*(t)$	$-\sum_{k=1}^{n_{\bar{p}}}(\tilde{\theta}_{mn})_k \dot{\eta}_{mn}^p(t)$
Inductance: L_{mn}^*	$\left(\sum_{k=1}^{n_{\bar{p}}}(\tilde{\theta}_{mn})_k\right)^{-2}$
Resistance: R_{mn}^*	$2\zeta_{mn}\omega_{mn}\left(\sum_{k=1}^{n_{\bar{p}}}(\tilde{\theta}_{mn})_k\right)^{-2}$
Capacitance: C_{mn}^*	$\omega_{mn}^{-2}\left(\sum_{k=1}^{n_{\bar{p}}}(\tilde{\theta}_{mn})_k\right)^2$

3.2 Equivalent impedance of the electrical domain

In this section, using the energy balance method and equivalent impedance of the electrical circuit, explicit analytical solutions for steady-state DC voltage and power outputs are derived. For modeling of the AC-DC converter circuit, it's been assumed that the diodes are ideal, thus their voltage drop is zero. Additionally, the current flowing to the AC-DC converter circuit $i_c^p(t)$ as shown in figure 1 can be described by the following piecewise equation:

$$i_c^p(t) = \begin{cases} C_e \dot{v}_c(t) + \frac{v_c(t)}{R_l} & \text{if } v^p = v_c^p \\ -C_e \dot{v}_c(t) - \frac{v_c(t)}{R_l} & \text{if } v^p = -v_c^p \\ 0 & \text{if } |v^p| < v_c^p \end{cases} \quad (5)$$

where $v_c^p(t)$ is the rectified voltage over the load resistance. If the smoothing capacitor is carefully chosen so that the output time constant $R_l C_e$ is much larger than the period of the excitation, the DC voltage output will have a constant V_c^p magnitude, and the ripple in the rectified voltage will be negligible for large values of smoothing capacitor^{22, 23}. The steady-state solution for the point force excitation in the form of $f(t) = F_0 e^{j\omega t}$ can be obtained by setting the modal coordinate as

$$\eta_{mn}^p(t) = H_{mn}^p e^{j(\omega t + \tau_{mn})} \quad (6)$$

where H_{mn}^p is the complex amplitude for the modal coordinate and τ_{mn} is the unknown phase shift between the force excitation and the vibration response in modal coordinate. From the current balance equation in (2), the equivalent current source of the harvester can be defined as

$$i^p(t) = \sum_{k=1}^{n_p} \sum_{n=1}^{\infty} \sum_{m=1}^{\infty} (i_{mn}^p(t))_k, \quad (i_{mn}^p(t))_k = -(\tilde{\theta}_{mn})_k \frac{d\eta_{mn}^p(t)}{dt} \quad (7)$$

Further, the dependent current source $i_k^p(t)$ is essentially summation of the electrical current $(i_{mn}^p(t))_k$ over all the m by n modes of the k -th piezo-patch harvester and its complex amplitude can be written as

$$I_k^p = \sum_{n=1}^{\infty} \sum_{m=1}^{\infty} (I_{mn})_k, \quad (I_{mn})_k = -j\omega(\tilde{\theta}_{mn})_k H_{mn}^p e^{j\tau_{mn}} \quad (8)$$

Finally, steady-state form of the overall current source $i^p(t)$ is given by

$$i^p(t) = I^p e^{j\omega t}, \quad I^p = \sum_{k=1}^{n_p} I_k^p \quad (9)$$

Integrating the current balance equation in (2) over the half-cycle period of the excitation, while substituting the current flown to the AC-DC circuit with (5) gives

$$V_c^p = \left(\frac{R_l}{\frac{\pi}{2} + (R_l \omega) C_p^{eq}} \right) I^p \quad (10)$$

Here, C_p^{eq} is the overall piezoelectric capacitance for the parallel connection of piezo-patches and equals to

$C_p^{eq} = \sum_{k=1}^{n_p} (C_p)_k$. Now if all the I_k^p for $k=1, 2, \dots, n_p$ are found, the steady-state DC voltage output for the parallel configuration of multiple piezo-patch harvesters can be directly obtained from (10). To do so, let the Z_{eq}^p be the equivalent impedance for the parallel configuration, which represents the AC-DC circuit dynamics and the equivalent

piezo capacitance as illustrated in figure 3. Note that the equivalent impedance Z_{eq}^p is the function of excitation frequency, equivalent piezoelectric capacitor and dynamics of the interface circuit. However, it doesn't depend on the number of vibrational modes and consequently the number of current sources. Determination of the equivalent impedance for a SDOF piezoelectric energy harvester is presented in ²¹ where $Z_{eq}^p = Z_R^p + jZ_I^p$ and its real and imaginary parts are given as

$$Z_R^p = \frac{2R}{(\pi/2 + C_{\bar{p}}^{eq} R \omega)^2}, \quad Z_I^p = -\frac{R}{(\pi/2 + C_{\bar{p}}^{eq} R \omega)} \quad (11)$$

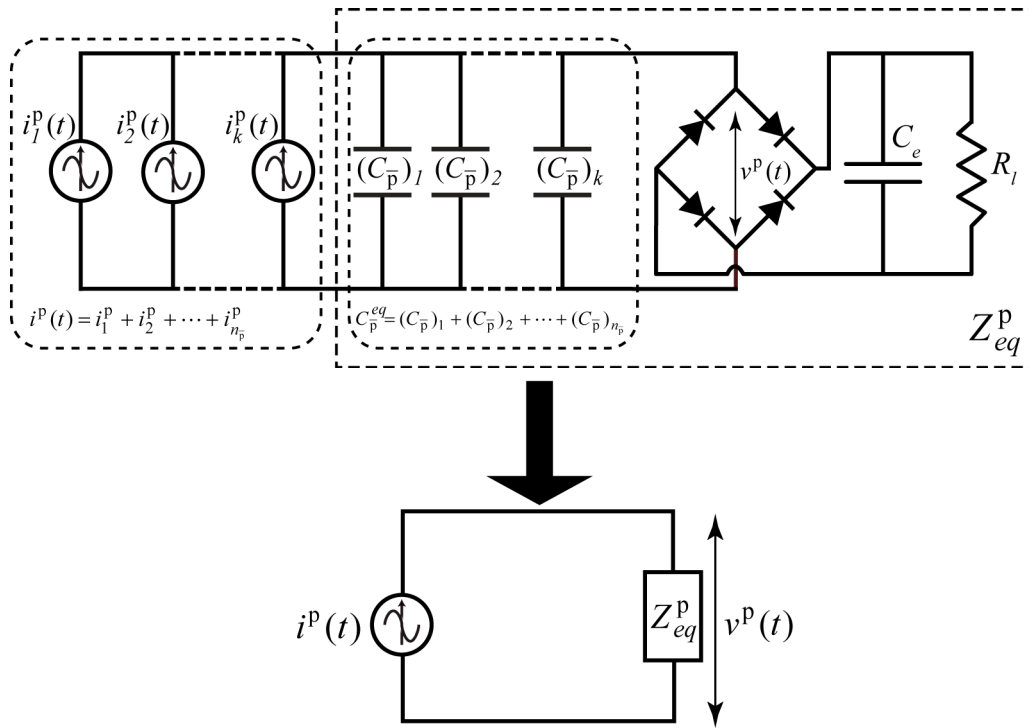


Figure 3. Electrical current model of the harvester: (a) connected AC-DC converter (b) Equivalent Impedance replacing the piezoelectric capacitance and the AC-DC converter

This implementation is valid, since Z_{eq}^p is not related to the mechanical part. The relation of current and voltage passing through Z_{eq}^p in figure 3 can be written as

$$v^p(t) = Z_{eq}^p(i^p(t)) = Z_{eq}^p\left(\sum_{k=1}^{n_{\bar{p}}} i_k^p(t)\right) \quad (12)$$

Replacing the $v^p(t)$ in the equation (1) with (12), the electromechanical equation for the parallel configuration of piezo-patches yield to the following steady-state forms

$$(\omega_{mn}^2 - \omega^2 + j2\zeta_{mn}\omega_{mn}\omega)H_{mn}^p e^{j\tau_{mn}} - Z_{eq}^p\left(\sum_{k=1}^{n_{\bar{p}}} I_k^p\right)\sum_{k=1}^{n_{\bar{p}}}(\tilde{\theta}_{mn})_k = F_0\phi_{mn}(x_0, y_0) \quad (13)$$

Dividing the both sides by $\sum_{k=1}^{n_p} (\tilde{\theta}_{mn})_k$ and rearranging the above equation using (8), we have

$$\left[\frac{2\zeta_{mn}\omega_{mn}}{\left(\sum_{k=1}^{n_p} (\tilde{\theta}_{mn})_k\right)^2} + j \frac{\omega^2 - \omega_{mn}^2}{\omega \left(\sum_{k=1}^{n_p} (\tilde{\theta}_{mn})_k\right)^2} \right] \left(\sum_{k=1}^{n_p} (I_{mn})_k \right) + Z_{eq}^p \left(\sum_{k=1}^{n_p} \sum_{n=1}^{\infty} \sum_{m=1}^{\infty} (I_{mn})_k \right) = - \frac{F_0 \phi_{mn}(x_0, y_0)}{\sum_{k=1}^{n_p} (\tilde{\theta}_{mn})_k} \quad (14)$$

By defining $I_{mn} = \sum_{k=1}^{n_p} (I_{mn})_k$ and $V_{mn} = - \frac{F_0 \phi_{mn}(x_0, y_0)}{\sum_{k=1}^{n_p} (\tilde{\theta}_{mn})_k}$, equation (14) can be written in a following matrix equation

$$\mathbf{V} = \mathbf{Z} \mathbf{I} \quad (15)$$

where the equivalent voltage amplitude vector \mathbf{V} with the dimensions of $mn \times 1$, equals to $\mathbf{V} = [V_{11} \ V_{12} \ \dots \ V_{mn}]^T$ and the equivalent current amplitude vector with the dimensions $mn \times 1$ is $\mathbf{I} = [I_{11} \ I_{12} \ \dots \ I_{mn}]^T$. The square matrix \mathbf{Z} with the dimensions of $mn \times mn$ can be written as

$$Z_{ij} = \begin{cases} \frac{2\zeta_{mn}\omega_{mn}}{\left(\sum_{k=1}^{n_p} (\tilde{\theta}_{mn})_k\right)^2} + j \frac{\omega^2 - \omega_{mn}^2}{\omega \left(\sum_{k=1}^{n_p} (\tilde{\theta}_{mn})_k\right)^2} + Z_{eq}^p & i = j \\ Z_{eq}^p & i \neq j \end{cases} \quad (16)$$

Note that from equation (16), $I^p = \sum_{k=1}^{n_p} \sum_{n=1}^{\infty} \sum_{m=1}^{\infty} (I_{mn})_k = \sum_{n=1}^{\infty} \sum_{m=1}^{\infty} I_{mn}$ and each I_{mn} can be found by solving the linear system of equations in (22). Therefore, the steady-state magnitude of DC voltage across the resistive load can be obtained as

$$V_c = \left(\frac{R_l}{\frac{\pi}{2} + (R_l \omega) C_{\bar{p}}^{eq}} \right) |I^p| = \left(\frac{R_l}{\frac{\pi}{2} + (R_l \omega) C_{\bar{p}}^{eq}} \right) \left| \sum_{n=1}^{\infty} \sum_{m=1}^{\infty} I_{mn}^* \right| \quad (17)$$

4. MODEL VALIDATION

In this section, equivalent circuit model of a fully clamped plate with two perfectly bonded piezo-patches is constructed in SPICE software and simulation results are validated for the standard AC input – AC output and AC input – DC output problems. Equivalent circuit model results for AC output configuration are validated using the distributed parameter model developed by Aridogan et al¹⁷. For DC output configuration, the closed-form analytical expression (in section 3) is used for obtaining the DC output results and compared with numerical simulations. The geometric and material properties of the aluminum plate and piezo-patches are given in Table 2.

Table 2. Geometric, material and dielectric properties

Property	Aluminum	Piezoceramic
Length (mm)	580	72.4
Width (mm)	540	72.4
Thickness (mm)	1.96	0.267
Young's modulus (GPa)	70	66
Mass density (kg m^{-3})	2700	7800
Piezoelectric constant d^{31} (pm V^{-1})	-	-190
Permittivity constant $\bar{\epsilon}_{33}^S$ (nF m^{-1})	-	10.38
Damping ratio ζ_{mn}	0.01	0.01

4.1 AC input – AC output case study

In the standard AC input – AC output problem, the energy harvesting circuit is simply represented by only a resistance load. Parameters of the multi-mode equivalent circuit model in Table 1 are identified from analytical model considering the 5×5 vibration modes of the plate. Steady-state voltage frequency response functions (FRFs) of the multiple piezo-patch harvesters in parallel configuration under the effect of 1N harmonic point force are shown for the first four vibration modes in figure 4 with resistive load cases ranging from 100Ω to $1\text{M}\Omega$. The first four short-circuit resonance frequencies of the fully clamped plate in this case study are 53.32, 103.90, 183.84 and 206.54 Hz.

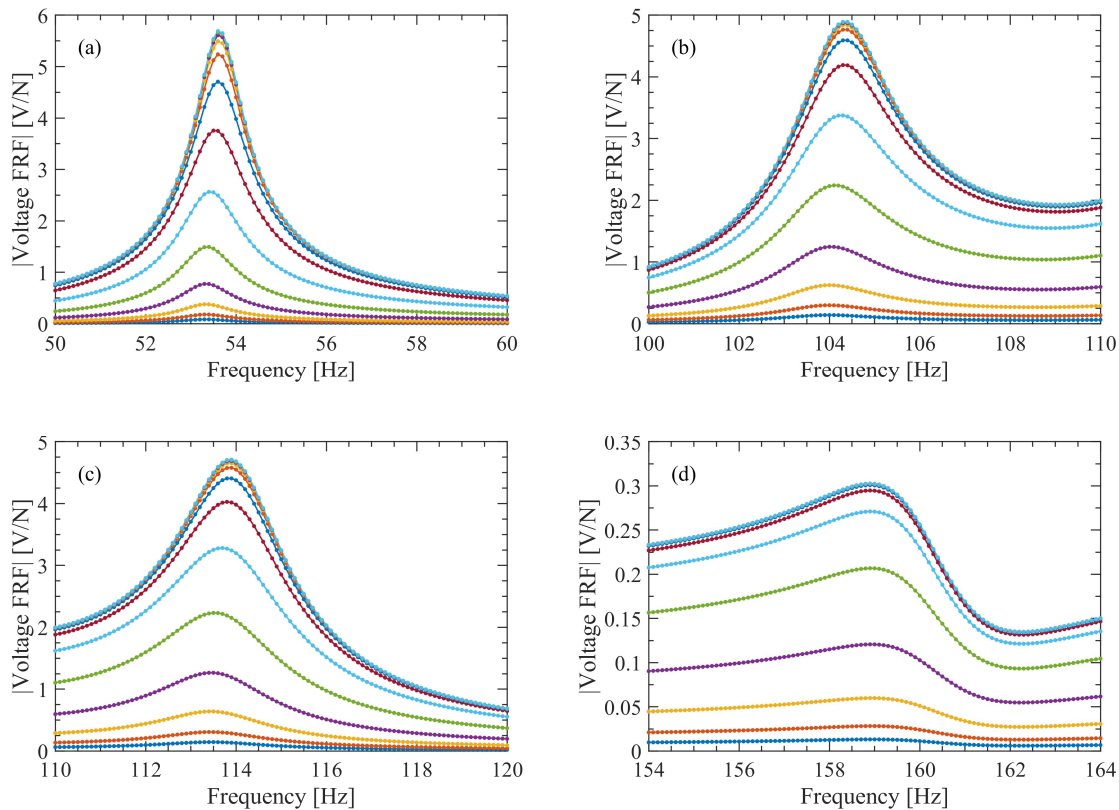


Figure 4. AC input – AC output case: Comparison of analytical (solid line) and SPICE (dot markers) voltage FRFs for the multiple piezo-patch harvesters connected in parallel: a) first mode b) second mode c) third mode d) fourth mode

4.2 AC input – DC output case study

The standard AC-DC piezoelectric energy harvesting circuit consists of a full-wave rectifier, a smoothing capacitor, and a resistive load as shown in figure 1. Derivation of the analytical model for estimating the DC voltage output of multiple piezo-patch harvesters was presented in section 3 assuming ideal behavior of the diodes. Transient simulations over wide range of resistance values are performed to estimate the steady-state DC voltage across the load resistance. Note that since the unwanted ripple in the voltage and current output depend on the output time constant $R_l C_e$, smoothing capacitor is carefully chosen so that that the product of $R_l C_e$ is much higher than the ripple period. To this end, in all simulations to ensure the stable voltage output, the output time constant is always around 0.1 seconds, which is approximately ten times the period of voltage/current ripple at the first vibration mode. Therefore, for the load resistance values ranging from 100Ω to $1M\Omega$, the smoothing capacitor values differ from $1mF$ to $100nF$.

Figure 5 presents the comparison of SPICE simulation and the analytical results for DC voltage FRFs across the various load resistances (100Ω to $1M\Omega$) under $1N$ harmonic force excitation. Results again are shown for the first four vibration modes of the structure.

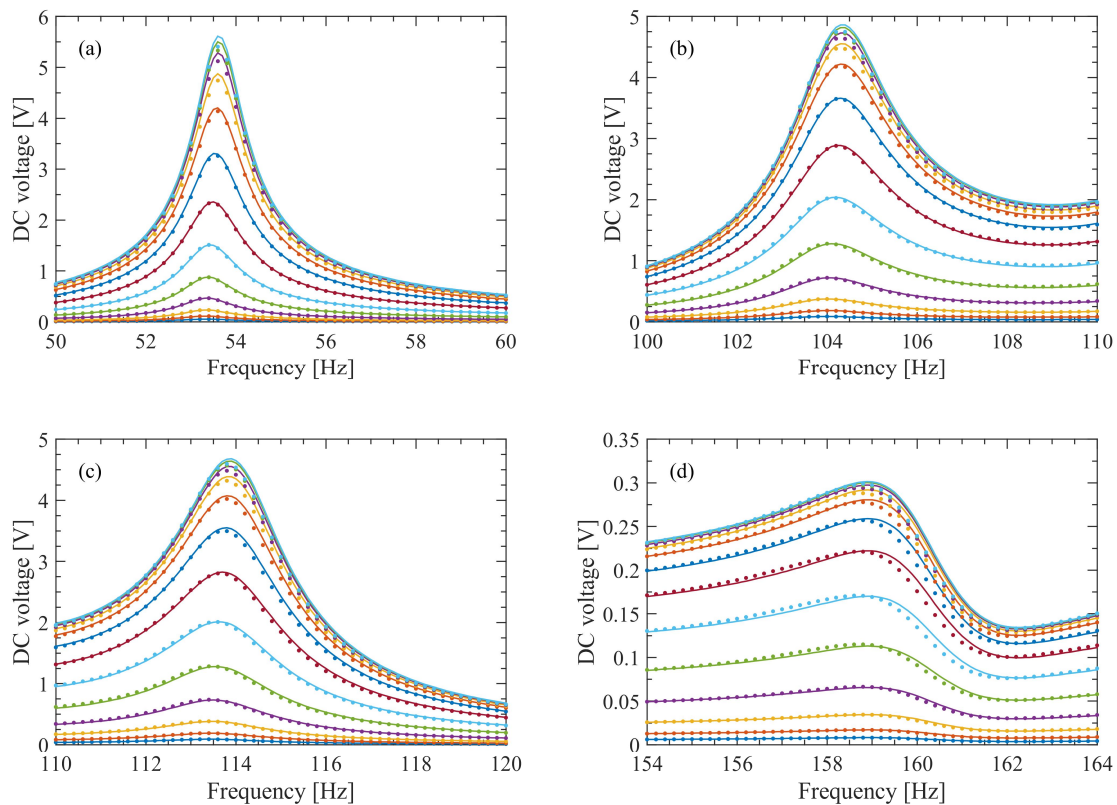


Figure 5. AC input – DC output case: Comparison of the analytically derived model (solid lines) and SPICE simulation results (dot markers) for the parallel configuration of multiple piezo-patch harvesters: (a) first mode, (b) second mode (c) third mode d) fourth mode

4.3 Comparison of power outputs for AC input – AC output & AC input – DC output problems

To investigate the variation of electrically harvested power with different interface circuits, analytical and numerical simulations of peak power output with different resistance load values, for the first four vibration modes, are presented

in figure 6. Power peak amplitudes are extracted at the open-circuit resonance frequency of the plate under 1N excitation force input. The analytical and SPICE simulation results for the modal power outputs are in perfect agreement for both cases.

The power curves have similar trends for both AC – AC and AC – DC cases, exhibiting an optimum resistive load for the maximum power output. Note that in both cases, strain mode shape related cancellation can be seen for the fourth vibration mode. It is also clear that in the AC – AC case, more power is delivered to the electrical part compared to the AC – DC case. The reason can be referred to the general impedance matching theory²⁴, that in weakly coupled system like the plate harvester investigated here, the equivalent resistance in the mechanical impedance (as given in Table 1) is usually much larger than the real component of the equivalent electrical impedance Z_{eq}^P . Therefore, with presence of the AC-DC converter, magnitude of Z_R^P is usually lower than the value of simple resistance load R_l in the AC – AC case, which causes less power to flow into the harvesting circuit.

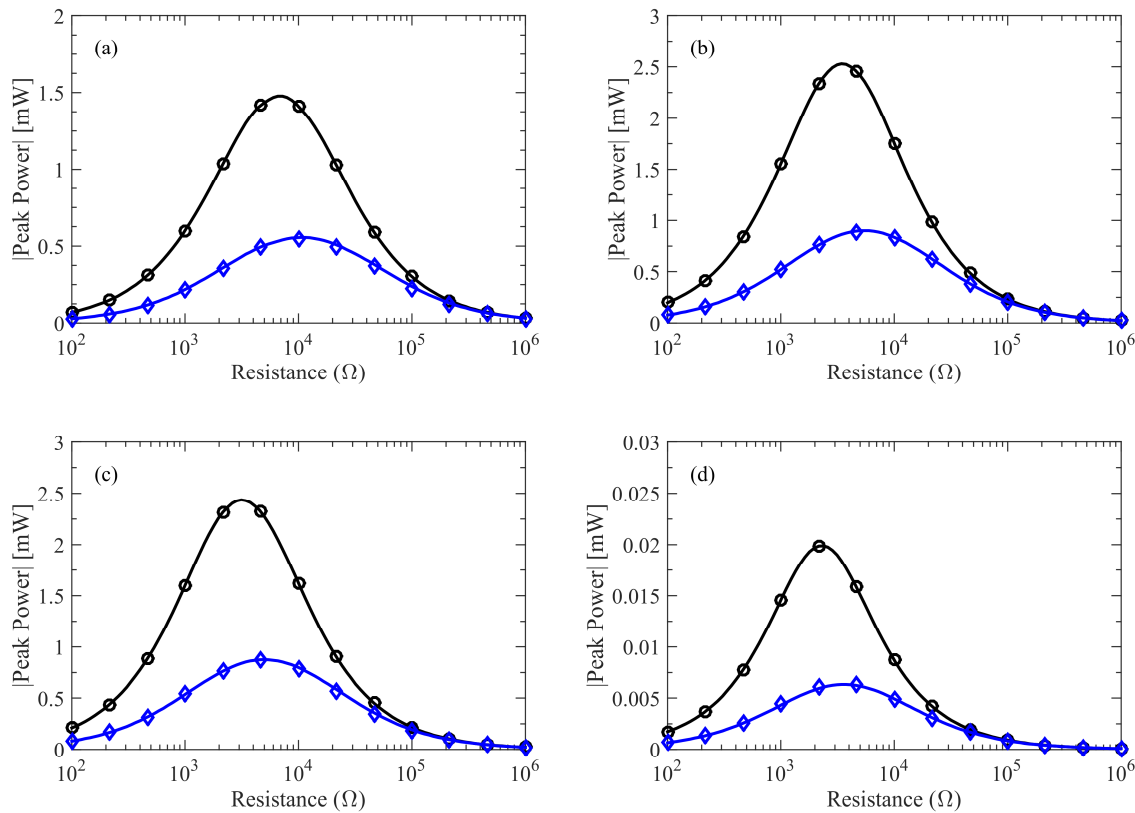


Figure 6. Variations of the peak power amplitudes with load resistance at the open-circuit resonance frequencies for the AC input – AC output (in black) and AC input – DC output (in blue) cases: a) first mode b) second mode c) third mode d) fourth mode. Analytical models shown with solid lines and numerical simulation shown with markers.

5. CONCLUSION

Multiple piezo-patch harvesters are considered as an alternative to cantilevered beam harvesters for exploiting the structural vibrations of thin plates without modifying the dynamics. In this study, closed-form the analytical expression was derived for modeling DC electrical outputs of the harvesters connected in parallel. The equivalent circuit model of the electromechanical system was developed and constructed in SPICE software, and simulation results of voltage frequency response functions were validated against the existing analytical model¹⁷ in the AC input – AC output case.

Then, using the multi-mode equivalent circuit parameters, transient simulations were conducted in the AC input – DC output case for wide range of resistance load values. Steady-state DC electrical responses were investigated for the first four modes of the plate. Resonant DC power outputs across different resistive loads were computed and compared with the peak voltage amplitudes in the AC output case. The multi-mode equivalent circuit model as well as the equivalent impedance approach proposed in this study enable exploring harvesting performance of the single/multiple piezo-patch harvesters attached to thin plates with any linear and nonlinear electrical components.

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