- True / false or multiple choice about: ISA definition, classification/ comparison, alignments, big vs little endian, instruction format, fields,

- translate certain expression that includes variables and operations into assembly of one of architecture, i.e.

load-store, or register-memory, stack etc., instructions will be provided in random order,

- translate c like code into MIPS64 , C code can be if - else statement , or for loop, addresses of variables will be included, in addition to the list of MIPS64 instructions

-Single Cycle datapath: features, restrictions, components, control signals for each instructions/ format (original datapath)

- how to modify for certain instructions, just likes examples discussed in the class and in the HW3, or highlight parts of datapath