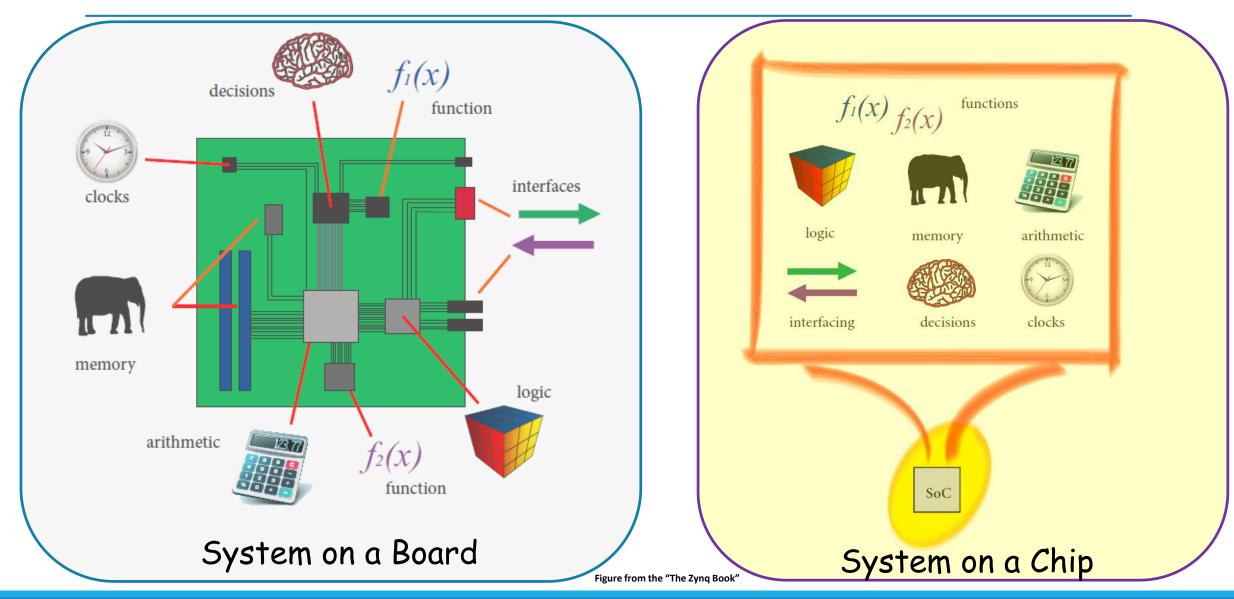
Diseño avanzado de sistemas embebidos en lógica programable:
Zynq APSoC, Vivado-HLS y SDSoC.
Curso de Posgrado

System on a Chip (SoC)

Cristian Sisterna

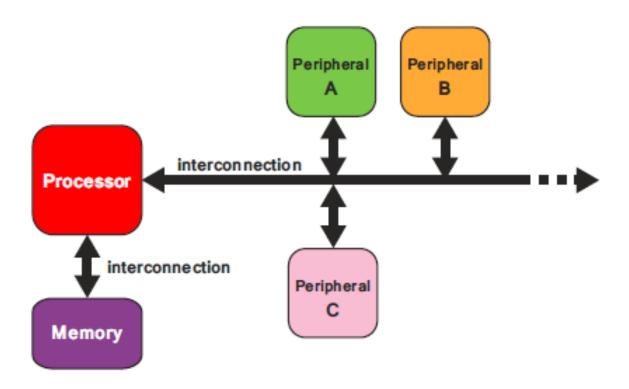
Universidad Nacional San Juan Argentina

System on Chip with Zynq

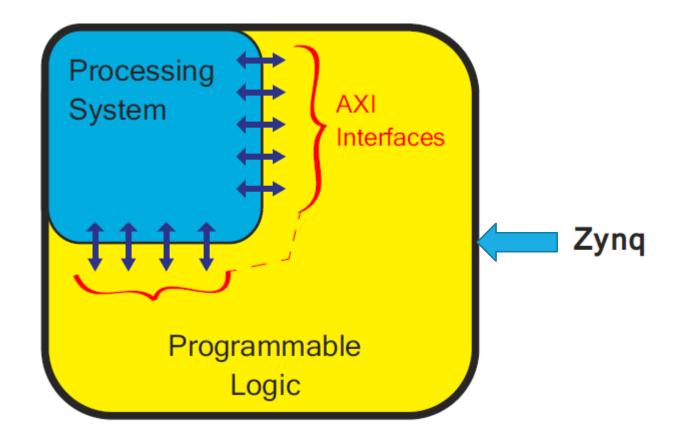


ICTP

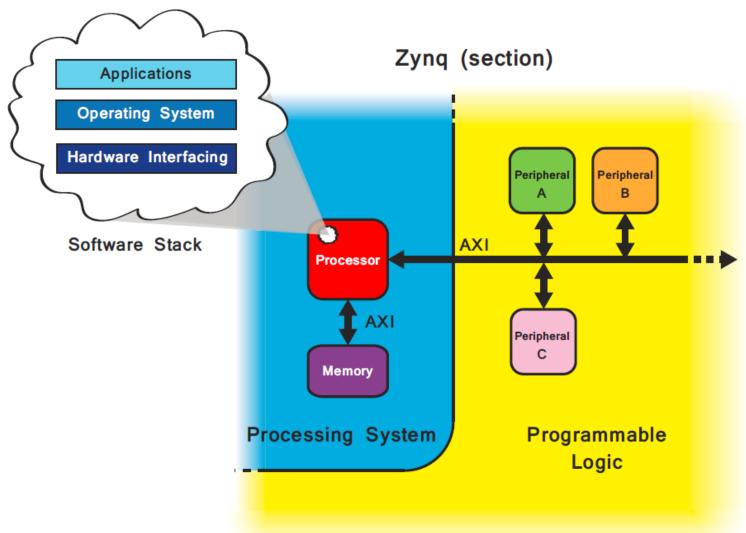
A Simple View of an Embedded SoC



A Simple View of the Zynq SoC



Software System, Hardware System and Zynq

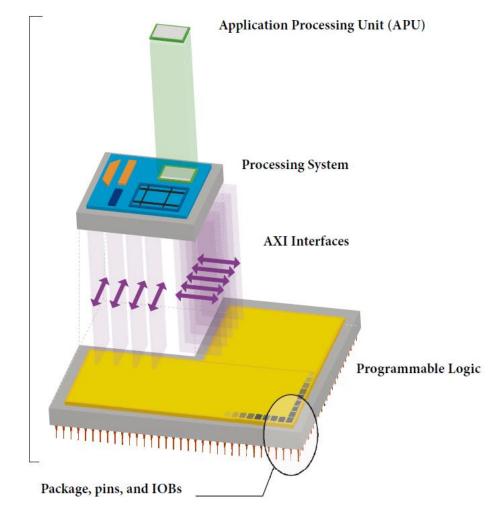


ICTP

Figure from the "The Zynq Book"

SoC

Architectural View of the Zynq



Zynq Architecture and Features

SoC Design Flow

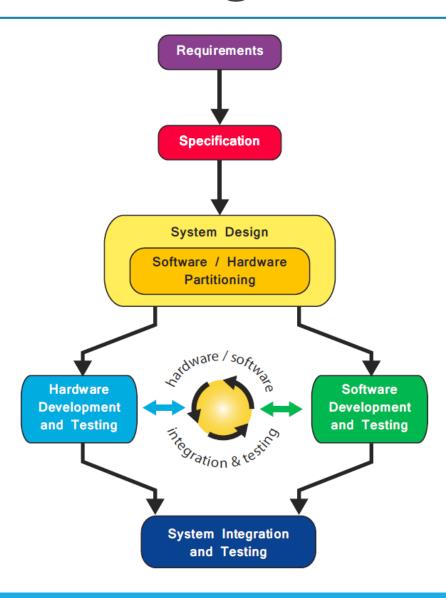
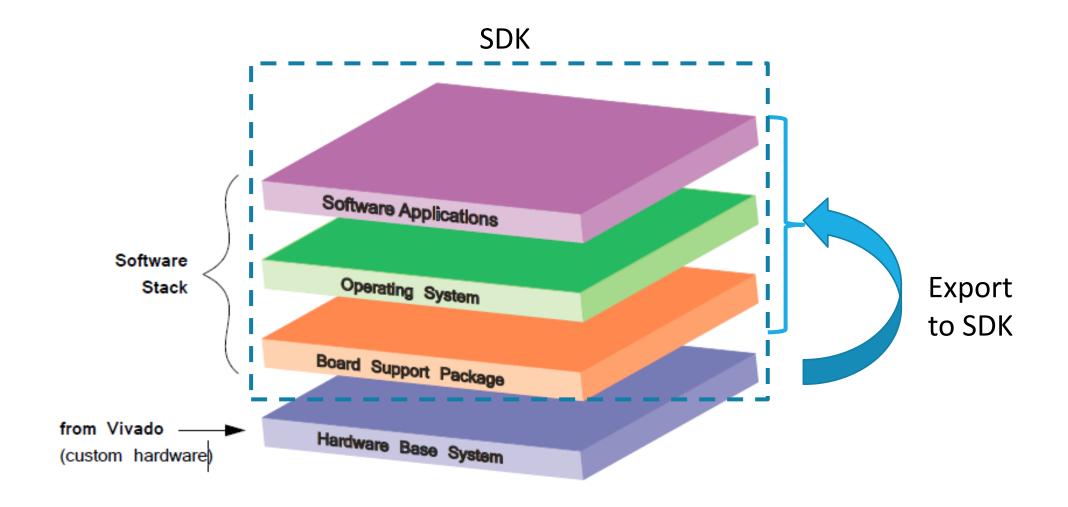
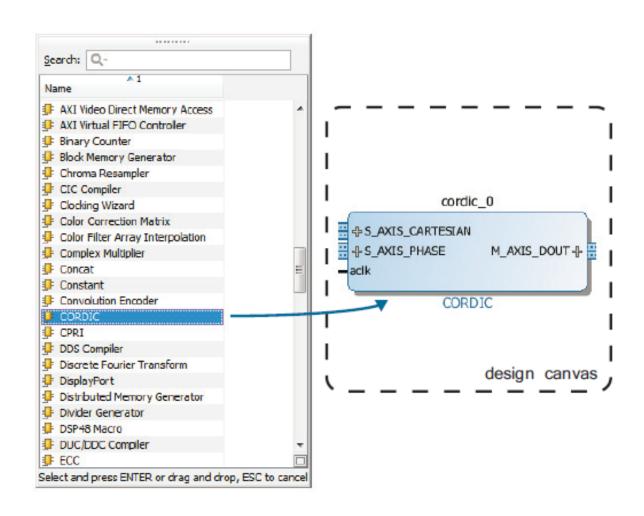


Figure from the "The Zynq Book"

Hardware and Software Layers in a SoC



IP Availability for SoC Designs



ICTP