

System on a Chip (SoC)

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System on Chip with Zynq

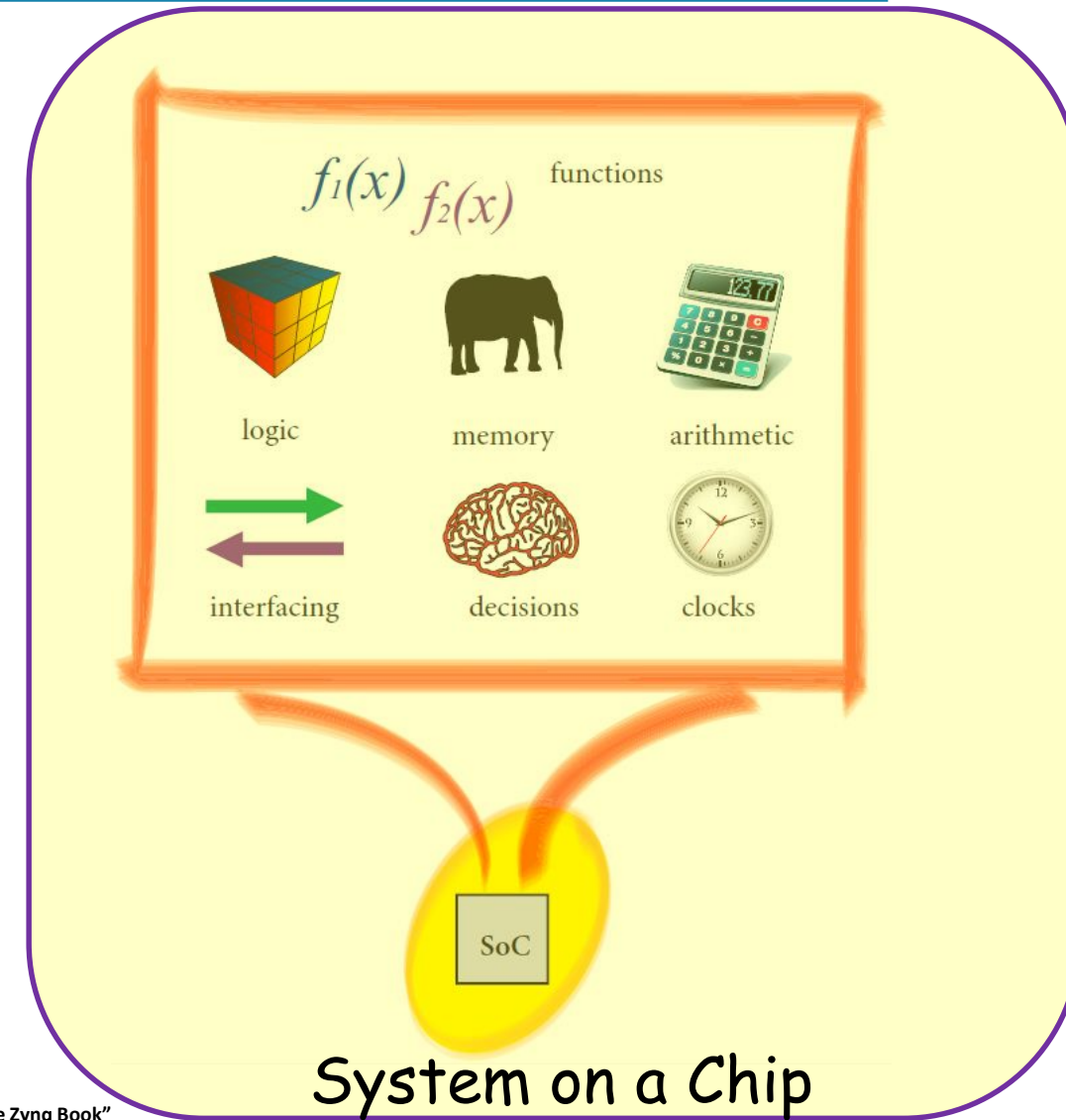
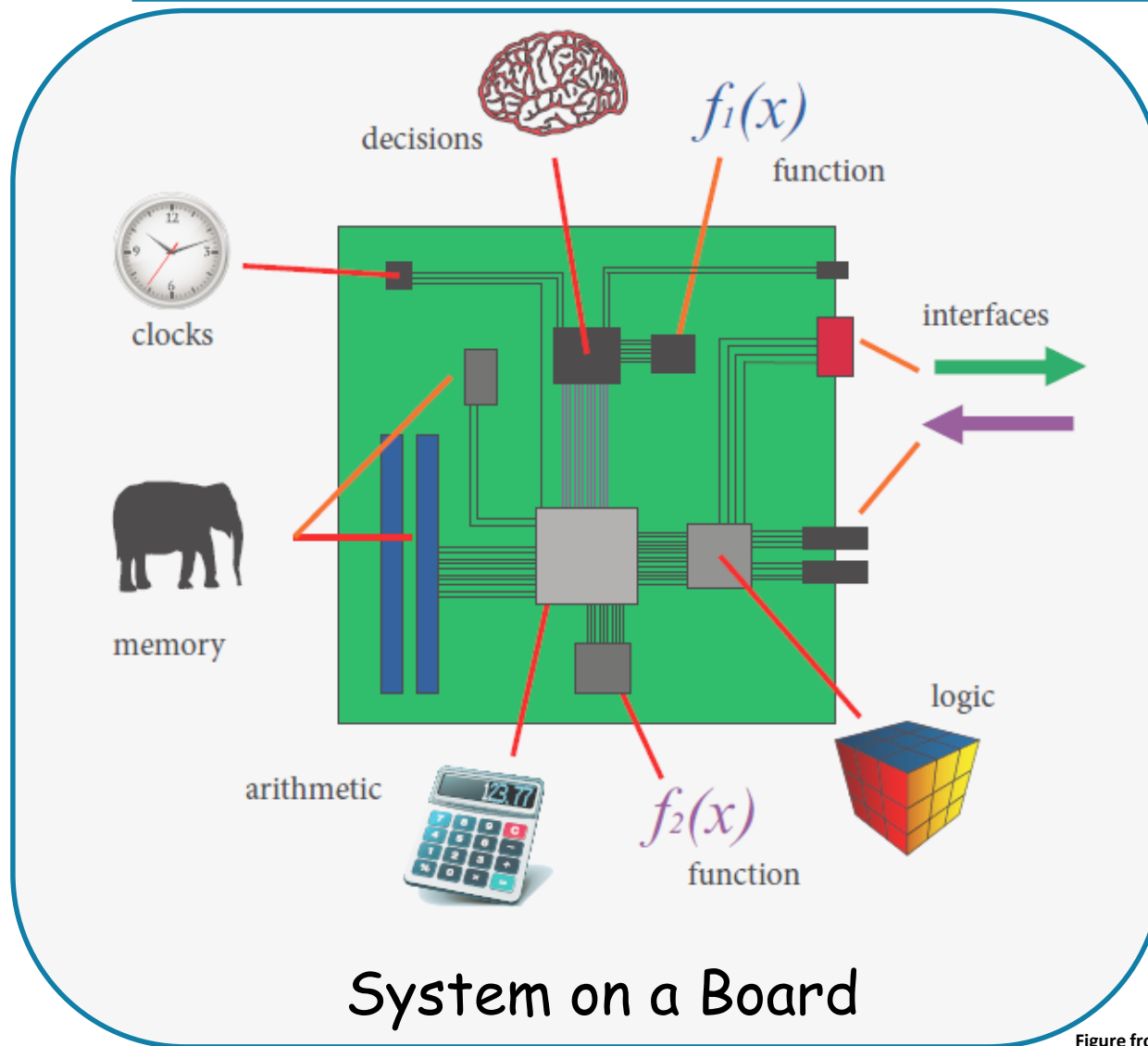


Figure from the "The Zynq Book"

A Simple View of an Embedded SoC

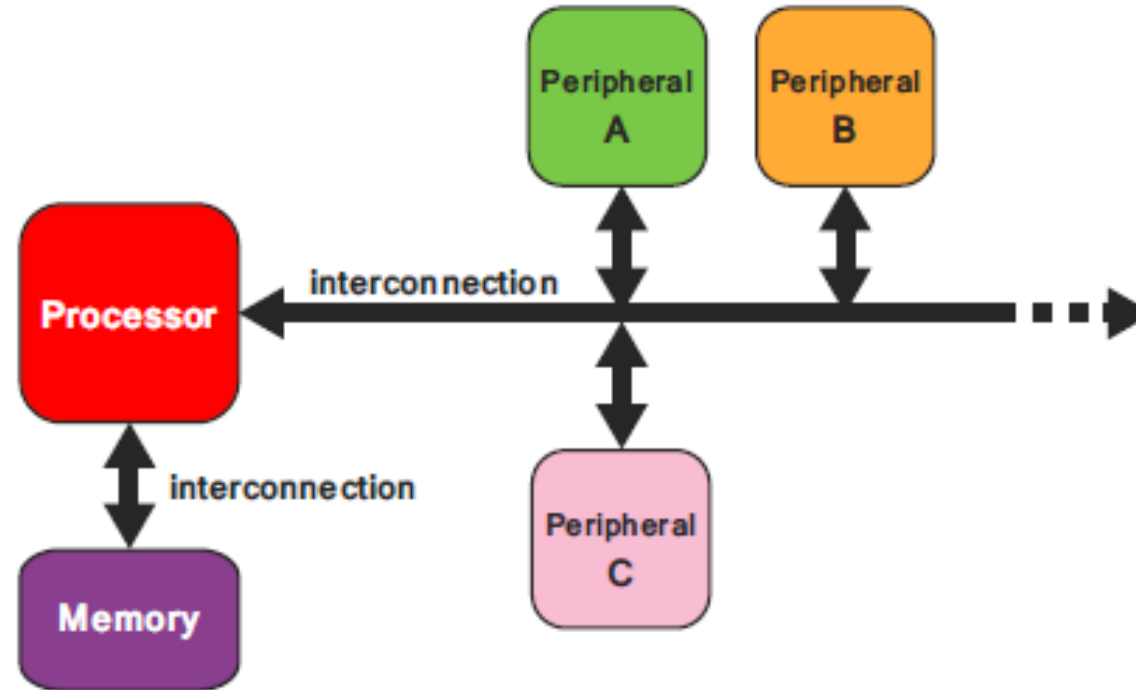


Figure from the "The Zynq Book"

A Simple View of the Zynq SoC

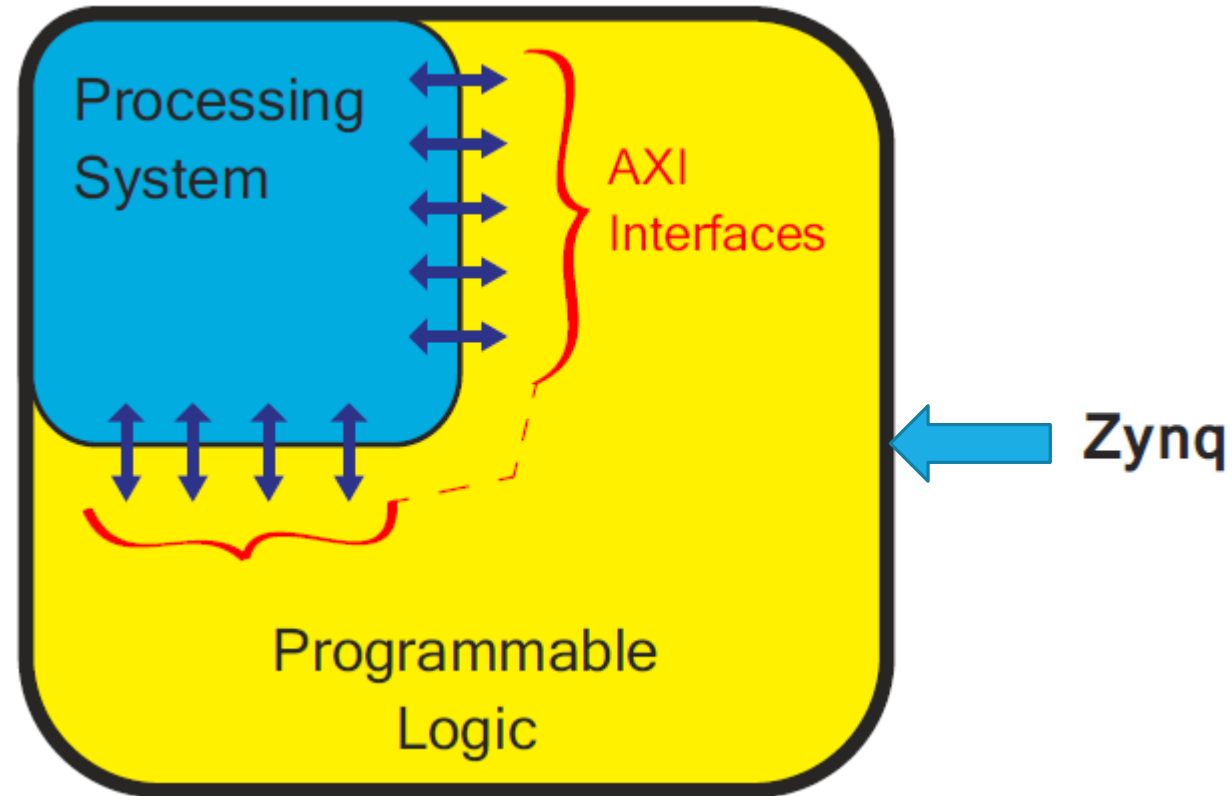


Figure from the "The Zynq Book"

Software System, Hardware System and Zynq

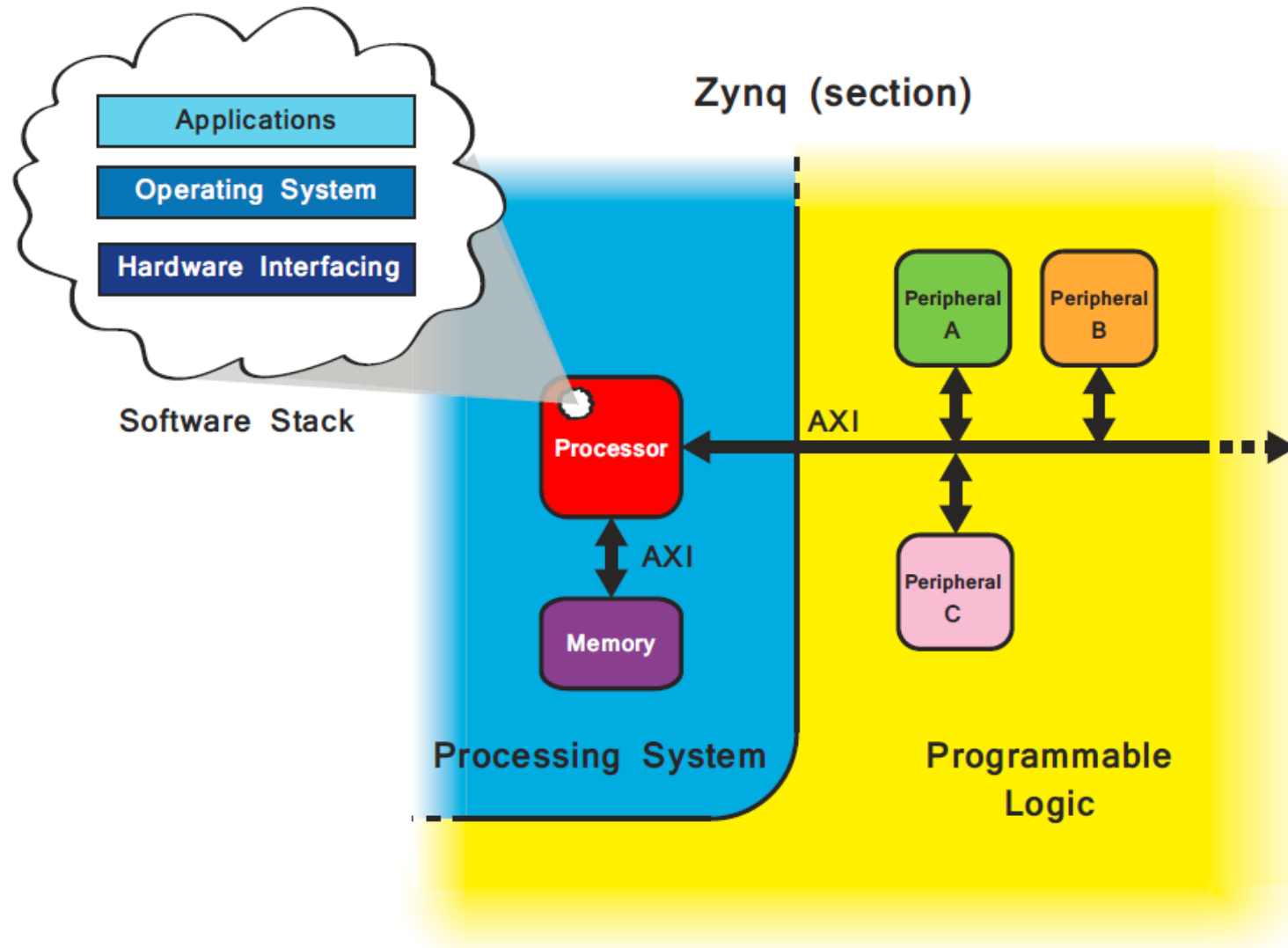
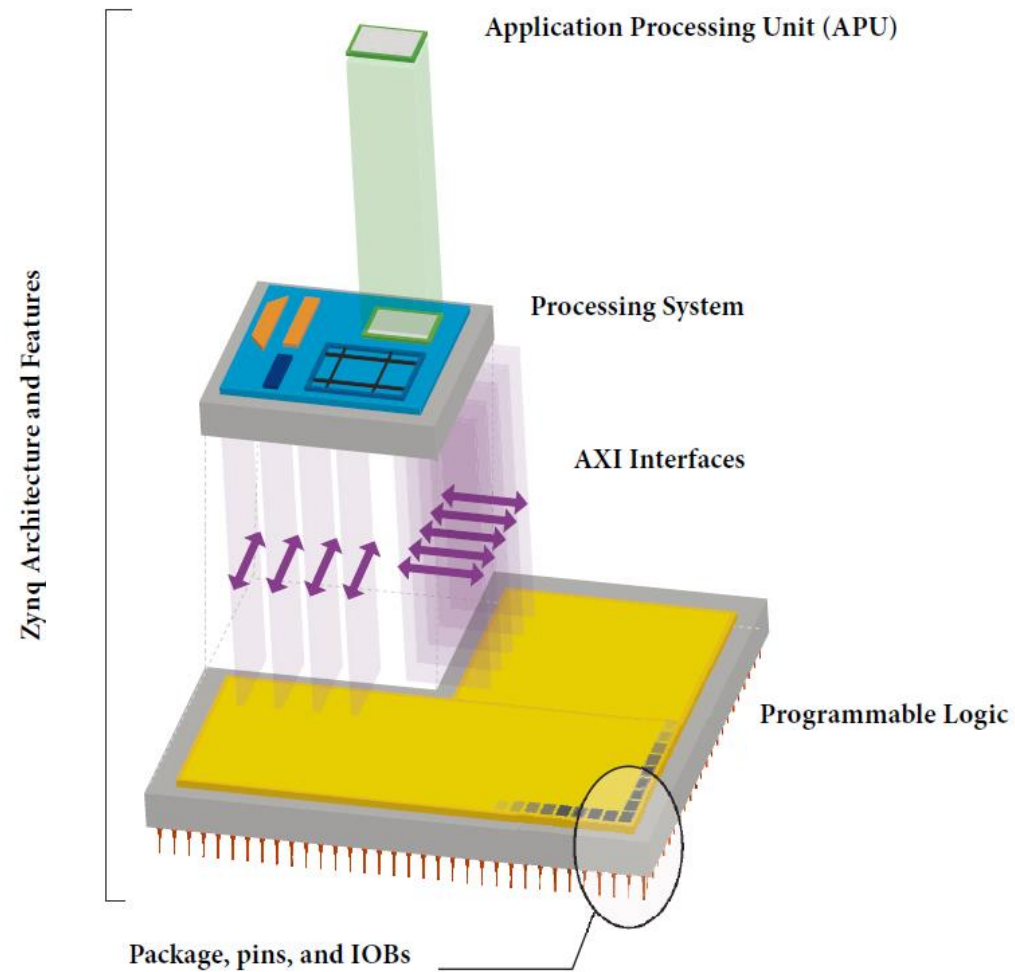


Figure from the "The Zynq Book"

Architectural View of the Zynq



SoC Design Flow

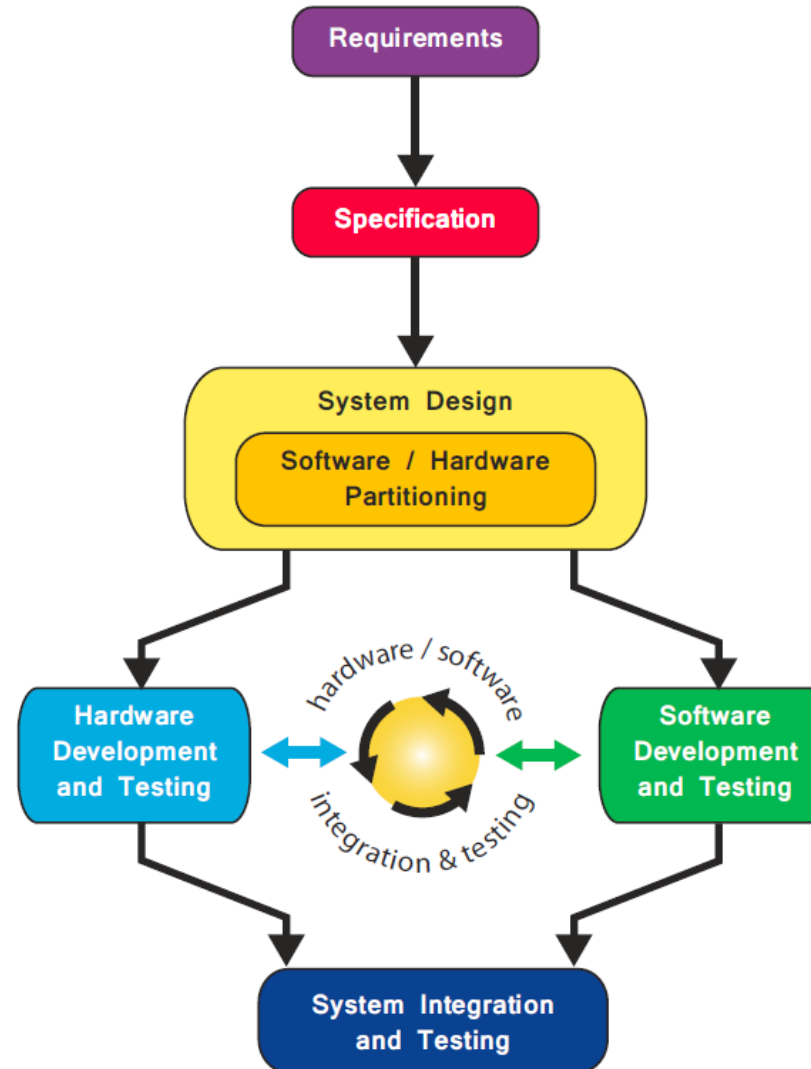


Figure from the "The Zynq Book"

Hardware and Software Layers in a SoC

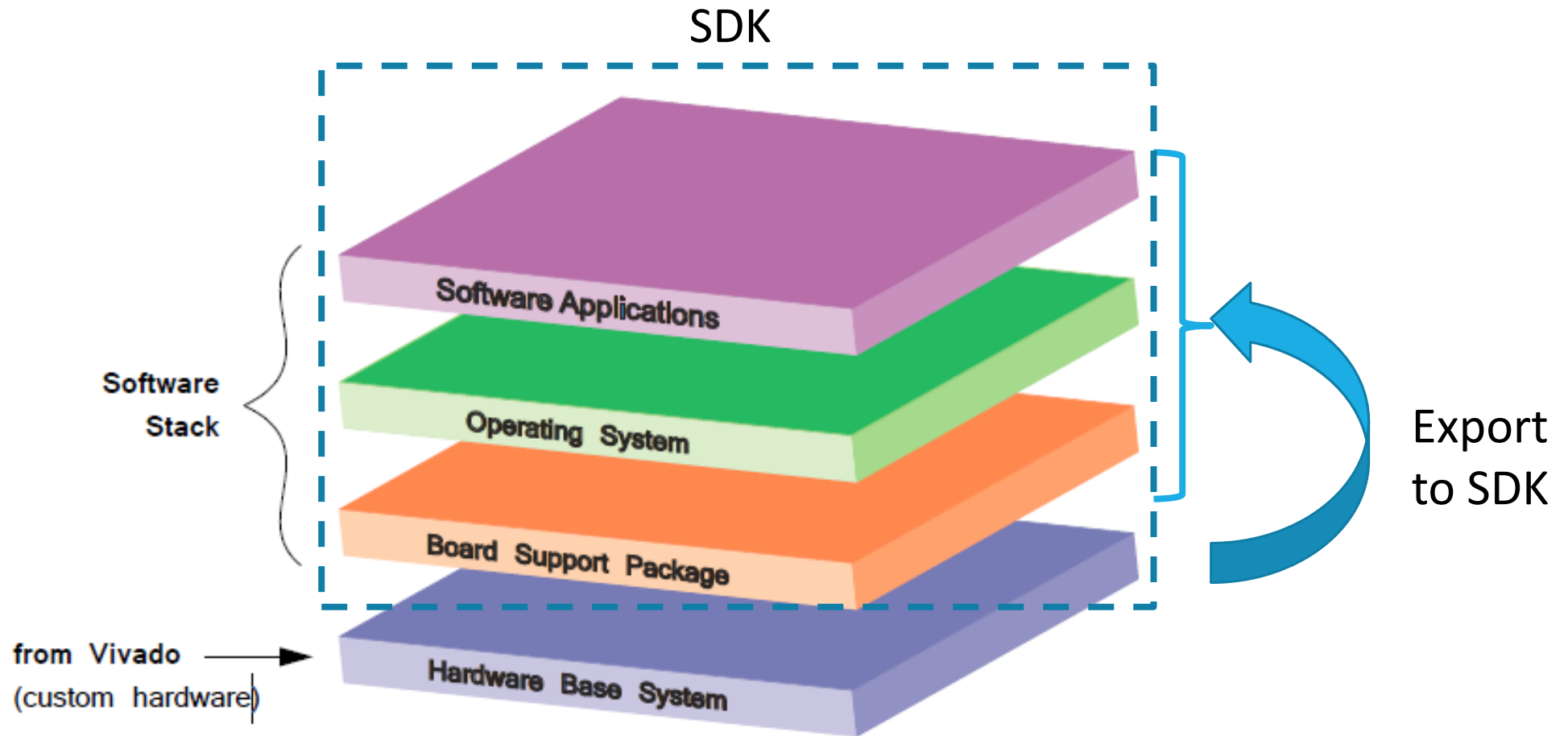


Figure from the "The Zynq Book"

IP Availability for SoC Designs

