



Address Mapping Logic:

```

reg error = {ovrrun, framing}
if (pwrite)
  if (psel && penable)
    if (paddr == 0x2)
      write_select = 0x1
    else if (paddr == 0x4)
      write_select == 0x2
    else
      pslverr = 1
  else if pwrite == 0
    if (paddr == 0x0)
      read_select = 0x1
    else if (paddr == 0x1)
      read_select = 0x0
    else if (paddr == 0x2)
      read_select = 0x3
    else if (paddr == 0x4)
      read_select = 0x4
    else if (paddr == 0x6)
      read_select = 0x2
  
```