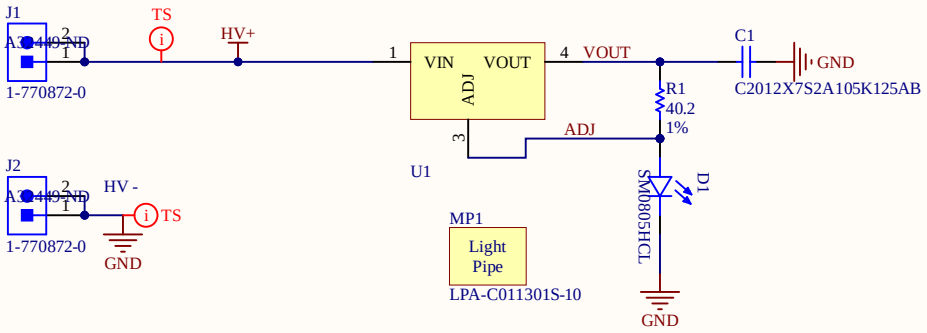

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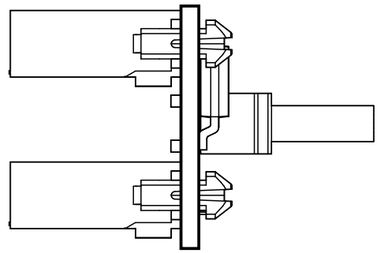
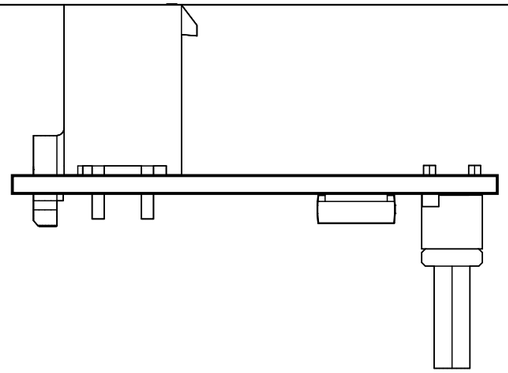
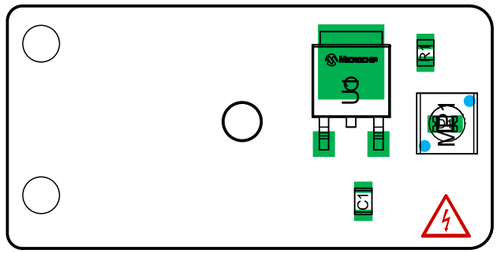
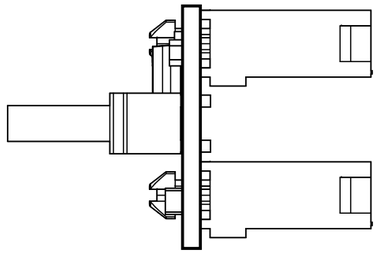
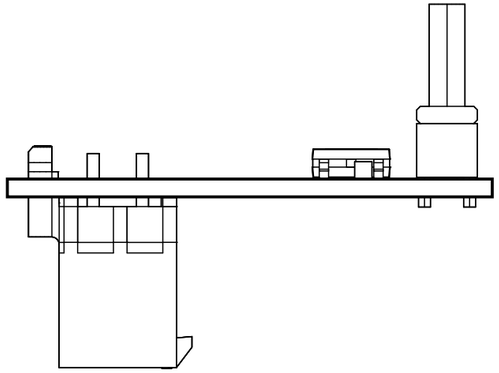
Size: Letter	Number	Revision
Date: 1/30/2020		Sheet of
File: C:\git\..\60V_Ind.SchDoc		Drawn By:

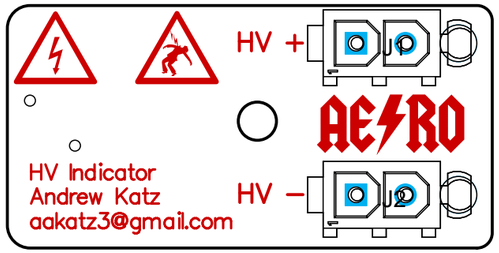












NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS ARE IN INCHES
TOLERANCES:
FRACTIONAL±
ANGULAR: MACH± BEND ±
TWO PLACE DECIMAL ±
THREE PLACE DECIMAL ±

INTERPRET GEOMETRIC TOLERANCING PER:

MATERIAL

FINISH

DO NOT SCALE DRAWING

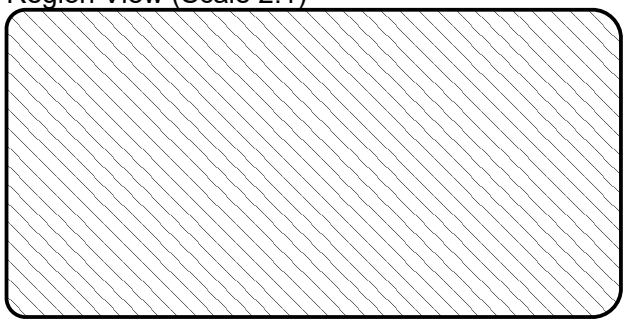
	NAME	DATE
DRAWN		1/30/2020
CHECKED		
ENG APPR.		
MFG APPR.		
Q.A.		

COMMENTS:

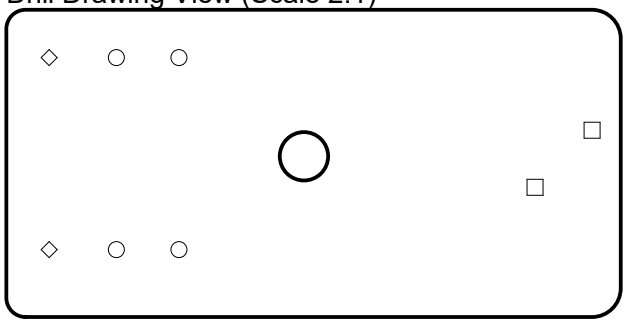
TITLE

SIZE	DWG. NO.	
SCALE: 1:1	WEIGHT:	SHEET 1 OF 1

Region View (Scale 2:1)



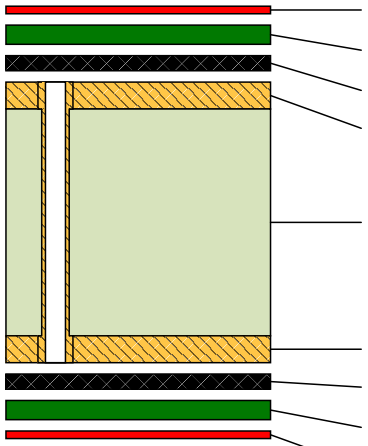
Drill Drawing View (Scale 2:1)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
□	2	40.00mil	Нет	None
○	4	55.00mil	Есть	None
◇	2	125.00mil	Нет	None
	8 Total			

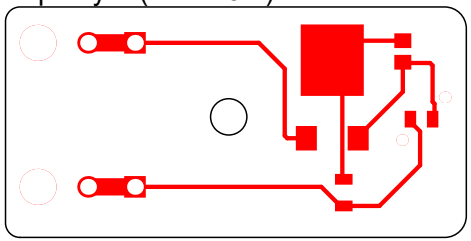
Layer Stack Legend



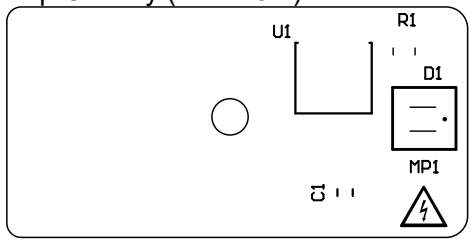
Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
Surface Material	Top Solder	1.00mil(0mm)	SM-001	Solder Mask	GTS
PbSn	Top Surface Finish	0.79mil(0mm)		Surface Finish	
Cu	Top Layer	1.40mil(0mm)		Signal	GTL
Core		*56.00mil(1mm)*	*FR-4*	*Dielectric*	
Cu	Bottom Layer	1.40mil(0mm)		Signal	GBL
PbSn	Bottom Surface Finish	0.79mil(0mm)		Surface Finish	
Surface Material	Bottom Solder	1.00mil(0mm)	SM-001	Solder Mask	GBS
	Bottom Overlay			Legend	GBO

Total thickness: 62.37mil(2mm)

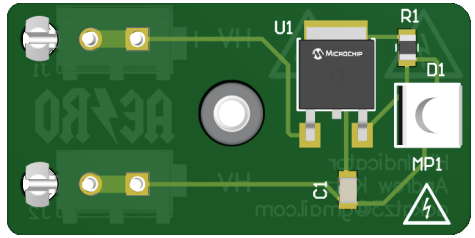
Top Layer (Scale 3:2)



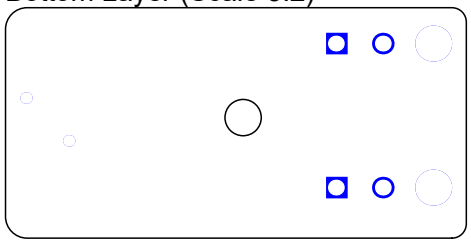
Top Overlay (Scale 3:2)



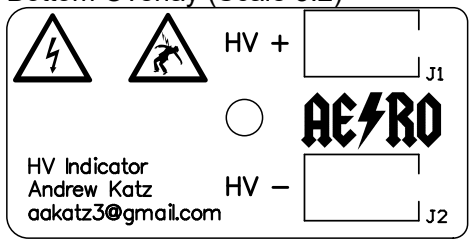
Realistic View



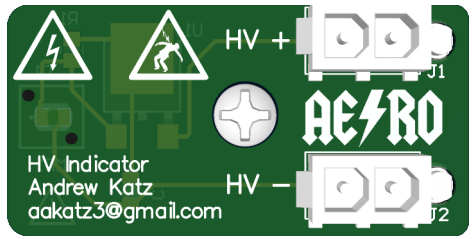
Bottom Layer (Scale 3:2)



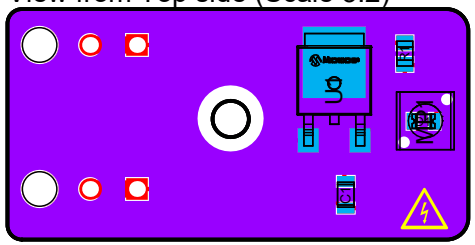
Bottom Overlay (Scale 3:2)



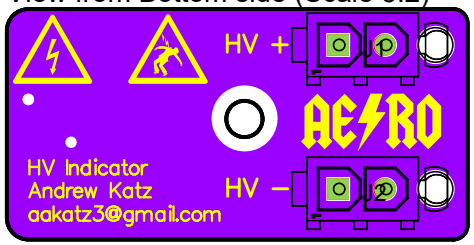
Realistic View

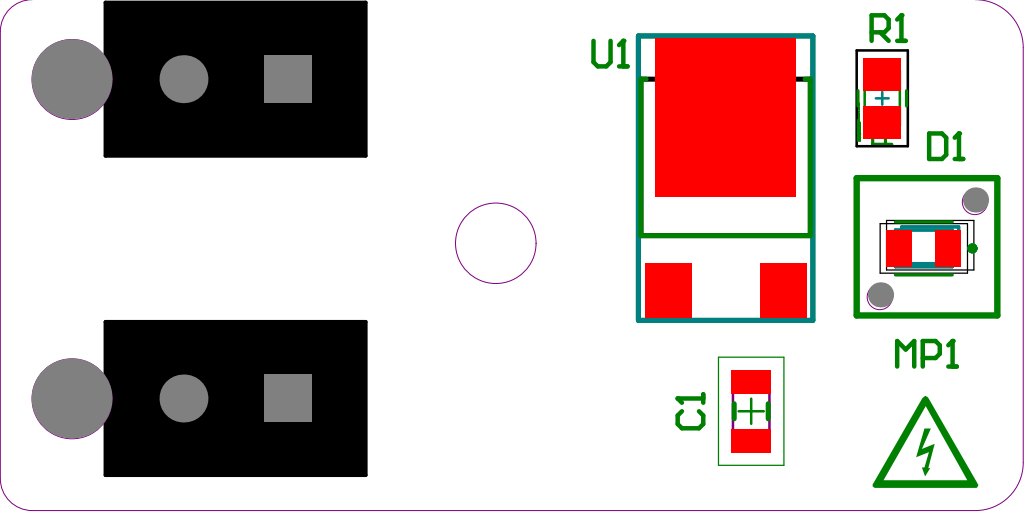


View from Top side (Scale 3:2)

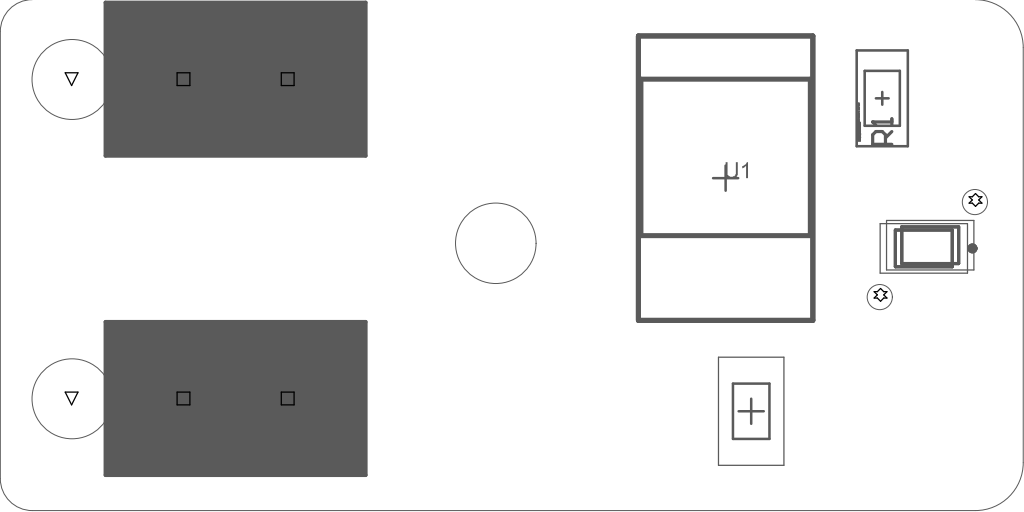


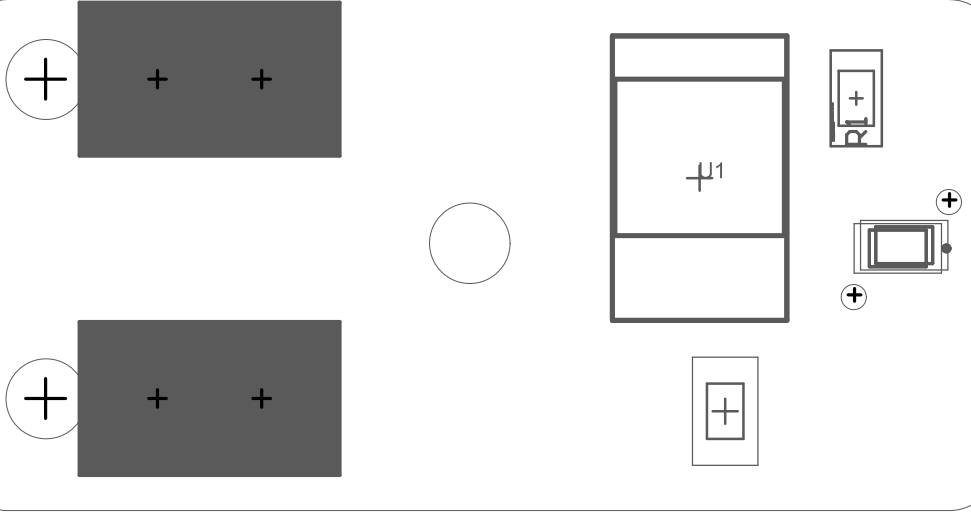
View from Bottom side (Scale 3:2)




U1
R1
D1
MP1
C1

J1
+ HV
AERO
J2
– HV
HV Indicator
Andrew Katz
aakatz3@gmail.com


μ1
R1


R1
μ1

Line #	Name	Description	Designator	Revision ID	Revision State	Quantity	Manufacturer 1	Manufacturer Part Number 1	Manufacturer Lifecycle 1	Supplier 1	Supplier Part Number 1	Supplier Unit Price 1	Supplier Subtotal 1
6	C2012X7S2A105K125AB	Chip Capacitor, 1 uF, +/- 10%, 100 V, -55 to 125 degC, 0805 (2012 Metric), RoHS, Tape and Reel	C1		Unknown server	1	TDK	C2012X7S2A105K125AB	Volume Production	Digi-Key	445-5205-6-ND	0.55	0.55
9	SM0805HCL	Red 631nm LED Indication - Discrete 1.9V 0805 (2012 Metric)	D1		Not managed	1	Bivar	SM0805HCL	Volume Production	Digi-Key	492-2281-1-ND	0.57	0.57
3	1-770872-0	Connector Header Through Hole 2 position 0.163" (4.14mm)	J1, J2		Not managed	2	TE Connectivity	1-770872-0	Volume Production	Digi-Key	A32449-ND	1.22	2.44
7	LPA-C011301S-10	Light Pipe Single Clear Rigid Round with Flat Top, 3mm Board Mount, Press Fit	MP1		Not managed	1	Lumex	LPA-C011301S-10	Volume Production	Digi-Key	67-1833-ND	1.61	1.61
1	1-172165-2	2 Position Rectangular Housing Connector Plug Red 0.163" (4.14mm)	P1		Not managed	1	TE Connectivity	1-172165-2	Volume Production	Digi-Key	A111949-ND	0.69	0.69
2	1-172165-9	2 Position Rectangular Housing Connector Plug Black 0.163" (4.14mm)	P2		Not managed	1	TE Connectivity	1-172165-9	Volume Production	Digi-Key	A111950-ND	0.57	0.57
5	770904-1	Socket Contact Crimp 18-22 AWG Stamped	PIN1, PIN2, PIN3, PIN4		Not managed	4	TE Connectivity	770904-1	Volume Production	Digi-Key	A25684CT-ND	0.11	0.44
4	40.2 Ohms	Resistor Surface Mount, 40.2 Ohms, 0805 Footprint, 1% Tolerance 0.125W	R1		Not managed	1	Vishay	CRCW080540R2FKEA	Volume Production	Digi-Key	541-40.2CDKR-ND	0.1	0.1
8	LR8K4-G	Linear Voltage Regulator IC Positive Adjustable 1 Output 10mA TO-252, (D-Pak)	U1		Not managed	1	Microchip Supertex	LR8K4-G	Volume Production	Digi-Key	LR8K4-GCT-ND	0.93	0.93

Design Rules Verification Report

Filename : C:\git\AERO_2019-2020\60V_IND\Altium\60V_Ind.PcbDoc

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.254mm) (All),(All)	0
Clearance Constraint (Gap=0.85mm) (InNetClass('TS')),(InNetClass('TS'))	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.254mm) (Max=1.524mm) (Preferred=0.254mm) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Hole Size Constraint (Min=0.025mm) (Max=12.7mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.127mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.127mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0
Total	0

Electrical Rules Check Report

Class	Document	Message
Warning	60V_Ind.SchDoc	Off grid MP1 at 5602.457mil,5936.055mil