

# **K. J. Somaiya College of Engineering, Mumbai-77** (A Constituent College of Somaiya Vidyavihar University)

Constituent College of Somaiya Vidyavihar University

Department of Sciences and Humanities



| Course Name:                | Elements of Electrical and Electronics<br>Engineering | Semester:    | I/II            |
|-----------------------------|---|--------------|-----------------|
| <b>Date of Performance:</b> | 27/12/22  | Batch No:    | C2-2            |
| <b>Faculty Name:</b>        | Jyoti Varavedkar                                      | Roll No:     | 1601012210<br>9 |
| Faculty Sign & Date:        |   | Grade/Marks: | / 25            |

# **Experiment No: 10**

# Title: Inverting and Non-inverting amplifier using OPAMP

# **Aim and Objective of the Experiment:**

- To understand inverting configuration of OPAMP
- To understand Non-inverting configuration of OPAMP
- the concept of negative feedback and closed loop configuration of OPAMP.
- To understand gain of inverting and non-inverting OPAMP

# COs to be achieved:

**CO5:** Understand operational amplifier and its applications

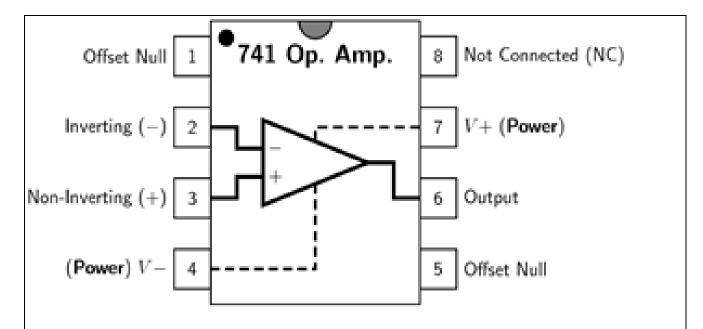
# Circuit Diagram/ Block Diagram:

Pin diagram of IC 741



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#### **Procedure**

1. Click on following link for Virtual lab experiment.

http://vlabs.iitkgp.ac.in/be/exp17/index.html#

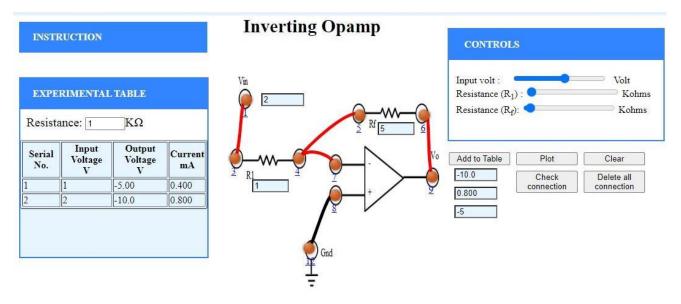
- 2. Go through the theory, solve pre-quiz.
- 3. Go to simulation and perform the experiment by following the given procedure.
- 4. Prepare observation table, plot the graph based on observations.
- 5. Write conclusion based on observations.

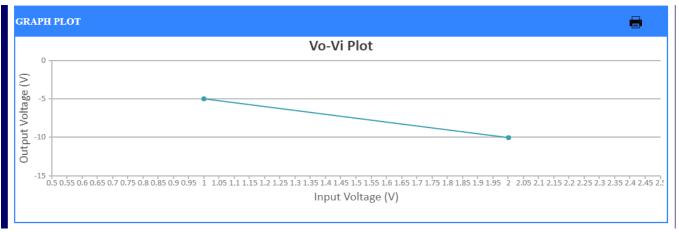
#### **Observations:**



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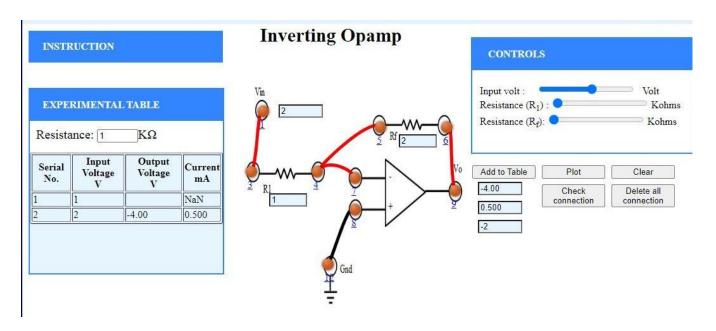




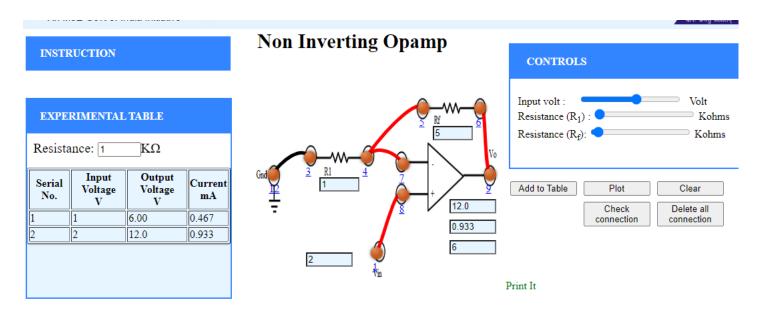


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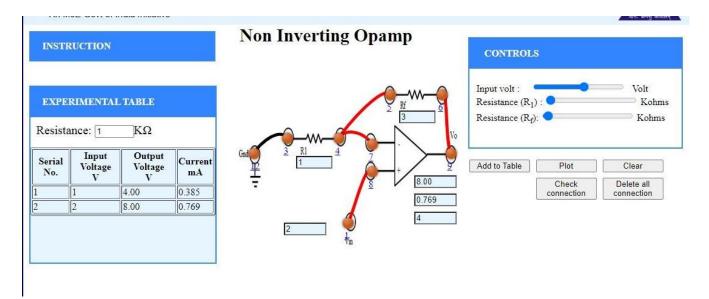
#### **NON - INVERTING:**

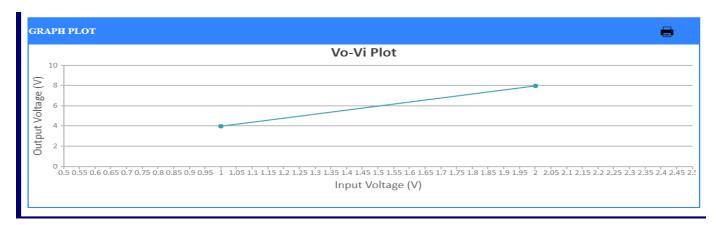




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#### **Conclusion:**

Thus, we learnt the working of OPAMP in inverting and non-inverting configuration.

Signature of faculty in-charge with Date: