Problem 8.1

a) IREF = IDI = ID2 => IREF =
$$\frac{k_N}{2} (V_1 - V_2 - V_{TN})^2 (1 + \lambda_N (V_1 - V_2))$$

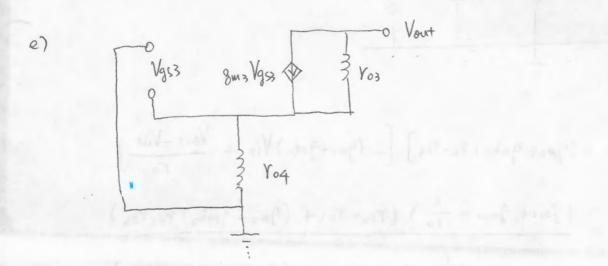
= $\frac{k_N}{2} (V_2 - V_{TN})^2 (1 + \lambda_N V_2)$

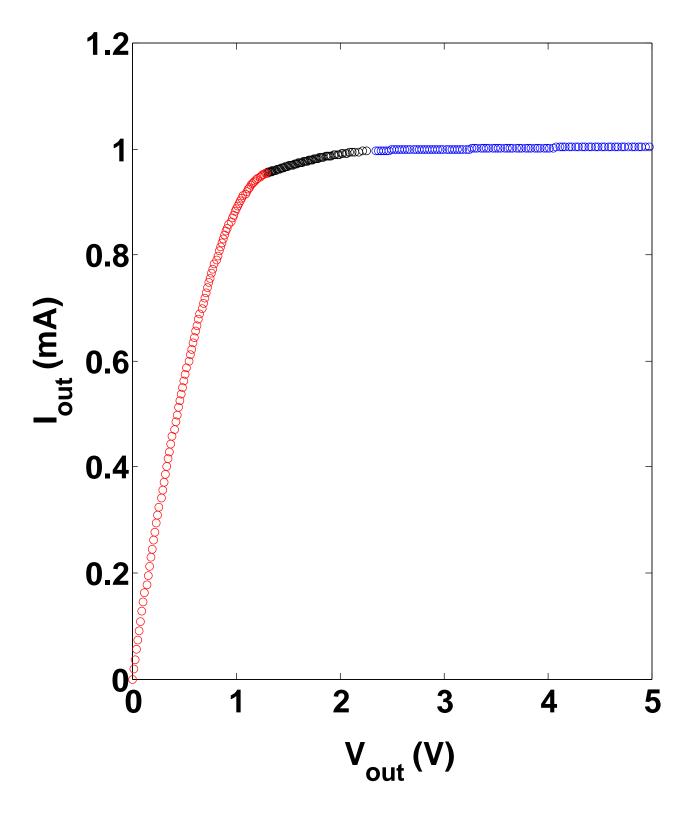
b)
$$I_{D3} = I_{D4} \Rightarrow \frac{kn}{2} (V_1 - V_3 - V_{TN})^2 (1 + \lambda_n (V_{OUT} - V_3))$$

$$= \frac{kn}{2} (V_2 - V_{TN})^2 (1 + \lambda_n V_3)$$

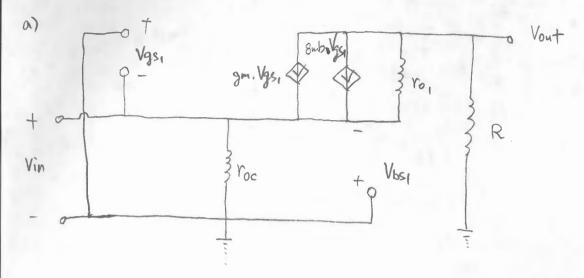
$$\Rightarrow V_{OUT} = \frac{1}{\lambda_n} \left[\frac{(V_2 - V_{TN})^2 (1 + \lambda_n V_3)}{(V_1 - V_3 - V_{TN})^2} - 1 \right] + V_3$$

c)
$$M_3: V_1 - V_{TN} < V_{OUT}$$
 $M_4: V_2 - V_{TN} < V_3$
 $\Rightarrow V_{OUT} > 2.3703V$

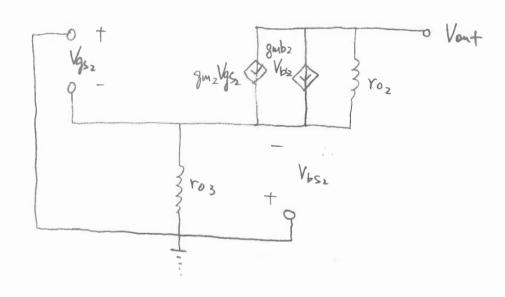




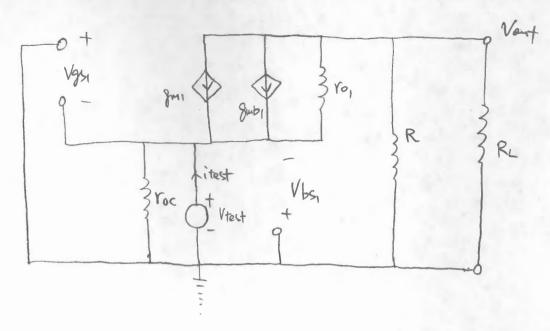
Problem 8.2

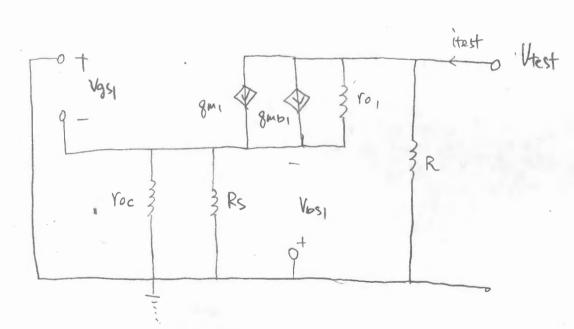


The circuit for R:



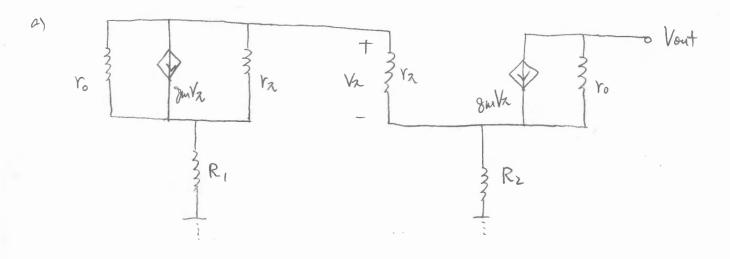
$$\Rightarrow Av = \frac{V_{out}}{V_{in}} = \frac{\left(9m_1 + 9mb_1 + \frac{1}{r_{o_1}}\right)R}{1 + \frac{R}{r_{o}}}$$





itest = id +
$$\frac{V_{\text{test}}}{R} = \frac{V_{\text{test}}}{R} + \frac{V_{\text{test}}}{V_{\text{oi}}} / \left[1 + \frac{V_{\text{oc}}//R_{\text{S}}}{V_{\text{oi}}} + (g_{\text{mi}} + g_{\text{mbi}})(V_{\text{oc}}//R_{\text{S}})\right]$$

Problem 8.4



$$V_{OC} = \frac{V_{test}}{i_{test}} = \frac{r_o}{R_2} + \frac{(1+g_m r_a)r_o}{R+r_a} + 1$$

$$\frac{1}{R_2} + \frac{1}{R+r_a}$$

a) The Small Signal model becomes (assuring the output resistance of the current source is so)

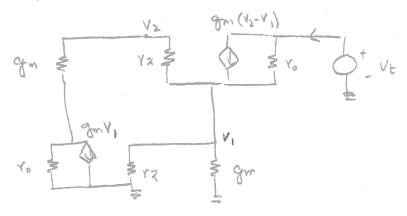
$$V_{OC} = \frac{V_{t}}{it}$$

$$V_{OM} = \frac{V_{t}}{it}$$

$$V_{t} = \frac{V_{t}}{it}$$

$$V_{t} = \frac{V_{t}}{it}$$

The BJTs By and B2 one diede connected and their circuits can be replaced by conductances ofm. to get.



This can be further simplified (Since gm >> 8 = 1/1):

ker at (1) gives:

kel at (2) gives:

$$\frac{V_{1}-V_{2}}{V_{\pi}} = g_{m}V_{1} + \frac{V_{2}}{V_{6}}$$

$$\Rightarrow V_{2} = -g_{m}(v_{\pi}||v_{6})V_{1}$$

$$\Rightarrow V_{2}-V_{1} = -[g_{m}(v_{\pi}||v_{6})+1]V_{1}$$

$$\frac{V_{t}}{r_{0}} = \frac{V_{1}}{r_{0}} - g_{uv} \left(v_{2} - V_{1} \right) + g_{uv} V_{1}$$

$$\Rightarrow V_{t} \approx V_{1} \left[1 + g_{uv} V_{0} + g_{uv} V_{0} \left[g_{uv} \left(v_{\pi} | | V_{0} \right) + 1 \right] \right]$$

b) $V_{OUT} > V_{BE-ON} + V_{CE-SAT}$