### 3F4: Data Transmission

#### Handout 10: Convolutional Codes

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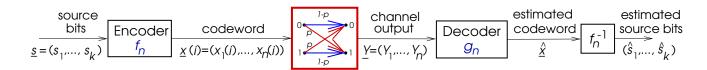
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#### Lent Term 2019

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# Block codes for the BSC(p)

- Codebook  $B_n$  consists of  $M = |B_n| = 2^k$ codewords  $\underline{x}(i) = (x_1(i), \dots, x_n(i)), i = 1, 2, \dots, M$
- Size of the code is  $M = 2^k = \text{size of codebook}$
- Encoder  $f_n: \{0,1\}^k \to B_n$  maps source strings to codewords
- Rate of the code is  $R_n = k/n$  bits/transmission
- Channel output string  $\underline{Y} = (Y_1, \dots, Y_n) \in A_Y^n$
- **Decoder**  $g_n : A_Y^n \to B_n$



 Idea: Since the BSC(p) will flip ≈ np of the bits of x add redundancy to x so that the channel errors can likely be corrected!

### Example: The redundancy of natural language

#### Plain text message

This is a very simple test sentence, used in class to illustrate the effects of random noise

#### In binary ASCII

#### Reconstructed after going through a BSC(p)

- p=0.05 This is a very 3i-pl% tes4 s%n4ence <usgt i| cmasS
  toAilluCdr!te uèm eFdeëtc Of vandom nois%</pre>

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### Good codes and capacity

• Error probability (maximal) of an (n, k) code code is

$$P_e^{(n)} = \max_{1 \leq i \leq M} \Pr(g_n(\underline{Y}) \neq \underline{x}(i) \,|\, \underline{x}(i) \,$$
 was sent)

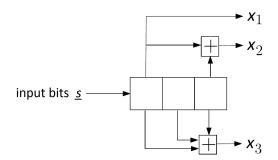
- Rate R = k/n is **achievable** if there are (n, k) codes with  $R_n \to R$  bits/trans, and  $P_e^{(n)} \to 0$ , as  $n \to \infty$
- Capacity C = the largest achievable rate
- For the BSC:  $C = 1 H_2(p)$  bits/trans where  $H_2(p) = -p \log_2 p - (1-p) \log_2 (1-p)$

#### Good codes have:

- High rate  $R = k/n \approx C$
- Low probability of error  $P_e^{(n)} \approx 0$
- Computationally efficient encoding and decoding
- A code is **linear** if, whenever  $\underline{s}$  gets encoded as  $\underline{x}$  and  $\underline{s}'$  gets encoded as  $\underline{x}'$ , the sum  $\underline{s} + \underline{s}'$  gets encoded as  $\underline{x} + \underline{x}'$

### Convolutional codes

In convolutional codes, a stream of input bits is transformed into a stream of code bits using a shift register (filter)

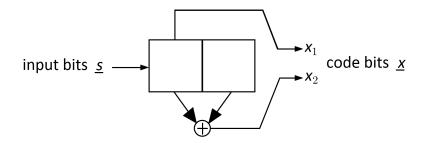


- Here, for every k = 1 data bit, we have n = 3 code bits
- Assuming the initial state of the shift register is  $(0\ 0\ 0)$  the code bits corresponding to the input  $\underline{s}=1010$  are  $(111\ 001\ 100\ 001)$
- Dependence between code bits is created via the shift register

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### Parameters of a convolutional code

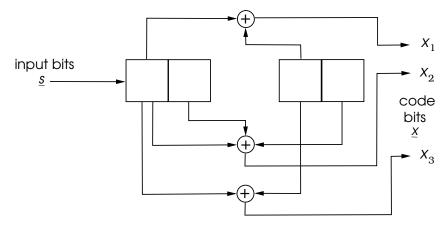
A rate  $\frac{1}{2}$  convolutional code with k=1 input bit and an S=2-stage shift register



In a general convolutional code, at each *time instant*: k input bits are fed into a shift register with S stages, and its contents are used to produce n code bits, for a rate of R = k/n

# Another example of a convolutional code

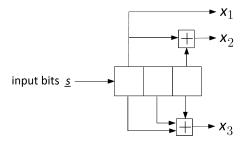
A rate  $\frac{2}{3}$  convolutional code with k=2 input bits and an S=2-stage shift register



- We often, but not always, consider k = 1:
   one bit shifted into the shift register at a time
- We always assume that the shift register is initially all zeros
- Although a convolutional code can be thought of as a block code, there is no fixed blocklength for the input sequence or the coded sequence

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### Generators of a convolutional code



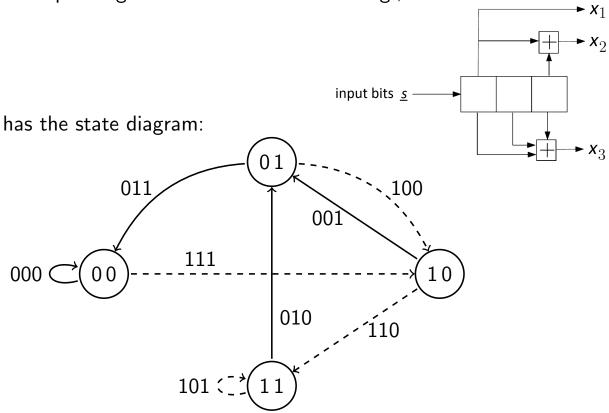
- If  $\underline{x}, \underline{x}'$  are the code sequences corresponding to source sequences  $\underline{s}, \underline{s}'$ , respectively, the code sequence for  $(\underline{s} + \underline{s}')$  is  $(\underline{x} + \underline{x}')$
- Thus convolutional codes are linear codes
- Similarly to a generator matrix, the generation of the code bits can be described by *n generators*, which indicate which shift register bits are added to generate each code bit
- In the example above, these generators are

$$g_1 = (100), \quad g_2 = (101), \quad g_3 = (111)$$

 Specifying the generators is equivalent to describing the convolutional code via a block diagram

### Finite-state machine description

Convolutional codes can also be represented via *state diagrams* corresponding to *finite-state machines*. E.g., the code:



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### The state diagram

Suppose we have a code with S shift register stages and assuming k=1 bit is fed into the register at each time:

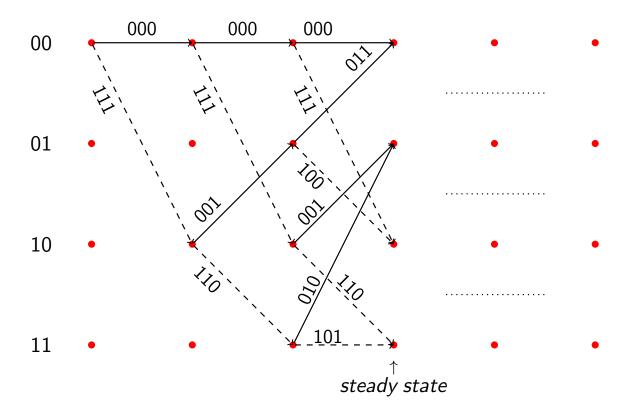
- The **states**, represented as the nodes of the state diagram, are the contents of the first S-1 stages of each shift register; so there are  $2^{S-1}$  states
- Two possible transitions out of each state, represented as directed edges, one for each possible input bit 0 or 1
- In the example above, solid lines correspond to input bit 0 and dashed lines to input 1
- Each edge has a label, which is the output corresponding to that transition

**Simple exercise**: Draw the state diagram for the code on slide 6. **Important exercise**: Draw the (four-state) state diagram for the code on slide 7. Label each branch with the input and output bits corresponding to the transition along that branch.

*N.B.* The most important and useful representation of a convolutional code is via a *trellis diagram* . . .

# The trellis representation

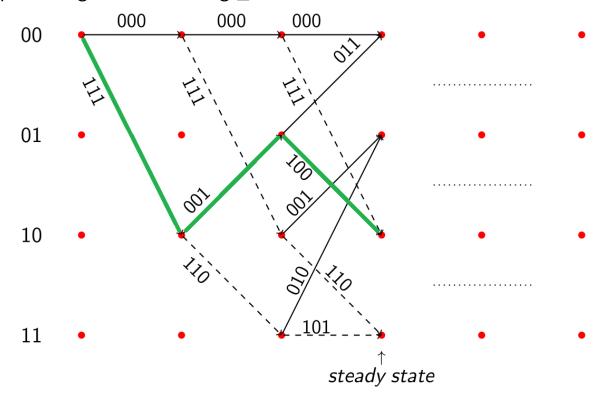
For the earlier example of an (n = 3, k = 1, S = 3) code:



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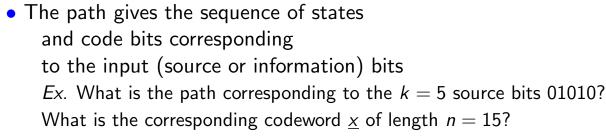
# The trellis representation

E.g.: The path traced by the input string  $\underline{s}=101$  producing the code string  $\underline{x}=111~001~100$ 



# The trellis representation

- Number of states in each step of trellis  $= 2^{S-1} =$  number of possibilities for first (S-1) stages of shift register
- Each sequence of input bits determines a path in the trellis



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01

10

11

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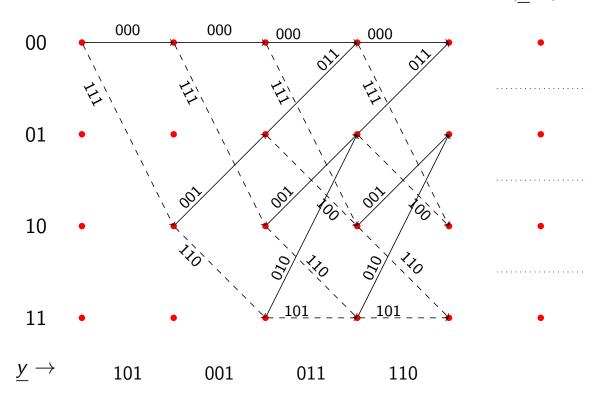
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- The finite-state machine and the trellis
   are equivalent representations of a convolutional code
   except that the former does not have the notion of time
- The trellis representation is very helpful in visualising the decoding of a convolutional code

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# Decoding convolutional codes

Recall: Optimal decoding is minimum distance decoding E.g.: If we receive  $\underline{y}=101~001~011~111$ , we need to find a path in the trellis which gives a code sequence  $\hat{\underline{x}}$  minimising  $d(y,\hat{\underline{x}})$ 



# Decoding convolutional codes

- For k input bits, so that y has length n = 3k the number of possible trellis paths is 2k which grows exponentially with k
- Finding the min-distance path for given  $\underline{y}$  via exhaustive search, is impossibly complex

There is a simple algorithm to find the minimum-distance path called the **Viterbi algorithm** 

It is an instance of a general idea called dynamic programming

To understand the idea, let us consider an example of finding the shortest path between two cities, say Glasgow and Athens . . .

Manchester

Birmingham Amsterdam

Glasgow

Liverpool

Bristol

Brussels

Athens

Athens

Stage 1 Stage 2 Stage 3

- Problem: Given the distances between all pairs of cities find the min-distance path Glasgow  $\rightarrow$  Athens, subject to:
- Your path should pass through exactly one city in each stage
- E.g.: Glasgow–Manchester–London–Amsterdam–Athens is a valid path

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If  $s_3$  is a city in Stage 3:

- A) min-dist(Gla Ath) =  $\min_{s_3} \{ \min\text{-dist}(Gla s_3) + d(s_3 \to Ath) \}$ Thus we now have to find the min-distance from Gla to each of the stage 3 cities. For each fixed  $s_3$ :
- B) min-dist(Gla  $-s_3$ ) = min<sub> $s_2$ </sub>{min-dist(Gla  $-s_2$ ) + d( $s_2 \rightarrow s_3$ )} Similarly, for each  $s_2$ :
- C) min-dist(Gla  $-s_2$ ) = min<sub>s1</sub>{min-dist(Gla  $-s_1$ ) + d( $s_1 \rightarrow s_2$ )} Finally, for each stage 1 city  $s_1$ , min-dist(Gla  $-s_1$ ) is known:
  - 1. Use this to solve (C) for each  $s_2$
  - 2. Then use the solution to solve (B) for each  $s_3$
  - 3. Then use solution of (B) to solve (A)

This algorithm, called **dynamic programming**, is much more efficient than an exhaustive search among all paths: Complexity only grows linearly with the number of stages

Next time we will apply it to trellis decoding . . .