

NAME	Type	OpCode	R1	R2	R3	Shamt	Funct	Imm	26-Bit	Description
sll	R3	0	-	RS	RD	SHAMT	0			ShiftLeftLogicalSHAMT
srl	R3	0	-	RS	RD	SHAMT	2			ShiftRightLogicalSHAMT
sra	R3	0	-	RS	RD	SHAMT	3			ShiftRightArithmeticSHAMT
slv	R	0	Shift	RS	RD	0	4			ShiftLeftLogicalVariable
slv	R	0	Shift	RS	RD	0	6			ShiftRightLogicalVariable
srav	R	0	Shift	RS	RD	0	7			ShiftRightArithmeticVariable
jr	R4	0	Addr	0	0	0	8			JumpRegister
jalr	R5	0	Addr	0	RD	0	9			JumpAndLinkRegister
mfhi	R1	0	0	0	RD	0	16			MoveFromHI
mthi	R4	0	RS	0	0	0	17			MoveToHI
mflo	R1	0	0	0	RD	0	18			MoveFromLO
mtlo	R4	0	RS	0	0	0	19			MoveToLO
mult	R6	0	R1	R2	0	0	24			Multiply
multu	R6	0	R1	R2	0	0	25			MultiplyUnsigned
div	R6	0	R1	R2	0	0	26			DivideR1/R2(HI-remainder,LO-quotient)
divu	R6	0	R1	R2	0	0	27			DivideR1/R2Unsigned
add	R	0	R1	R2	RD	0	32			Addition
addu	R	0	R1	R2	RD	0	33			AdditionUnsigned(withoutoverflow)
sub	R	0	R1	R2	RD	0	34			SubtractR1-R2
subu	R	0	R1	R2	RD	0	35			SubtractR1-R2Unsigned(withoutoverflow)
and	R	0	R1	R2	RD	0	36			AND
or	R	0	R1	R2	RD	0	37			OR
xor	R	0	R1	R2	RD	0	38			XOR
nor	R	0	R1	R2	RD	0	39			NOR
slt	R	0	R1	R2	RD	0	42			SetifLessThan(RD=1ifR1<R2)
sltu	R	0	R1	R2	RD	0	43			SetifLessThanUnsigned(RD=1ifR1<R2)
bgez	I2	1	RS	1				Offset/4		BranchifRSGreaterorEqualToZero
bgezal	I2	1	RS	17				Offset/4		BranchandLinkifRSGreaterorEqualToZero
bltz	I2	1	RS	0				Offset/4		BranchifRSLessThanZero
bltzal	I2	1	RS	16				Offset/4		BranchandLinkifRSLessThanZero
j	J	2							Target	Jump
jal	J	3							Target	JumpAndLink
beq	I	4	R1	R2				Offset/4		BranchifEqual(ifR1=R2)
bne	I	5	R1	R2				Offset/4		BranchifNotEqual(ifR1<>R2)
blez	I2	6	RS	0				Offset/4		BranchifRSLessorEqualToZero
bgtz	I2	7	RS	0				Offset/4		BranchifRSGreaterThanZero
addi	I	8	RS	RD				Imm		AdditionImmediate
addiu	I	9	RS	RD				Imm		AdditionImmediateUnsigned(withoutoverflow)
slti	I	10	RS	RD				Imm		SetifRSLessThanImmediate
sltiu	I	11	RS	RD				Imm		SetifRSLessThanImmediateUnsigned
andi	I	12	RS	RD				Imm		ANDImmediate
ori	I	13	RS	RD				Imm		ORImmediate
xori	I	14	RS	RD				Imm		XORImmediate
lui	I1	15	0	RD				Imm		LoadUpperImmediate(Immediatex2^16)
lb	I	32	Addr	RD				Offset		LoadByte
lh	I	33	Addr	RD				Offset		LoadHalfWord
lwl	I	34	Addr	RD				Offset		LoadWordLeft(4highbytesof8-byteword)
lw	I	35	Addr	RD				Offset		LoadWord
lbu	I	36	Addr	RD				Offset		LoadByteUnsigned
lhu	I	37	Addr	RD				Offset		LoadHalfWordUnsigned
lwr	I	38	Addr	RD				Offset		LoadWordRight(4lowbytesof8-byteword)
sb	I	40	Addr	RS				Offset		StoreByte
sh	I	41	Addr	RS				Offset		StoreHalfWord
swl	I	42	Addr	RS				Offset		StoreWordLeft(4highbytesof8-byteword)
sw	I	43	Addr	RS				Offset		StoreWord
swr	I	46	Addr	RS				Offset		StoreWordRight(4lowbytesof8-byteword)