

NAME	Type	OpCode	R1	R2	R3	Shamt	Funct	Imm	26-Bit	Description
remu		Pseudo								UnsignedRemainder
rol		Pseudo								RotateLeft
ror		Pseudo								RotateRight
sb	I	40	Addr	RS					Offset	StoreByte
sd		Pseudo								StoreDoubleWord
seq		Pseudo								SetifEqual
sge		Pseudo								SetifGreaterThanOrEqual
sgeu		Pseudo								SetifGreaterThanOrEqualUnsigned
sgt		Pseudo								SetifGreater_Than
sgtu		Pseudo								SetifGreater_ThanUnsigned
sh	I	41	Addr	RS					Offset	StoreHalfWord
sll	R3	0	-	RS	RD	SHAMT	0			ShiftLeftLogicalSHAMT
sllv	R	0	Shift	RS	RD	0	4			ShiftLeftLogicalVariable
slt	R	0	R1	R2	RD	0	42			SetifLessThan(RD=1ifR1<R2)
slti	I	10	RS	RD					Imm	SetifRSLessThanImmediate
sltiu	I	11	RS	RD					Imm	SetifRSLessThanImmediateUnsigned
sltu	R	0	R1	R2	RD	0	43			SetifLessThanUnsigned(RD=1ifR1<R2)
sne		Pseudo								SetifNotEqual
sra	R3	0	-	RS	RD	SHAMT	3			ShiftRightArithmeticSHAMT
srav	R	0	Shift	RS	RD	0	7			ShiftRightArithmeticVariable
srl	R3	0	-	RS	RD	SHAMT	2			ShiftRightLogicalSHAMT
srlv	R	0	Shift	RS	RD	0	6			ShiftRightLogicalVariable
sub	R	0	R1	R2	RD	0	34			SubtractR1-R2
subu	R	0	R1	R2	RD	0	35			SubtractR1-R2(withoutoverflow)
sw	I	43	Addr	RS					Offset	StoreWord
swl	I	42	Addr	RS					Offset	StoreWordLeft(4highbytesof8-byteword)
swr	I	46	Addr	RS					Offset	StoreWordRight(4lowbytesof8-byteword)
ulh		Pseudo								UnalignedLoadHalfWord
ulhu		Pseudo								UnalignedLoadHalfWordUnsigned
ulw		Pseudo								UnalignedLoadWord
ush		Pseudo								UnalignedStoreHalfWord
usw		Pseudo								UnalignedStoreWord
xor	R	0	R1	R2	RD	0	38			XOR
xori	I	14	RS	RD					Imm	XORImmediate