Computer Science M51A — **Logic Design of Digital Systems**
Winter 2003
Prof. Miloš D. Ercegovac
TA: Ninoslav Stojčić
HOMEWORK ASSIGNMENTS

**Homework No. 5:**

Chapter 7: 7.4, 7.6, 7.10, 7.12, 7.16, 7.24

Due Friday, 21/2 in recitation

General Information on Homeworks:

- Homeworks must be turned in at the beginning of the recitation. Homework solutions are discussed in the recitation when the homeworks are due. NO LATE HOMEWORKS ACCEPTED.