## Computer Science M51A — Logic Design of Digital Systems

Winter 2003 Prof. Miloš D. Ercegovac TA: Ninoslav Stojčić HOMEWORK ASSIGNMENTS

## Homework No. 4:

Chapter 5: 5.2, 5.6, 5.8, 5.14, 5.18

Chapter 6: 6.8, 6.10

Due Friday, 07/2 in recitation

## General Information on Homeworks:

• Homeworks must be turned in at the beginning of the recitation. Homework solutions are discussed in the recitation when the homeworks are due. NO LATE HOMEWORKS ACCEPTED.