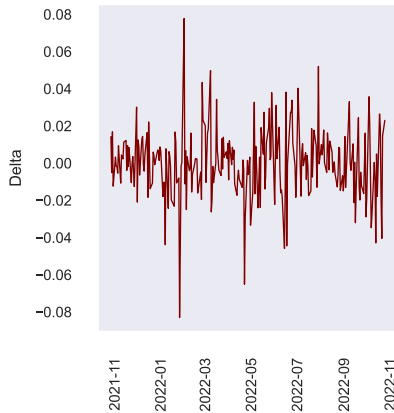
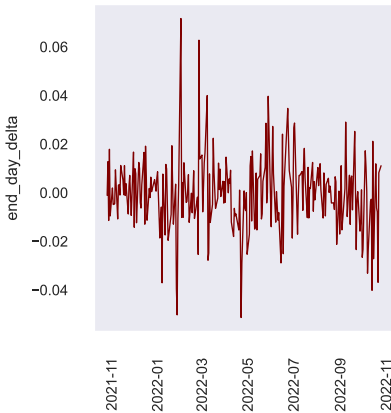
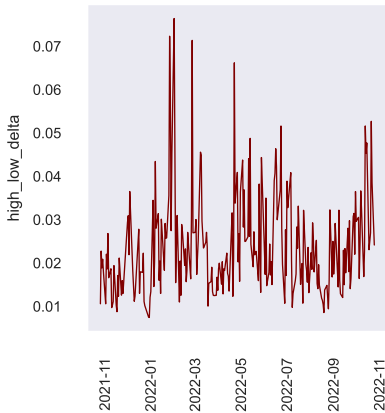
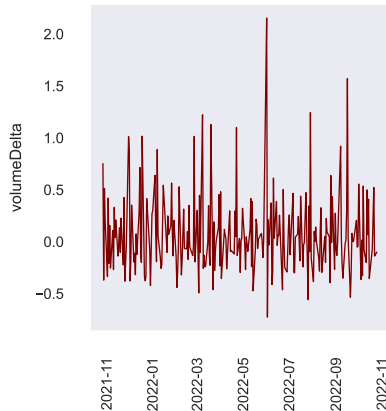
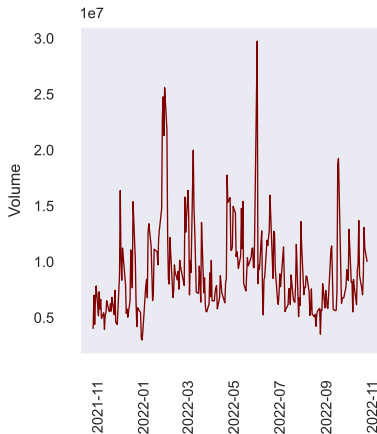
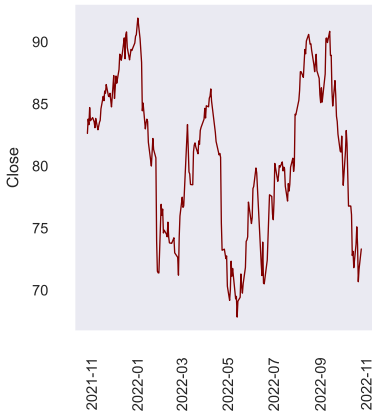
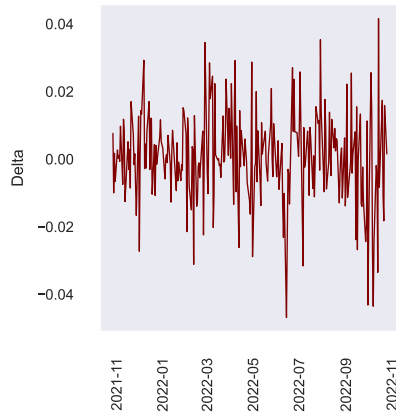
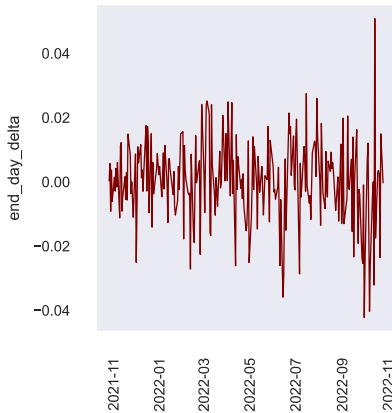
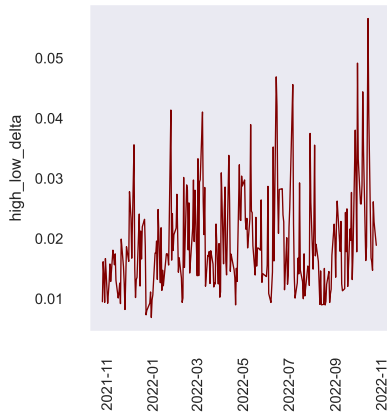
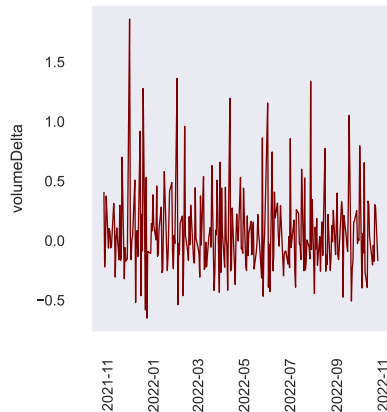
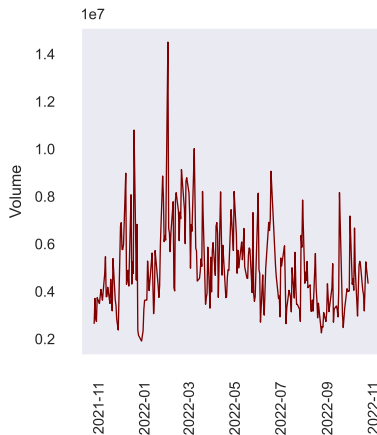
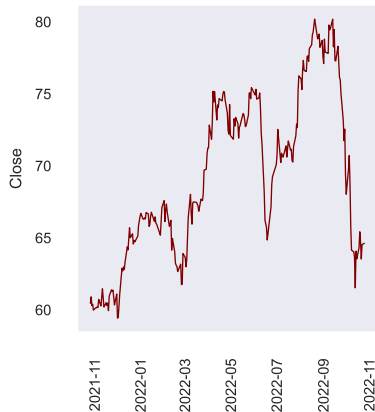


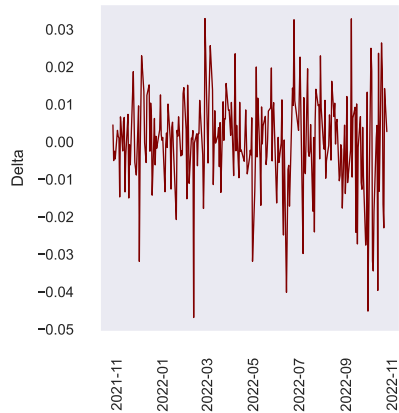
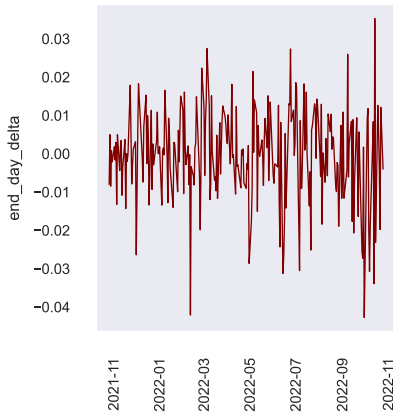
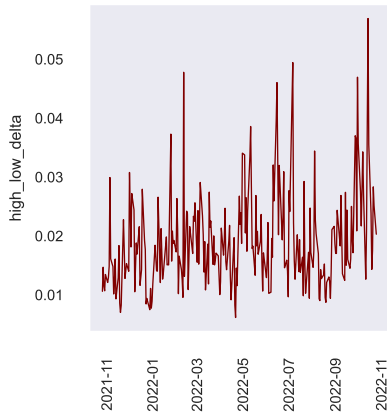
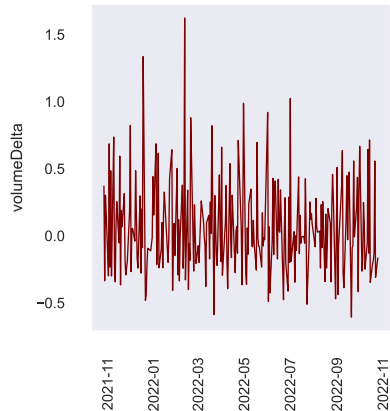
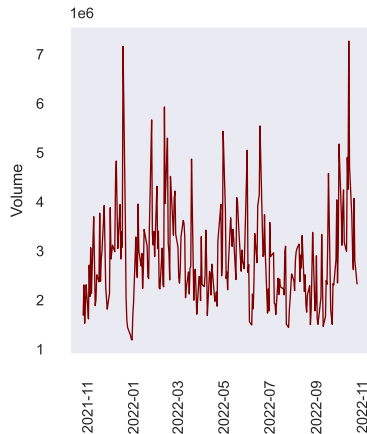
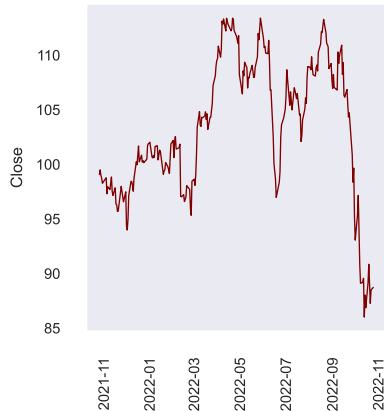
# NEE



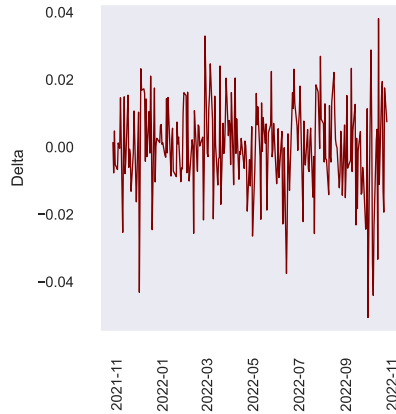
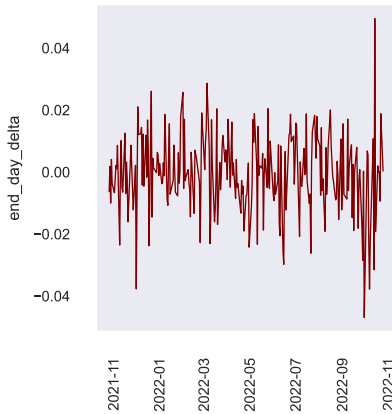
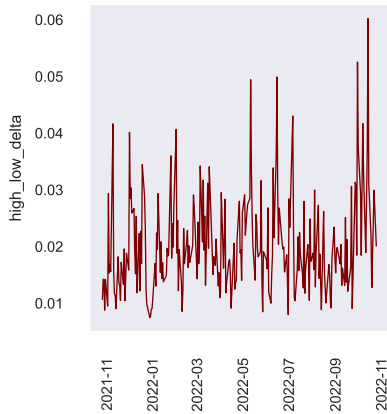
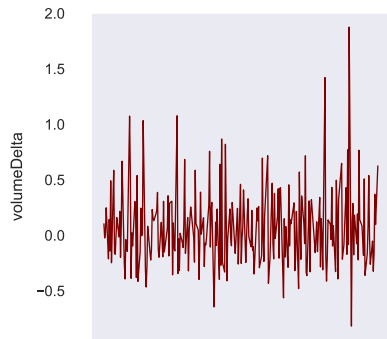
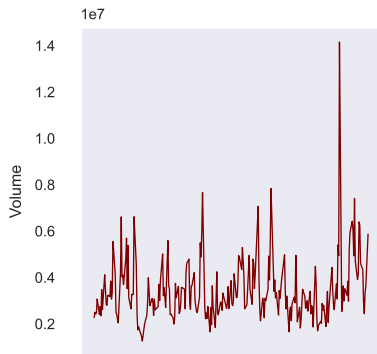
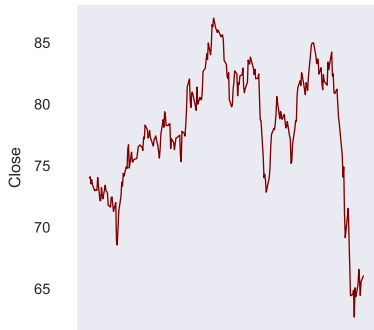
SO



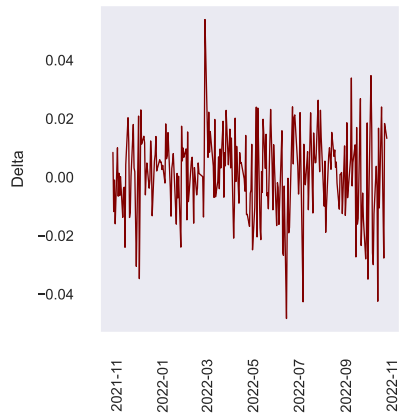
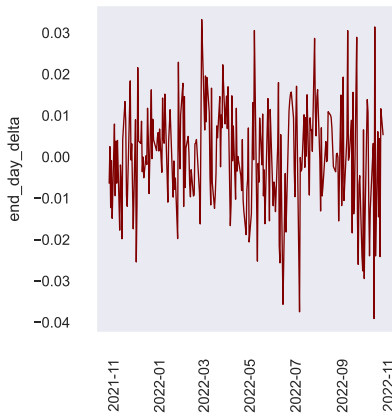
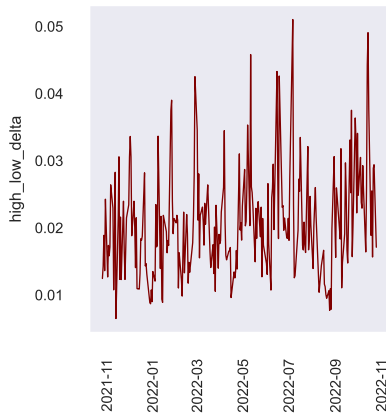
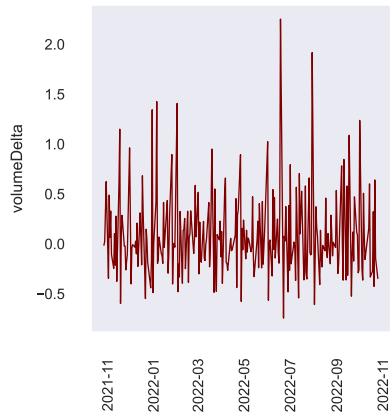
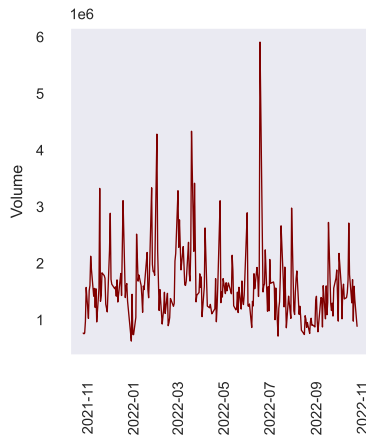
DUK



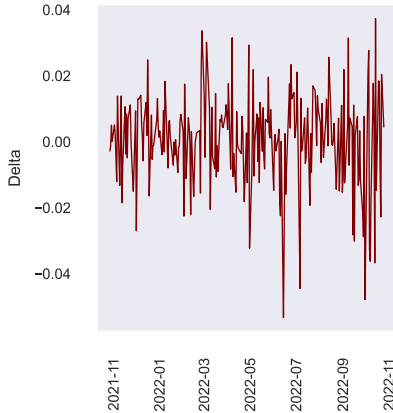
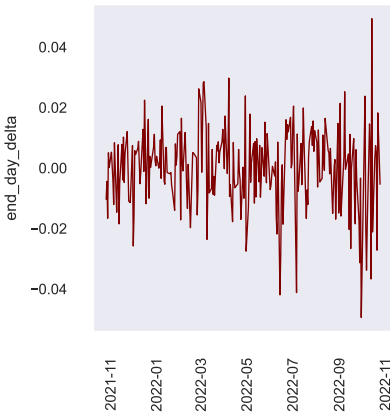
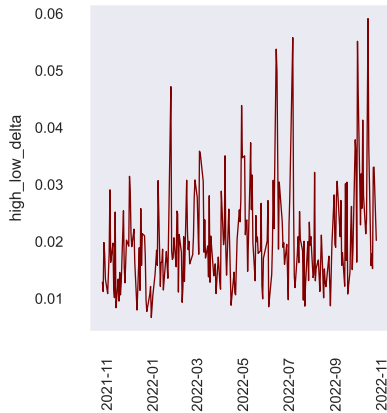
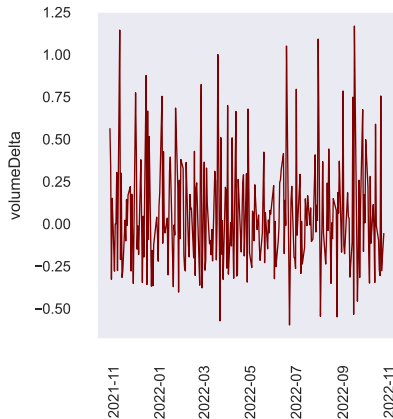
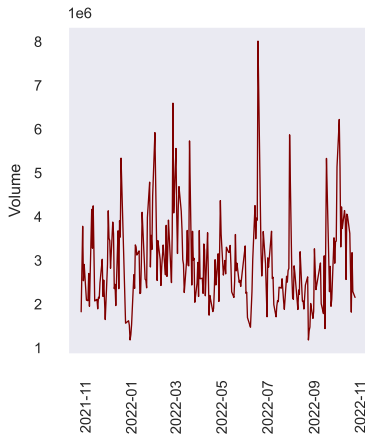
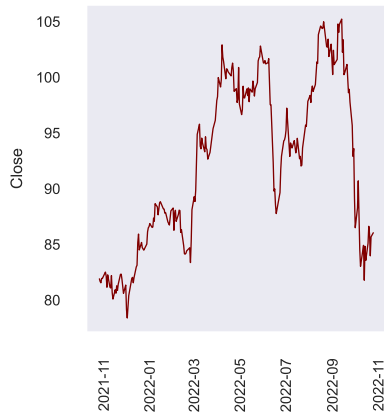
D



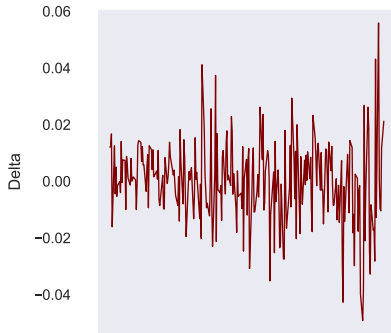
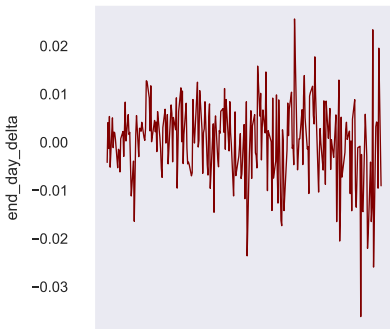
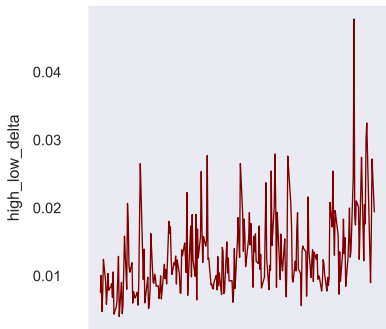
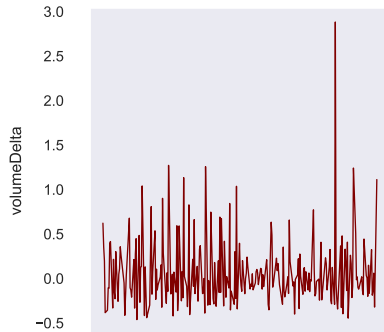
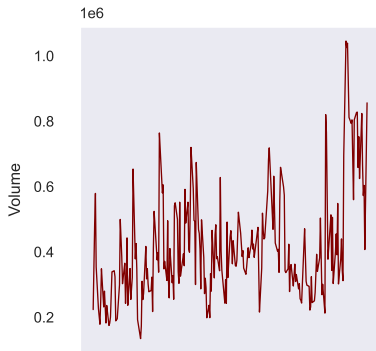
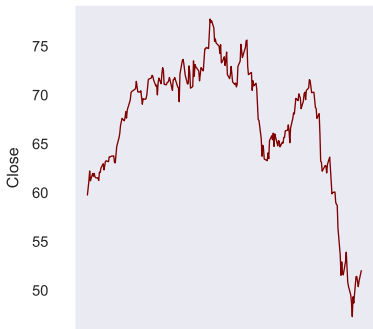
# SRE



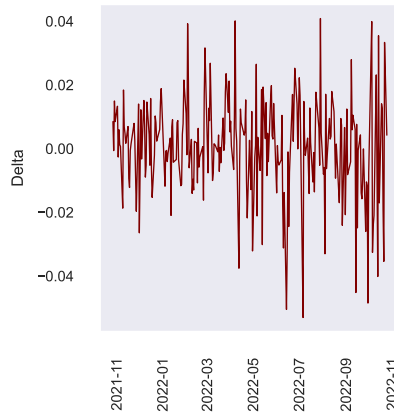
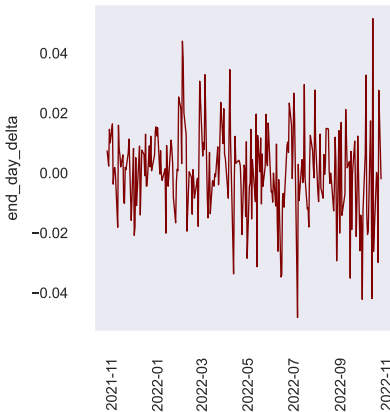
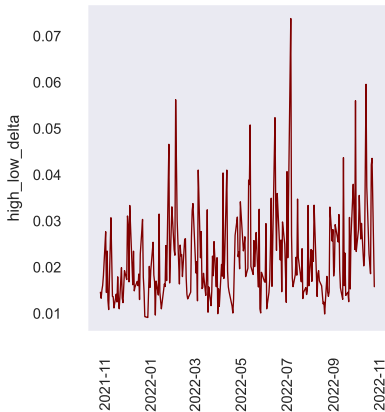
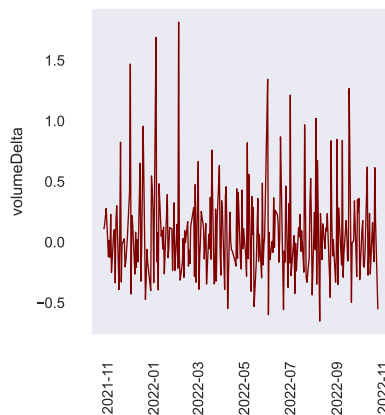
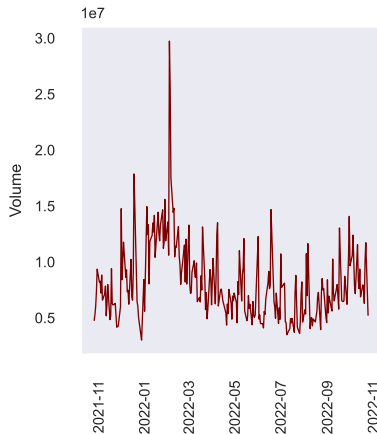
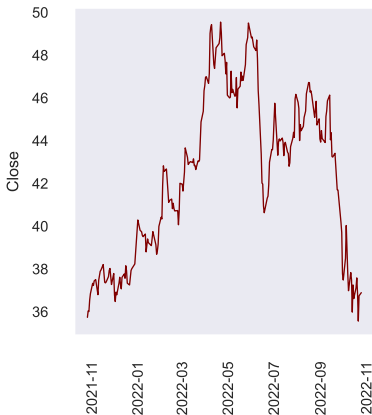
# AEP



# NGG

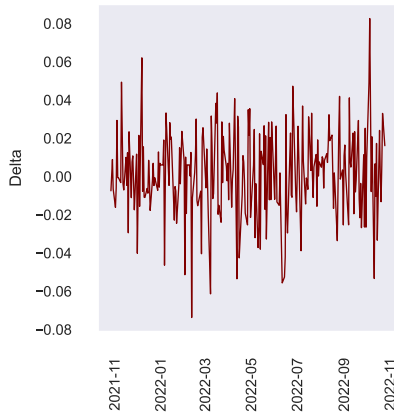
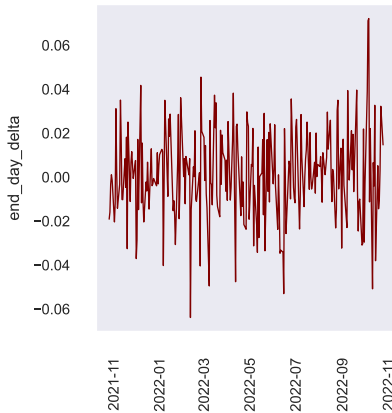
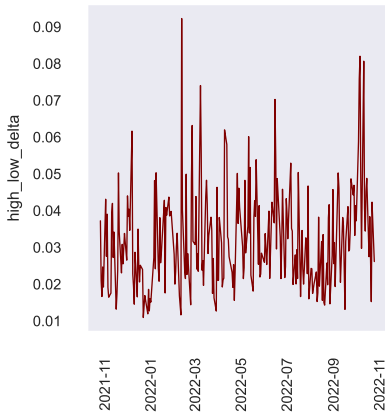
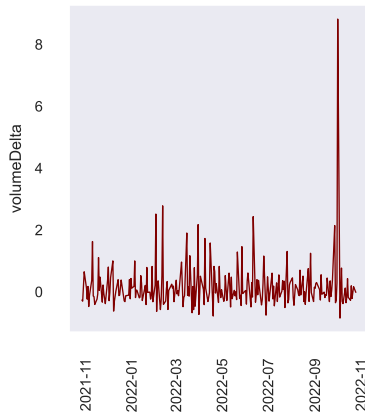
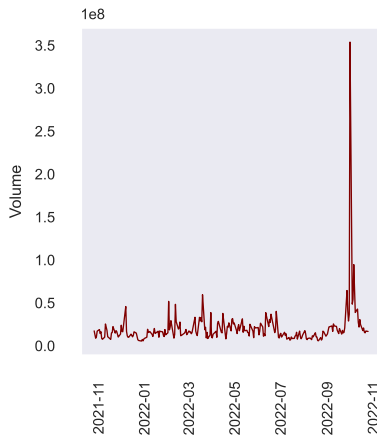
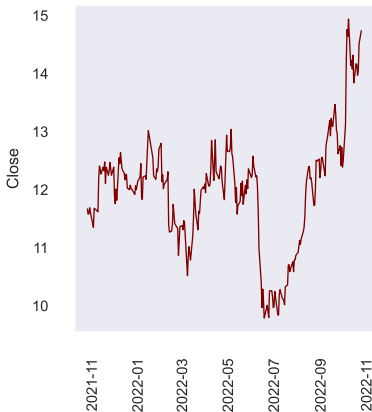


# EXC





# PCG



# XEL

